# **GA-7PESH4**

Dual LGA2011 sockets motherboard for Intel® E5-2600 series processors

User's Manual

Rev. 1003

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# **Documentation Classifications**

In order to assist in the use of this product, GIGABYTE provides the following types of documentations:

For detailed product information, carefully read the User's Manual.

For product-related information, check on our website at: http://www.gigabyte.com

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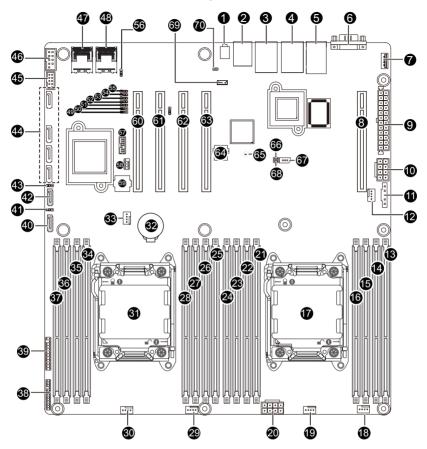
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# **Box Contents**

- $\checkmark$  Motherboard
- ☑ Driver CD
- ☑ Two SATA cables
- ☑ I/O Shield
- ☑ CPU Power cables

- The box contents above are for reference only and the actual items shall depend on the product package you obtain.
  The box contents are subject to change without notice.
- The motherboard image is for reference only.

# GA-7PESH4 Motherboard Layout



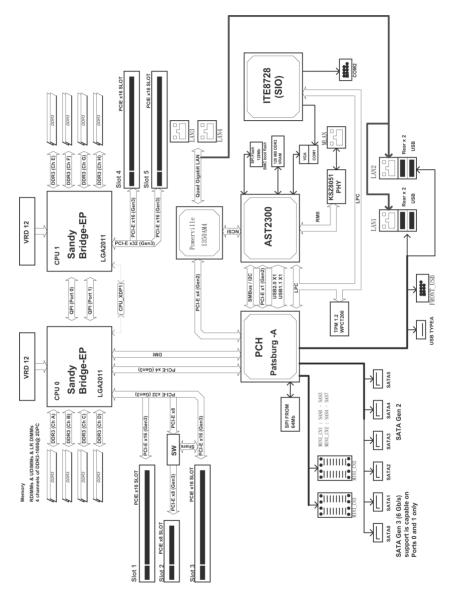
ltem	Code	Description
1	SW1	ID switch
2	MLAN1	BMC Management LAN port
3	USB_LAN1	LAN1 port (top) / USB ports (bottom)
4	USB_LAN2	LAN2 port (top) / USB ports (bottom)
5	LAN_3_4	LAN ports
6	COM1_VGA1	Serial port (top) / VGA port (bottom)
7	USB_A1	Type A USB connector
8	PCIE_5	PCI-E slot 5 (x16 slot)
9	ATX1	24 pin power connector
10	P12V_AUX2	8 pin power connector
11	PWR_DET1	PMBus connector
12	CPU1_FAN	CPU1 fan cable connector
13	DDR3_P1_E0	Channel 1 slot 0 (for secondary CPU)
14	DDR3_P1_E1	Channel 1 slot 1 (for secondary CPU)
15	DDR3_P1_F0	Channel 2 slot 0 (for secondary CPU)
16	DDR3_P1_F1	Channel 2 slot 1 (for secondary CPU)
17	CPU1	Intel LGA2011 socket (Secondary CPU)
18	SYS_FAN4	Systen fan connector #4
19	SYS_FAN3	Systen fan connector #3
20	P12V_AUX1	8 pin power connector
21	DDR3_P1_H1	Channel 4 slot 1 (for secondary CPU)
22	DDR3_P1_H0	Channel 4 slot 0 (for secondary CPU)
23	DDR3_P1_G1	Channel 3 slot 1 (for secondary CPU)
24	DDR3_P1_G0	Channel 3 slot 0 (for secondary CPU)
25	DDR3_P0_A0	Channel 1 slot 0 (for primary CPU)
26	DDR3_P0_A1	Channel 1 slot 1 (for primary CPU)
27	DDR3_P0_B0	Channel 2 slot 0 (for primary CPU)
28	DDR3_P0_B1	Channel 2 slot 1 (for primary CPU)
29	SYS_FAN2	Systen fan connector #2
30	SYS_FAN1	Systen fan connector #1
31	CPU0	Intel LGA2011 socket (Primary CPU)
32	BAT1	Battery socket
33	CPU0_FAN	CPU0 fan cable connector
34	DDR3_P0_D1	Channel 4 slot 1 (for primary CPU)
35	DDR3_P0_D0	Channel 4 slot 0 (for primary CPU)
36	DDR3_P0_C1	Channel 3 slot 1 (for primary CPU)
37	DDR3_P0_C0	Channel 3 slot 0 (for primary CPU)
38	BP_1	HDD back plane board header
39	FP_1	Front panel header
40	SATA1	SATA 6Gb/s connector
41	SATA_DOM1	SATA1 port DOM support jumper
42	SATA2	SATA 6Gb/s connector
43	SATA_DOM2	SATA2 port DOM support jumper
44	SATA3/SATA4/SATA5/SATA6	SATA 3Gb/s connectors

45	FRONT_USB	USB 2.0 connectors
46	COM2	Serial port cable connector
47	MINI_CN2	Mini SAS cable connector
48	MINI_CN1	Mini SAS cable connector
49	S3_MASK	S3 Power On Select jumper
50	FLASH_DP1	Flash descriptor security jumper
51	CLR_CMOS1	Clear CMOS jumper
52	SSB_ME1	ME recovery jumper
53	PASSWORD1	Skip supervisor paswword jumper
54	BIOS_RVCR1	BIOS recovery jumper
55	BMC_FRB1	Force to Stop FRB3 Timer jumper
56	BIOS_WP1	BIOS write protect jumper
57	TPM_1	TPM module connector
58	SATA_SGPIO1	SATA SGPIO header
59	SSB_BIOS1	BIOS ROM
60	PCIE_1	PCI-E slot 1 (x16 slot)
61	PCIE2	PCI-E slot 2 (x16 slot/Running at x8; shared
01	FOIEZ	bandwidth with PCI-E slot 3)
<u></u>		PCI-E slot 3 (x16 slot/Running at x8; shared
62	PCIE_3	bandwidth with PCI-E slot 2)
63	PCIE_4	PCI-E slot 4 (x16 slot)
64	U56	BMC uopgrade ROM
65	BMC_LED1	BMC firmware readiness LED
66	J2	LAN3 link/active LED
67	IPMB1	IPMB connector
68	J3	LAN4 link/active LED
69	SKU_KEY1	Intel C600 series upgrade key
70	JP1	Chassis intrusion header



**CAUTION!** If a SATA type hard drive is connected to the motherboard, please ensure the jumper is closed and set to **2-3 pins** (Normal mode), in order to reduce any risk of hard disk damage. Please refer to Page 35 for SATA\_DOM1 and SATA\_DOM2 jumper setting instruction.

# **Block Diagram**



# Chapter 1 Hardware Installation

# 1-1 Installation Precautions

The motherboard contains numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the user's manual and follow these procedures:

- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic components such as a motherboard, CPU or memory. If you do not have an ESD wrist strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- Do not place the computer system on an uneven surface.
- Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.

# 1-2 Product Specifications

CPU	<ul> <li>Support for Intel® Xeon® E5-2600 series processors in the LGA2011 package</li> <li>L3 cache varies with CPU</li> <li>Supports QuickPath Interconnect up to 8GT/s</li> </ul>
	<ul> <li>Enhanced Intel SpeedStep Technology (EIST)</li> </ul>
	Support Intel Virtualization Technology (VT)
Chipset	<ul> <li>Intel<sup>®</sup> C602 (Patsburg-A) Chipset</li> </ul>
Memory	16 x 1.5V DDR3 DIMM sockets supporting up to 128GB (UDIMM) and
	512GB (RDIMM) of system memory
	16 x 1.35V DDR3L DIMM sockets supporting up to 128 GB of system memory
	Four channel memory architecture
	<ul> <li>Support for 800/1066/1333/1600 memory modules</li> </ul>
	Support for ECC RDIMM/ UDIMM memory modules
	<ul> <li>Intel® I350 supports 10/100/1000 Mbps</li> </ul>
Expansion Slots	• 3 x PCI Express x16 slot, running at x16 (Gen3/PCIE_1/PCIE_4/PCIE_5)
	<ul> <li>1 x PCI Express x16 slot, running at x16 (Gen3/PCIE_3/)</li> </ul>
	• 1 x PCI Express x16 slot, running at x8 (Gen3/PCIE2/The slot shares bandwidth
	with the PCIE_3 slot. When the this slot is populated, the PCIE_3 slot will
	operate at up to x8 mode.)
Onboard Graphics	ASPEED® AST2300 supports 16MB VRAM
Storage Interface	Intel® C602 controller
	<ul> <li>2 x SATA 6Gb/s connectors (SATA1/SATA2)</li> </ul>
	• 4 x SATA 3Gb/s connectors (SATA3/SATA4/SATA5/SATA6)
	<ul> <li>1 x Mini-SAS connector (for 4 x SATA 3Gb/s ports)</li> </ul>
	• 1 x Mini-SAS connector (disabled, can be enabled via Intel® upgrade key)
	Support for Intel RSTe SATA RAID 0, RAID 1
USB	• Up to 6 USB 2.0/1.1 ports (4 on the back panel, 2 additional ports via the USB
	brackets connected to the internal USB headers)
	1 x Type A USB connector

# Intel C600 Upgrade ROM SKUs:

Upgrade ROM SKU#	SCU Ports	Protocol Enabled	Intel RSTe SAS RAID 5	
Patsburg-A; no upgrade ROM	4 ports	SATA Only	SATA RAID 5	
1	4 ports	SATA/SAS	No	
2	4 ports	SATA/SAS	Yes	
5	8 ports	SATA/SAS	No	
6	8 ports	SATA/SAS	Yes	
9	8 ports	SATA Only	SATA RAID 5	

Internal	1 x 24-pin ATX main power connector
Connectors	2 x 8-pin ATX 12V power connector
	<ul> <li>2 x Mini SAS connectors (SAS 3Gb/s signal)</li> </ul>
	2 x SATA 6Gb/s connectors
	4 x SATA 3Gb/s connectors
	1 x PMBus header
	2 x CPU fan headers
	4 x System fan headers
	1 x Front panel header
	1 x Back plane borad header
	• 1 x USB 2.0/1.1 header
	1 x USB 2.0/1.1 Type A connector
	1 x TPM module connector
	1 x Serial port connector
	1 x SKU KEY header
	1 x SPGIO header
Rear Panel I/O	<ul> <li>4 x USB 2.0/1.1 ports</li> </ul>
	<ul> <li>5 x RJ-45 port (1 x 10/100 dedicated management LAN port)</li> </ul>
	1 x COM port
	1 x VGA port
	1 x ID Switch button
I/O Controller	ASPEED® AST2300 BMC chip
Hardware	System voltage detection
Monitor	CPU/System temperature detection
	CPU/System fan speed detection
	CPU/System fan speed control
	<ul> <li>Whether the CPU/system fan speed control function is supported will depend on the CPU/system cooler you install.</li> </ul>
BIOS	1 x 64 Mbit flash
	AMI BIOS
Form Factor	EATX Form Factor; 12 inch x 12.9 inch, 10 layers PCB

\* GIGABYTE reserves the right to make any changes to the product specifications and product-related information without prior notice.

# 1-3 Installing the CPU and CPU Cooler

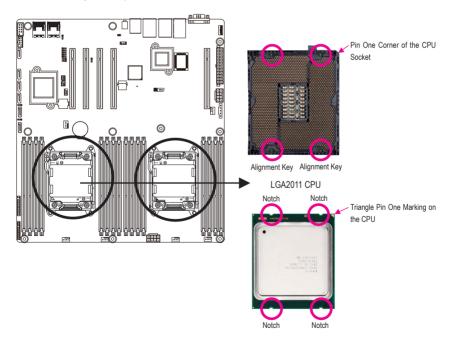


Read the following guidelines before you begin to install the CPU:

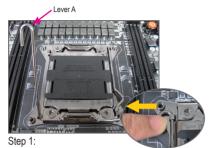
- Make sure that the motherboard supports the CPU.
- Always turn off the computer and unplug the power cord from the power outlet before installing the CPU to prevent hardware damage.
- Locate the pin one of the CPU. The CPU cannot be inserted if oriented incorrectly. (Or you may locate the notches on both sides of the CPU and alignment keys on the CPU socket.)
- Apply an even and thin layer of thermal grease on the surface of the CPU.
- Do not turn on the computer if the CPU cooler is not installed, otherwise overheating and damage of the CPU may occur.
- Set the CPU host frequency in accordance with the CPU specifications. It is not recommended
  that the system bus frequency be set beyond hardware specifications since it does not meet the
  standard requirements for the peripherals. If you wish to set the frequency beyond the standard
  specifications, please do so according to your hardware specifications including the CPU,
  graphics card, memory, hard drive, etc.

# 1-3-1 Installing the CPU

A. Locate the alignment keys on the motherboard CPU socket and the notches on the CPU.



- B. Follow the steps below to correctly install the CPU into the motherboard CPU socket.
  - Before installing the CPU, make sure to turn off the computer and unplug the power cord
    from the groups at let to groups to the CPU
  - from the power outlet to prevent damage to the CPU.
  - To protect the socket contacts, do not remove the protective plastic cover unless the CPU is inserted into the CPU socket. Save the cover properly and replace it if the CPU is removed.



Push the lever closest to the "unlock" marking "
(below referred as lever A) down and away from the socket to release it.



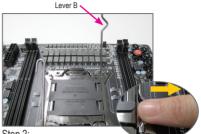
#### Step 3:

Gently press lever A to allow the load plate to rise. Open the load plate. (Note: DO NOT touch the socket contacts after the load plate is opened.)



#### Step 5:

Once the CPU is properly inserted, carefully replace the load plate. Then secure lever B under its retention tab. The protective plastic cover may pop off from the load plate during the process of engaging the lever. Remove the cover. Save the cover properly and always replace it when the CPU is not installed.



Step 2:

Push the lever closest to the "lock" marking "合" (below referred as lever B) down and away from the socket. Then lift the lever.





Hold the CPU with your thumb and index fingers. Align the CPU pin one marking (triangle) with the pin one corner of the CPU socket (or align the CPU notches with the socket alignment keys) and carefully insert the CPU into the socket vertically.





Finally, secure lever A under its retention tab to complete the installation of the CPU.

# 1-3-2 Installing the CPU Cooler

Refer to the steps below to correctly install the CPU cooler on the motherboard. (Actual installation process may differ depending the CPU cooler to be used. Refer to the user's manual for your CPU cooler.)



#### Step 1:

Apply an even and thin layer of thermal grease on the surface of the installed CPU.



#### Step 3:

Use one hand to hold the cooler and the other to tighten the screws in a diagonal sequence with a screw driver. Begin tightening a screw with a few turns and repeat with the screw diagonally opposite the one you just tightened. Then do the same to the other pair. Next, fully tighten the four screws.



Place the cooler atop the CPU, aligning the four mounting screws with the mounting holes on the ILM. (If your cooler has a fan grill which may cause interference when you tighten the screws, remove it first and replace it after tightening the screws.)



Step 4:

Finally, attach the power connector of the CPU cooler to the CPU fan header (CPU\_FAN) on the motherboard.



Please pay more attention when removing the CPU cooler because the thermal grease/tape between the CPU cooler and CPU may adhere to the CPU. Inadequately removing the CPU cooler may damage the CPU.

# 1-4 Installing the Memory



Read the following guidelines before you begin to install the memory:

 Make sure that the motherboard supports the memory. It is recommended that memory of the same capacity, brand, speed, and chips be used.

(Go to GIGABYTE's website for the latest supported memory speeds and memory modules.)

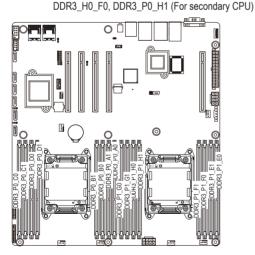
- Always turn off the computer and unplug the power cord from the power outlet before installing the memory to prevent hardware damage.
- Memory modules have a foolproof design. A memory module can be installed in only one direction. If you are unable to insert the memory, switch the direction.

# 1-4-1 Four Channel Memory Configuration

This motherboard provides eight DDR3 memory sockets and supports Four Channel Technology. After the memory is installed, the BIOS will automatically detect the specifications and capacity of the memory. Enabling Four Channel memory mode will be four times of the original memory bandwidth.

The four DDR3 memory sockets are divided into four channels each channel has two memory sockets as following:

Channel 1: DDR3\_P0\_A0, DDR3\_P0\_A1 (For pimary CPU) DDR3\_P0\_E0, DDR3\_P0\_E1 (For secondary CPU) Channel 2: DDR3\_P0\_B0, DDR3\_P0\_B1(For pimary CPU) DDR3\_P0\_F0, DDR3\_P0\_F1 (For secondary CPU) Channel 3: DDR3\_P0\_C0, DDR3\_P0\_C1(For pimary CPU) DDR3\_P0\_G0, DDR3\_P0\_G1 (For secondary CPU) Channel 4: DDR3\_P0\_D0, DDR3\_P0\_D1(For pimary CPU)



Due to CPU limitations, read the following guidelines before installing the memory in Four Channel mode.

- 1. Four Channel mode cannot be enabled if only one DDR3 memory module is installed.
- 2. When enabling Four Channel mode with two or four memory modules, it is recommended that memory of the same capacity, brand, speed, and chips be used for optimum performance.

# 1-4-2 Installing a Memory

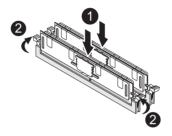


Before installing a memory module, make sure to turn off the computer and unplug the power ord from the power outlet to prevent damage to the memory module.

#### Be sure to install DDR3 DIMMs on this motherboard.

#### Installation Step:

- Step 1. Insert the DIMM memory module vertically into the DIMM slot, and push it down.
- Step 2. Close the plastic clip at both edges of the DIMM slots to lock the DIMM module.
- Note: For dual-channel operation, DIMMs must be installed in matched pairs.
- Step 3. Reverse the installation steps when you wish to remove the DIMM module.

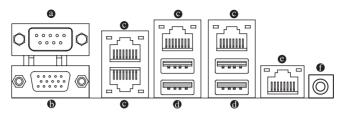


# 1-4-3 DIMM Population Table

	1N or 2N	DIMM1	DIMM0	
	1N	Empty	Single-Rank	
	1N	Empty	Dual-Rank	
	1N	Empty	Quad-Rank	
R-DIMM	1N	Single-Rank	Single-Rank	
	1N	Single-Rank	Dual-Rank	
	1N 1N	Dual-Rank	Dual-Rank	
		Single-Rank	Quad-Rank	
	1N	Dual-Rank	Quad-Rank	
	1N	Quad-Rank	Quad-Rank	

	1N or 2N	DIMM1	DIMM0
	1N	Empty	Single-Rank
	1N	Empty	Dual-Rank
U-DIMM	2N	Single-Rank	Single-Rank
	2N	Single-Rank	Dual-Rank
	2N	Dual-Rank	Dual-Rank

#### 1-5 **Back Panel Connectors**



#### Ø Serial Port

Connects to serial-based mouse or data processing devices.

#### Video Port

The video in port allows connect to video in, which can also apply to video loop thru function.

#### RJ-45 LAN Ports (Gigabit Ethernet LAN Ports)

The Gigabit Ethernet LAN port provides Internet connection at up to 1 Gbps data rate. The following describes the states of the LAN port LEDs.

#### USB 2.0/1.1 Port

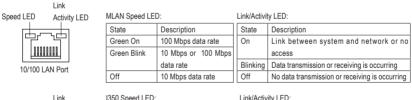
The USB port supports the USB 2.0/1.1 specification. Use this port for USB devices such as a USB keyboard/mouse, USB printer, USB flash drive and etc.

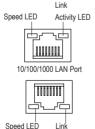
#### KVM Server Management 10/100 LAN Port

The LAN port provides Internet connection with data transfer speeds of 10/100Mbps.

#### ID Switch Button

This button provide the selected unit idenfication function.



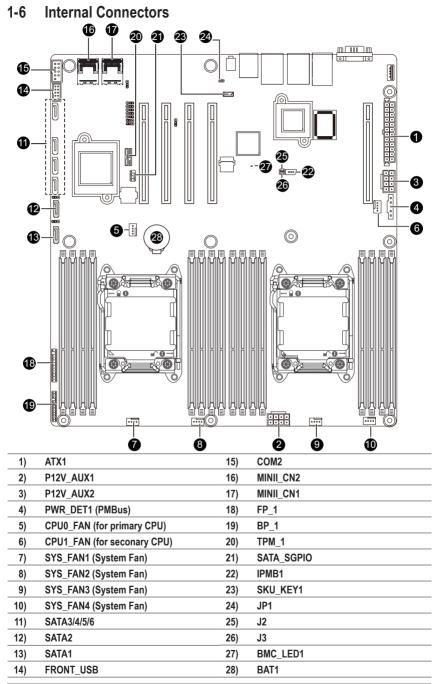


1350 Speed LED	:	Link/Activity LED:	
State	Description	State	Description
Yellow On	1 Gbps data rate	On	Link between system and network or no
Yellow Blink	Identify 1 Gbps data		access
	rate	Blinking	Data transmission or receiving is occurring
Green On	100 Mbps data rate	Off	No data transmission or receiving is occurring
Green Blink	Identify 100 Mbps data		
	rate		
Off	10 Mbps data rate		

Activity LED



- When removing the cable connected to a back panel connector, first remove the cable from your device and then remove it from the motherboard.
- When removing the cable, pull it straight out from the connector. Do not rock it side to side to prevent an electrical short inside the cable connector.





Read the following guidelines before connecting external devices:

- First make sure your devices are compliant with the connectors you wish to connect.
- Before installing the devices, be sure to turn off the devices and your computer. Unplug the
  power cord from the power outlet to prevent damage to the devices.
- After installing the device and before turning on the computer, make sure the device cable has been securely attached to the connector on the motherboard.

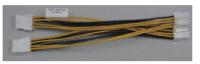
# 1/2/3) ATX1/P12V\_AUX1/P12V\_AUX2

# (2x4 12V Power Connector and 2x12 Main Power Connector)

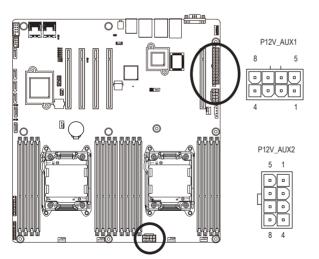
With the use of the power connector, the power supply can supply enough stable power to all the components on the motherboard. Before connecting the power connector, first make sure the power supply is turned off and all devices are properly installed. The power connector possesses a foolproof design. Connect the power supply cable to the power connector in the correct orientation. The 12V power connector mainly supplies power to the CPU. If the 12V power connector is not connected, the computer will not start.



If you are using the power supply that split up into several sets of +12V, please attach the power cable (see below picture) to P12V\_AUX1 and P12V\_AUX2 first, then connect the +12V power connector of the power supply into cable box-header.



- If you are using the power supply that provide single +12V, just install power supply connectors to P12V\_AUX1 and P12V\_AUX2 directly.
- To meet expansion requirements, it is recommended that a power supply that can withstand high power consumption be used (500W or greater). If a power supply is used that does not provide the required power, the result can lead to an unstable or unbootable system.



Pin No.	Definition
1	GND
2	GND
3	GND
4	GND
5	+12V
6	+12V
7	+12V
8	+12V

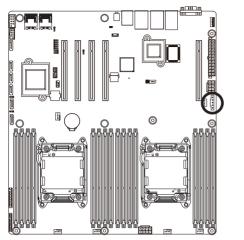


#### ATX1

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Pin No.	Definition	Pin No.	Definition
1	3.3V	13	3.3V
2	3.3V	14	-12V
3	GND	15	GND
4	+5V	16	PS_ON
5	GND	17	GND
6	+5V	18	GND
7	GND	19	GND
8	Power Good	20	-5V
9	5VSB (stand by +5V)	21	+5V
10	+12V	22	+5V
11	+12V	23	+5V
12	3.3V	24	GND

## 4) PWR\_DET1 (PMBus connector)

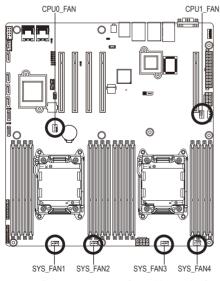


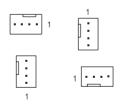
Pin No.	Definition
1	SMB CLK
2	SMB DATA
3	SMB Alert
4	GND
5	3.3V Sense

# 5/6/7/8/9/10) CPU0\_FAN/CPU1\_FAN/SYS\_FAN1/SYS\_FAN2/SYS\_FAN3/SYS\_FAN4 (CPU Fan/System Fan Headers)

The motherboard has two 4-pin CPU fan headers, four 4-pin system fan headers. Most fan headers possess a foolproof insertion design. When connecting a fan cable, be sure to connect it in the correct orientation (the black connector wire is the ground wire). The motherboard supports CPU fan speed control, which requires the use of a CPU fan with fan speed control design. For optimum heat dissipation, it is recommended that a system fan be installed inside the chassis.

1





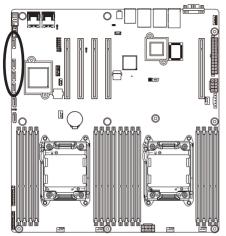
Pin No.	Definition	
1	GND	
2	+12V	
3	Sense	
4	Speed Control	

- Be sure to connect fan cables to the fan headers to prevent your CPU and system from overheating. Overheating may result in damage to the CPU or the system may hang.
- These fan headers are not configuration jumper blocks. Do not place a jumper cap on the headers.

# 11) SATA3/SATA4/SATA5/SATA6 (SATA 3Gb/s Connectors)

The SATA connectors conform to SATA 3Gb/s standard and are compatible with SATA 1.5 Gb/s standard. Each SATA connector supports a single SATA device.

7

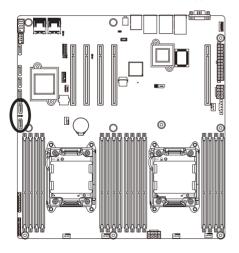


Pin No.	Definition
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

#### 12/13) SATA0/SATA1 (SATA 6Gb/s Connectors)

The SATA connectors conform to SATA 6Gb/s standard and are compatible with SATA 3Gb/s and 1.5Gb/s standard. Each SATA connector supports a single SATA device.

7



When	SATA_	DOM1/2	jumper
are set	to Nor	mal Mode	:

Pin No.	Definition	
1	GND	
2	TXP	
3	TXN	
4	GND	
5	RXN	
6	RXP	
7	GND	

When SATA\_DOM1/2 Jumper

are set to 1-2 pin:		
Pin No.	Definition	
1	GND	
2	TXP	
3	TXN	
4	GND	
5	RXN	
6	RXP	
7	P5V	

A RAID 0 or RAID 1 configuration requires at least two hard drives. If more than two hard drives are configured, the total number of hard drives must be an even number.
A RAID 10 configuration requires four hard drives.

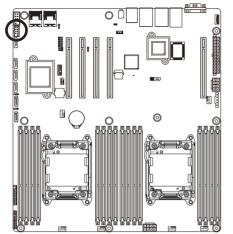
(Note) When a RAID configuration is built across the SATA 6Gb/s channels, the system performance of the RAID configuration may vary depends on the devices are connected.

# 14) FRONT\_USB (Front USB Headers)

The headers conform to USB 2.0/1.1 specification. Each USB header can provide two USB ports via an optional USB bracket. For purchasing the optional USB bracket, please contact the local dealer.

1 2

9 10

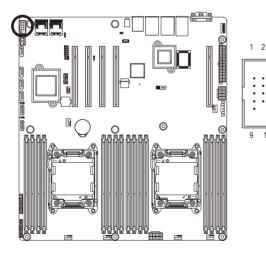


Pin No.	Definition
1	Power (5V)
2	Power (5V)
3	USB DX-
4	USB DY-
5	USB DX+
6	USB DY+
7	GND
8	GND
9	No Pin
10	NC

# 15) COM2 (Serial Port Headers)

The COM header can provide one serial port via an optional COM port cable. For purchasing the optional COM port cable, please contact the local dealer.

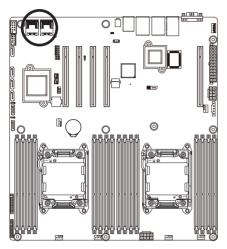
10

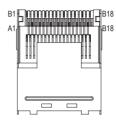


Pin No.	Definition
1	NDCD-
2	NDSR-
3	NSIN
4	NRTS-
5	NSOUT
6	NCTS-
7	NDTR-
8	NRI-
9	GND
10	No Pin

## 16/17) MINI\_CN2/MINI\_CN1 (Mini SAS cable connectors)

The Mini SAS connectors conform to SAS3Gb/s standard. Each Mini SAS connector supports four SASdevice. Please note that the default configuration is disabled, can be enabled via Intel® upgrade key.

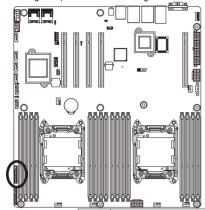




Pin No.	Definition	Pin No.	Definition
A1	GND	B1	GND
A2	RX0+	B2	TX0+
A3	RX0-	B3	TX0-
A4	GND	B4	GND
A5	RX1+	B5	TX1+
A6	RX1-	B6	TX1-
A7	GND	B7	GND
A8	SIB7	B8	SIB0
A9	SIB3	B9	SIB1
A10	SIB4	B10	SIB2
A11	SIB5	B11	SIB6
A12	GND	B12	GND
A13	RX2+	B13	TX2+
A14	RX2-	B14	TX2-
A15	GND	B15	GND
A16	RX3+	B16	TX3+
A17	RX3-	B17	TX3-
A18	GND	A18	GND

## 18) FP\_1 (Front Panel Headers)

Connect the power switch, reset switch, speaker, chassis intrusion switch/sensor and system status indicator on the chassis to this header according to the pin assignments below. Note the positive and negative pins before connecting the cables.



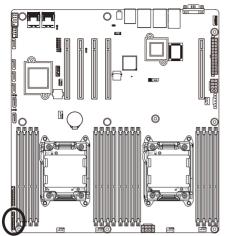
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Signal Name	Definition			
PWR_LED+	Power LED Anode			
5VSB	Front Panel Power			
KEY	Кеу			
ID_LED+	System ID LED Anode			
PWR_LED-	Power LED Cathode			
ID_LED-	System ID LED Cathode			
HDD_LED+	HDD Activity LED Anode			
SYS_STATUS+	System Fault LED Anode			
HDD_LED-	HDD Activity LED Cathode			
SYS_STATUS-	System Fault LED Cathode			
PWR_BTN	Power Switch			
LAN1_LED+	NIC#1 Activity LED Anode			
PWR_BTN (GND)	Power Switch (GND)			
LAN1_LED-	NIC#1 Activity LED Cathode			
RST_BTN	Reset Switch			
SDA	SMBus SDA			
RST_BTN (GND)	Reset Switch (GND)			
SCL	SMBus SCL			
ID_BTN	System ID Switch			
CASE_OPEN	Chassis Intrusion			
ID_BTN (GND)	System ID Switch (GND)			
LAN2_LED+	NIC#2 Activity LED Anode			
NMI_BTN	NMI to CPU Switch			
LAN2_LED-	NIC#2 Activity LED Cathode			
	Signal Name           PWR_LED+           5VSB           KEY           ID_LED+           PWR_LED-           ID_LED+           HDD_LED+           SYS_STATUS+           HDD_LED-           SYS_STATUS-           PWR_BTN           LAN1_LED+           RST_BTN (GND)           SDA           RST_BTN (GND)           SCL           ID_BTN           CASE_OPEN           ID_BTN (GND)           LAN2_LED+			

The front panel design may differ by chassis. A front panel module mainly consists of power switch, reset switch, power LED, hard drive activity LED, speaker and etc. When connecting your chassis front panel module to this header, make sure the wire assignments and the pin assignments are matched correctly.

- 27 -

19) BP\_1 (HDD Back Plane Board Hearders)



Pin No.	Definition	
1	AST2300_SCGCLK	
2	FM_THROTTLE_AND_N	
3	AST2300_SGLD	
4	IQO_FAN_12v_GATE_N	
5	AST2300_SGDOUT	
6	GND	
7	KEY	
8	RresetL_BRB	
9	GND	
10	BP_ALED_N	
11	BP_LED_G_N	
12	GND	
13	AST2300_SGDIN	
14	ASSESS#_LED_BPB	
15	GND	
16	SMB_BPB1_DATA	
17	GND	
18	SMB_BPB1_CLK	
19	P_3V3_AUX	
20	BP_HDD_TYPE	
21	P_3V3_AUX	
22	FAN_TYPE	
23	GND	
24	KEY	
25	BP_PRESENSE	
26	GND	

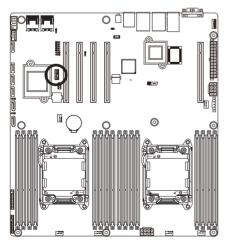
1 2

25 26

1 2

1314

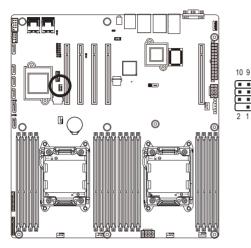
20) TPM\_1 (TPM Module Connector)



Pin No.	Definition		
1	CLK_33M_TPM		
2	P_3V3_AUX		
3	LPC_RST_DEBUG		
4	P3V3		
5	LPC_LAD0		
6	IRQ_SERIAL		
7	LPC_LAD1		
8	TPM_DET_N		
9	LPC_LAD2		
10	NC		
11	LPC_LAD3		
12	GND		
13	LPC_FRAME_N		
14	GND		

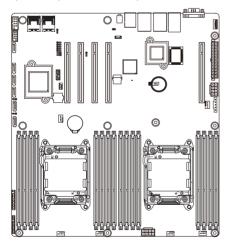
# 21) SATA\_SGPIO1 (SATA SGPIO Headers)

SGPIO is stands for Serial General Purpose Input/Output which is a 4-signal (or 4-wire) bus used between a Host Bus Adapter (HBA) and a backplane. Out of the 4 signals, 3 are driven by the HBA and 1 is driven by the backplane. Typically, the HBA is a storage controller located inside a server, desktop, rack or workstation computer that interfaces with Hard disk drives (HDDs) to store and retrieve data.



Pin No. Definition	
1	SGPIO_SATA_DATAIN
2	No Pin
3	SGPIO_SATA_DATAOUT
4	GND
5	GND
6	SGPIO_SATA_LOAD
7	NC
8	SGPIO_SATA_CLOCK

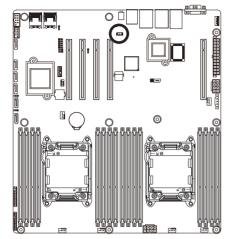
22) IPMB1 (IPMB Connector)



3		1
5	<u></u>	

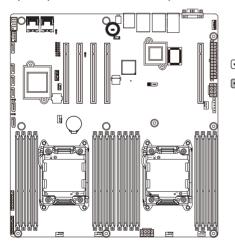
Pin No.	Definition	
1	SCL	
2	GND	
3	SDA	

23) SKU\_KEY1 (Intel C600 Series Upgrade Key)



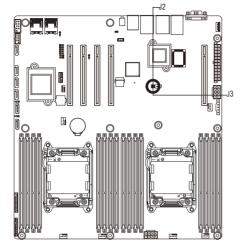
Pin No.	Definition	
1	GND	
2	FM_PBG_DYN_SKU_KEY	
3	GND	

# 24) JP1 (Chassis intrusion Header)



- Open: Normal operation (Default setting)
- Closed: Enable chassis intrusion alter.

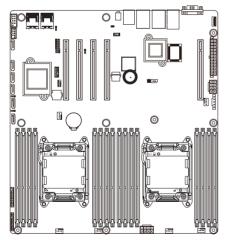
25/26) J2/J3 (LAN3/LAN4 link/active LED Headers)



2 💽 1

Pin No.	Definition	
1	Active	
2	Link	

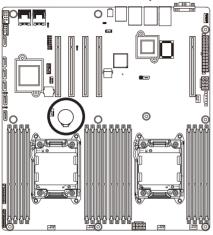
27) BMC\_LED1 (BMC Firmware Readiness LED)



State	Description	
On	BMC firmware is initial	
Blinking	BMC firmware is ready	
Off	System is powered off	

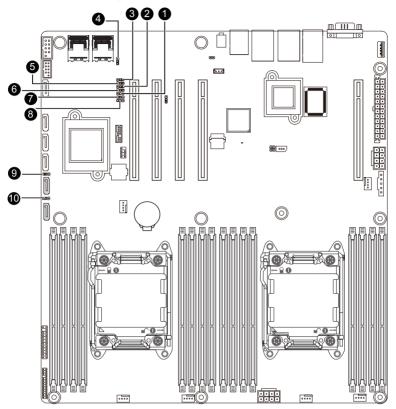
## 28) BAT1 (Battery)

The battery provides power to keep the values (such as BIOS configurations, date, and time information) in the CMOS when the computer is turned off. Replace the battery when the battery voltage drops to a low level, or the CMOS values may not be accurate or may be lost.





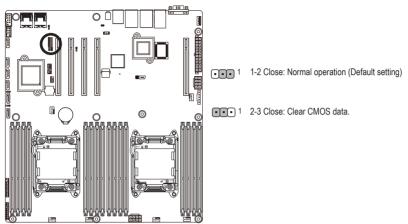
- Always turn off your computer and unplug the power cord before replacing the battery.
- Replace the battery with an equivalent one. Danger of explosion if the battery is replaced with an incorrect model.
- Contact the place of purchase or local dealer if you are not able to replace the battery by yourself or uncertain about the battery model.
- When installing the battery, note the orientation of the positive side (+) and the negative side (-) of the battery (the positive side should face up).
- · Used batteries must be handled in accordance with local environmental regulations.



1)	CLR_CMOS1	6)	SSB_ME1
2)	PASSWORD1	7)	FLASH_DP1
3)	BMC_FRB1	8)	S3_MASK
4)	BIOS_WP1	9)	SATA_DOM1
5)	BIOS_RVCR1	10)	SATA_DOM0

# 1) CLR\_CMOS1 (Clearing CMOS Jumper)

Use this jumper to clear the CMOS values (e.g. date information and BIOS configurations) and reset the CMOS values to factory defaults. To clear the CMOS values, place a jumper cap on the two pins to temporarily short the two pins or use a metal object like a screwdriver to touch the two pins for a few seconds.

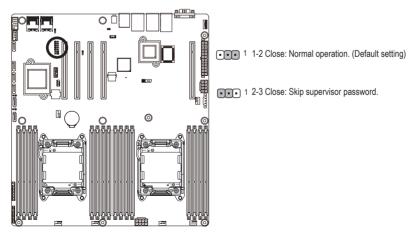


Always turn off your computer and unplug the power cord from the power outlet before clearing the CMOS values.

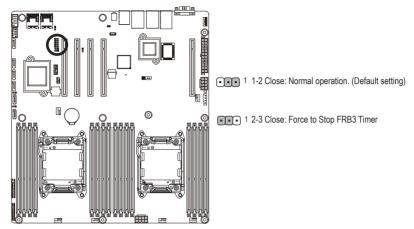
After clearing the CMOS values and before turning on your computer, be sure to remove the jumper cap from the jumper. Failure to do so may cause damage to the motherboard.

 After system restart, go to BIOS Setup Exit menu and load factory defaults (select Load Default Values) or manually configure the BIOS settings (refer to Chapter 2, "BIOS Setup," for BIOS configurations).

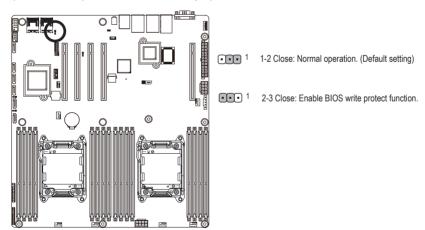
# 2) PASSWORD1 (Skip Supervisor Password Jumper)



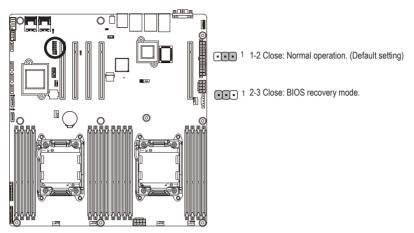
3) BMC\_FRB3 (Force to Stop FRB3 Timer Jumper)



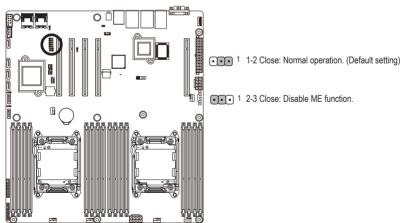
4) BIOS\_WP1 (BIOS Write Protect Jumper)



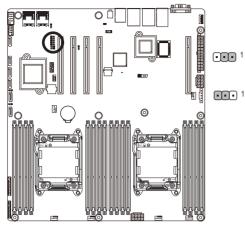
# 5) BIOS\_RVCR1 (BIOS Recovery Jumper)



6) SSB\_ME1 (ME enable/disable Jumper)



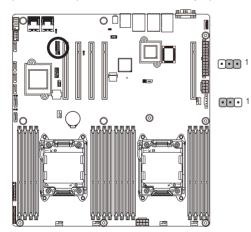
# 7) FLASH\_DP1 (Flash Descriptor Security Jumper)



1 1-2 Close: Flash Descriptor Security Overridden.

1 2-3 Close: Flash Descriptor Security in effect. (Default setting)

# 8) S3\_MASK (S3 Power On Select Jumper)



- I 1-2 Close: Stop an initial power on when BMC is not ready.
- 1 2-3 Close: Keep initial power on. (Default setting)

# 9/10) SATA\_DOM2/SATA\_DOM1 (SATA DOM Jumpers)

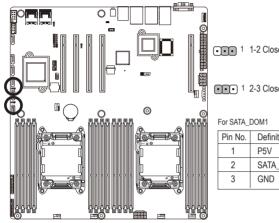


### CAUTION!

If the SATA DOM power is supplied by the motherboard, set the jumper to pin 1-2.

- If the SATA DOM power is supplied by external power, set the jumper to pin 2-3.
- If a SATA type hard drive is connected to the motherboard, please ensure the jumper is closed and set to **2-3 pins** (Default setting), in order to reduce any risk of hard disk damage.

Please refer to the pin definition table in the following.



••• 1 1-2 Close: Enable SATA0/SATA1 port DOM support.

••• 1 2-3 Close: Normal mode. (Default setting)

For SATA\_DOM2

).	Definition	Pin No.	Definition
	P5V	1	P5V
	SATA_DOM_1	2	SATA_DOM_2
	GND	3	GND

# Chapter 2 BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters and loading operating system, etc. BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the <F2> key during the POST when the power is turned on.



- BIOS flashing is potentially risky, if you do not encounter problems of using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.
- It is recommended that you not alter the default settings (unless you need to) to prevent system
  instability or other unexpected results. Inadequately altering the settings may result in system's
  failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values.
  (Refer to the "Restore Defaults" section in this chapter or introductions of the battery/clearing
  CMOS jumper in Chapter 1 for how to clear the CMOS values.)

<b>BIOS Setup Prog</b>	ram Function Keys
------------------------	-------------------

<←><→>	Move the selection bar to select the screen
<↑><↓>	Move the selection bar to select an item
<enter></enter>	Execute command or enter the submenu
<+>	Increase the numeric value or make changes
<->	Decrease the numeric value or make changes
<esc></esc>	Main Menu: Exit the BIOS Setup program
	Submenus: Exit current submenu
<f1></f1>	Show descriptions of general help
<f3></f3>	Restore the previous BIOS settings for the current submenus
<f9></f9>	Load the Optimized BIOS default settings for the current submenus
<f10></f10>	Save all the changes and exit the BIOS Setup program

### Main

This setup page includes all the items in standard compatible BIOS.

### Advanced

This setup page includes all the items of AMI BIOS special enhanced features.

(ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

### Chipset

This setup page includes all the submenu options for configuring the function of North Bridge and South Bridge.

(ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

### Security

Change, set, or disable supervisor and user password. Configuration supervisor password allows you to restrict access to the system and BIOS Setup.

A supervisor password allows you to make changes in BIOS Setup.

A user password only allows you to view the BIOS settings but not to make changes.

# Server Management

Server additional features enabled/disabled setup menus.

#### Boot

This setup page provides items for configuration of boot sequence.

#### Exit

Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing <F10> can also carry out this task.)

Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)

# 2-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

### Main Menu Help

The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

#### Submenu Help

While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.



When the system is not stable as usual, select the **Load Default Values** item to set your system to its defaults.

The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.

Main Advanced Chipset Se	Bios Setup Utility curity Server Mgmt Boot Save & Exi	.t
BIOS Information Project Version Build Date and Time	T02a 03/25/2013 16:05:08	Set the Date. Use Tab to switch between Date elements.
BMC Information BMC Firmware Version SDR Revision FRU Version	01.38 00.22 00.02	
Processor Information CPU 0 CPU 1 Max CPU Speed CPU Signature Processor Cores Microcode Patch	Genuine Intel(R) CPU @ 1.800 Genuine Intel(R) CPU @ 1.800 1800 MHz 306e2 4 x 2 208	
Memory Information Total Memory Current Memory Speed	2048 MB (DDR3) 1333 MHz [Wed 09/27/2013]	F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
System Date System Time	[WED 03/27/2013] [01:40:49]	E2C: EXII
Version	1 2.15.1236. Copyright (C) 2012 GIGAB\	/TE, Inc.

- ☞ BIOS Information
- ∽ Project Version

Display version number of the project.

○ BIOS Build Date and Time

Displays the date and time when the BIOS setup utility was created.

- ☞ BMC Information
- ☞ BMC Firmware Version

Display version number of the Firmware setup utility.

- SDR Reversion
   Display the SDR reversion information.
- FRU Version

Display the FRU reversion information.

- ∽ Processor Information
- CPU Type/ Max CPU Speed/ CPU Signature / Processor Cores / Microcode Patch Displays the technical specifications for the installed processor.
- ∽ Memory Information
- ∽ Total Memory / Current Memory Speed

Displays the technical specifications for the installed memory.

∽ System Date

Set the date following the weekday-month-day- year format.

∽ System Time

Set the system time following the hour-minute- second format.

# 2-2 Advanced Menu

The Advanced menu display submenu options for configuring the function of various hardware components. Select a submenu item, then press Enter to access the related submenu screen.

Bios Setup Utility Main Advanced Chipset Security Server Mgmt Boot Save & Exit	
<ul> <li>POI Subsystem Settings</li> <li>Runtime Error Logging</li> <li>CPU Configuration</li> <li>USB Configuration</li> <li>SATA Configuration</li> <li>Super 10 Configuration</li> <li>Serial Port Console Redirection</li> <li>Network Stack</li> </ul>	PCI, PCI-X and PCI Express Settings.
<ul> <li>Intel(R) I350 Gigabit Network Connection - 90:28:34:58:15:80</li> <li>Intel(R) I350 Gigabit Network Connection - 90:28:34:58:15:80</li> <li>Intel(R) I350 Gigabit Network Connection - 90:28:34:58:15:86</li> <li>Intel(R) I350 Gigabit Network Connection - 90:28:34:58:15:87</li> </ul>	++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

# 2-2-1 PCI Subsystem Settings

Bios Setup Utility	
[Enabled] [Enabled] [Enabled] [Enabled] [Enabled]	▲ Enable/Disable PCI-Express slot #1 I/O ROM.
[Enabled] [Enabled] [Enabled] [Enabled]	
[PXE]	
[Enabled] [Enabled] [Enabled] [Enabled]	★: Select Screen f1: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values
	F9: Optimized Defaults
[Disabled]	F10: Save & Exit ESC: Exit
[32 PCI Bus Clocks] [Disabled]	•
	<pre>[Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled]</pre>

	Bios Setup Utility	
Advanced		
		▲ Change PCI Express Devices
Onboard LAN1 Controller	[Enabled]	Settings.
Onboard LAN2 Controller	[Enabled]	
Onboard LANS Controller	[Enabled]	
Onboard LAN4 Controller	[Enabled]	
Onboard LAN I/O ROM Option	[PXE]	
Onboard LAN1 I/O ROM	[Enabled]	
Onboard LAN2 I/O ROM	[Enabled]	
Onboard LANS I/O ROM	[Enabled]	
Onboard LAN4 I/O ROM	[Enabled]	
		↔+: Select Screen
PCI 64bit Resources Handling		↑↓: Select Item
Above 4G Decoding	[Disabled]	Enter: Select +/-: Change Opt.
PCI Common Settings		F1: General Help
PCI Latency Timer	[32 PCI Bus Clocks]	F3: Previous Values
VGA Palette Snoop	[Disabled]	F9: Optimized Defaults
PERR# Generation	[Disabled]	F10: Save & Exit
SERR# Generation	[Disabled]	ESC: Exit
		•

# ∽ PCI Express Slot 1/2/3/4/5 I/O ROM

When enabled, This setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Enabled/Disabled. Default setting is **Enabled**.

# ∽ Onboard LAN1/2/3/4 Controller

Enable/Disable Onboard LAN controllers. Options available: Enabled/Disabled. Default setting is **Enabled**.

# ∽ Onboard LAN I/O ROM Option

Configure onboard LAN devices and initialize device expansion ROM. Options available: PXE/iSCSI. Default setting is **PXE**.

### ∽ Onboard LAN1/2/3/4 I/O ROM

Enable/Disable onboard LAN devices and initialize device expansion ROM. Options available: Enabled/Disabled. Default setting is **Disabled**.

### PCI 64bit Resources Handling

# ∽ Above 4G Decoding

Enable/Disable Above 4G Decoding. Options available: Enabled/Disabled. Default setting is **Disabled**.

### PCI Common Settings

# ☞ PCI Latency Timer

Configure PCI Latency Timer.

Options available: 32 PCI Bus Clocks/64 PCI Bus Clocks/96 PCI Bus Clocks/128 PCI Bus Clocks/160 PCI Bus Clocks/192 PCI Bus Clocks/224 PCI Bus Clocks/248 PCI Bus Clocks.

Default setting is 32 PCI Bus Clocks.

# OVGA Platte Snoop

Enable/Disable VGA Palette Tegisters Snooping.

Options available: Enabled/Disabled. Default setting is **Disabled**.

# ☞ PERR Generation

When this item is set to enabled, PCI bus parity error (PERR) is generated and is routed to NMI. Options available: Enabled/Disabled. Default setting is **Disabled**.

### ☞ SERR Generation

When this item is set to enabled, PCI bus system error (SERR) is generated and is routed to NMI. Options available: Enabled/Disabled. Default setting is **Disabled**.

# 2-2-1-1 PCI Express Settings

Advanced	Bios Setup Utility	
PCI Express Device Register Settings Relaxed Ordering Extended Tag No Snoop Maximum Payload Maximum Read Request Extended Synch Link Training Retry Link Training Timeout (uS) Unpopulated Links	(Disabled) (Disabled) (Enabled) (Auto) (Auto) (Disabled) (5) 100 (Keep Link ON)	Enables or Disables PCI Express Device Relaxed Ordering.
		++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
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### ∽ PCI Express Device Register Settings

### ☞ Relaxed Ordering

Enable/DIsable PCI Express Device Relaxed Ordering feature. Options available: Enabled/Disabled. Default setting is **Disabled**.

### Contract Strends Tag

When this feature is enabled, the system will allow device to use 8-bit Tag field as a requester. Options available: Enabled/Disabled. Default setting is **Disabled**.

### ా No Snoop

Enable/Disable PCI Express Device No Snoop option. Options available: Enabled/Disabled. Default setting is **Enabled**.

### ∽ Maximum Playload

Set maximum playlooad for PCI Express Device or allow system BIOS to select the value. Options available: Auto/128 Bytes/256 Bytes/512 Bytes/1024 Bytes/2048 Bytes/4096 Bytes. Default setting is **Auto**.

### Maximum Read Request

Set maximum Read Reuest size for PCI Express Device or allow system BIOS to select the value. Options available: Auto/128 Bytes/256 Bytes/512 Bytes/1024 Bytes/2048 Bytes/4096 Bytes. Default setting is **Auto**.

### C Extended Synch

When this feature is enabled, the system will allow generation of Extended Synchronization patterns. Options available: Enabled/Disabled. Default setting is **Enabled**.

# ∽ Link Training Retry

Define the number of Retry Attempts software wil take to retrain the link if previous training attempt was unsuccessful. Press <+> / <-> keys to increase or decrease the desired values.

# ∽ Link Training Timeout (us)

Define the number of Microseconds software will wait before polling 'Link Training' bit in Link Status register. Press <+> / <-> keys to increase or decrease the desired values. Value rang is from 10 to 10000 us.

### ∽ Unpopulated Links

When this item is set to 'Disable Link, the system will operate power save feature for those unpopulated PCI Express links.

Options available: Keep Link ON/ Disable. Default setting is Keep Link ON.

# 2-2-2 Runtime Error Logging

Advanced		Bios Setup Utility	
Runtime Error Logging	g Support [		Enable/Disable Runtime Error Logging Support. ++: Select Screen †!: Select Item
			Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F3: Optimized Defaults F10: Save & Exit ESC: Exit

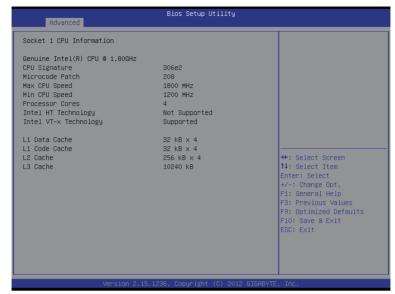
# ∽ Runtime Error Logging

Enable/Disable Runtime error logging support. Options available: Enabled/Disabled. Default setting is **Disabled**.

# 2-2-3 CPU Configuration

Advanced	Bios Setup Utility	
CPU Configuration > Socket 0 CPU Information > Socket 1 CPU Information		Socket specific CPU Information
CPU Speed 64-bit Active Processon Cores Limit CPUID Maximum Execute Disable Bit Handware Prefetcher	1800 MHz Supported [A11] [Disabled] [Enabled] [Enabled]	
DCU Streamer Prefetcher DCU IP Prefetcher	[Enabled] [Enabled] [Enabled]	<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
Version 2.15.12	36. Copyright (C) 2012 GIGA	BYTE, Inc.

Advanced	Bios Setup Utility	
Socket 1 CPU Information Genuine Intel(R) CPU @ 1.80GHz CPU Signature Microcode Patch Max CPU Speed Min CPU Speed Processor Cores Intel HT Technology Intel VT-x Technology L1 Data Cache L2 Cache L3 Cache	306e2 208 1800 MHz 1200 MHz 4 Not Supported Supported 32 kB × 4 32 kB × 4 256 kB × 4 10240 kB	++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
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- ☞ CPU Information
- ∽ Socket 0/1 CPU Information
- CPU Type/ Signature / Microcode Patch / Max CPU Speed / Min CPU Speed / Processor Cores / Intel HT Technology / Intel VT-x Technology
- Displays the technical specifications for the installed processor.

# Intel HT Technology / Intel VT-x Technology

Displays the support information for the installed processor.

∽ Cache Information

### C L1 Data Cache / L1 Code Cache / L2 Cache / L3 Cache

Displays the technical specifications for the installed processor.

→ CPU Speed / 64-bit

Displays the technical specifications for the installed processor.

∽ Active Processor Cores (Note)

Allows you to determine whether to enable all CPU cores. Options available: All/1/2/3. Default setting is **All**.

∽ Limit CPUID Maximum

When enabled, the processor will limit the maximum COUID input values to 03h when queried, even if the processor supports a higher CPUID input value.

When disabled, the processor will return the actual maximum CPUID input value of the processor when queried.

Options available: Enabled/Disabled. Default setting is **Disabled**.

(Note) This item is present only if you install a CPU that supports this feature. For more information about Intel CPUs' unique features, please visit Intel's website.

# ☞ Execute Disable Bit

When enabled, the processor prevents the execution of code in data-only memory pages. This provides some protection against buffer overflow attacks.

When disabled, the processor will not restrict code execution in any memory area. This makes the processor more vulnerable to buffer overflow attacks.

Options available: Enabled/Disabled. Default setting is **Enabled**.

### → Hardware Prefetcher

Select whether to enable the speculative prefetch unit of the processor.

Options available: Enabled/Disabled. Default setting is Enabled.

# ∽ Adjacent Cache Line Prefetch

When enabled, cache lines are fetched in pairs. When disabled, only the required cache line is fetched. Options available: Enabled/Disabled. Default setting is **Enabled**.

# DCU Streamer Prefetch

Enable prefetch of next L1 Data line based upon multiple loads in same cache line. Options available: Enabled/Disabled. Default setting is **Enabled**.

# → DCU IP Prefetch

Enable prefetch of next L1 Data line based upon sequential load history. Options available: Enabled/Disabled. Default setting is **Enabled**.

# Intel Virtualization Technology

Select whether to enable the Intel Virtualization Technology function. VT allows a single platform to run multiple operating systems in independent partitions.

Options available: Enabled/Disabled. Default setting is **Enabled**.

# 2-2-3-1 CPU Power Management Configuration

Advanced	Bios Setup Utility	
CPU Power Management Configuration Power Technology EIST P-STATE Coordination CPU C3 Report CPU C5 report Package C State limit Energy Performance	[Custom] [Enabled] [HH_ALL] [Enabled] [Enabled] [No Limit] [Balanced Performance]	Enable the power management features.
		++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.15.12	36. Copyright (C) 2012 GIGABYTE	, Inc.

### ∽ CPU Power Management Configuration

#### ∽ Power Technology

Configure the power management features.

Options available: Disable/Energy Efficient/Custom. Default setting is Custom.

### ∽ EIST (Enhanced Intel SpeedStep Technology)

Conventional Intel SpeedStep Technology switches both voltage and frequency in tandem between high and low levels in response to processor load.

Options available: Enabled/Disabled. Default setting is Enabled.

### ∽ P-STATE Coordination

In HW\_ALL mode, the processor hardware is responsible for coordinating the P-state among logical processors dependencies. The OS is responsible for keeping the P-state request up to date on all logical processors.

In SW\_ALL mode, the OS Power Manager is responsible for coordinating the P-state among logical processors with dependencies and must initiate the transition on all of those Logical Processors. In SW\_ANY mode, the OS Power Manager is responsible for coordinating the P-state among logical processors with dependencies and may initiate the transition on any of those Logical Processors. Options available: HW\_ALL/SW\_ALL/SW\_ANY. Default setting is **HW\_ALL**.

### 🗢 Turbo Mode

When this item is enabled, tje processor will automatically ramp up the clock speed of 1-2 of its processing cores to improve its performance.

When this item is disabled, the processor will not overclock any of its core.

Options available: Enabled/Disabled. Default setting is **Enabled**.

# → CPU C3/C6 Report (Note)

Allows you to determine whether to let the CPU enter C3/C6 mode in system halt state. When enabled, the CPU core frequency and voltage will be reduced during system halt state to decrease power consumption. The C3/C6 state is a more enhanced power-saving state than C1. Options available: Enabled/Disabled. Default setting is **Enabled**.

### ☞ Package C State Limit

Configure state for the C-State package limit. Options available: C0/C1/C6/C7/No Limit. Default setting is **No Limit**.

### ∽ Energy Performance

Energy Performance Bias is Intel CPU function. The larger value in MSR\_ENERGY\_PERFORMANCE\_BIAS register, CPU will save more power but lose more performance. **Note:** This register will be changed by OS too if OS support it like Windows 2008 or newer Linux. Options available: Performance : Write value 0 into MSR\_ENERGY\_PERFORMANCE\_BIAS Balanced Performance: Write value 7 into MSR\_ENERGY\_PERFORMANCE\_BIAS Balanced Energy: Write value 11 into MSR\_ENERGY\_PERFORMANCE\_BIAS

Energy Efficient: Write value 15 into MSR\_ENERGY\_PERFORMANCE\_BIAS Default setting is **Performance**.

(Note) This item is present only if you install a CPU that supports this feature. For more information about Intel CPUs' unique features, please visit Intel's website.

# 2-2-4 USB Configuration

USB Configuration		Enables Legacy USB
USB Devices: 1 Drive, 2 Keyboards, 2	Mice, 1 Point	support. AUTO option disables legacy support . no USB devices are connected. DISABLE option
Legacy USB Support EHCI Hand-off	[Enabled] [Disabled]	will keep USB devices available only for EFI applications.
		++: Select Screen
		t↓: Select Item Enter: Select +/-: Change Opt. F1: General Help
		F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

☞ USB Configuration

# ☞ Legacy USB Support

Enables or disables support for legacy USB devices. Options available: Auto/Enabled/Disabled. Default setting is **Enabled**.

# ☞ EHCI Hand-off

Enable/Disable EHCI (USB 2.0) Hand-off function. Options available: Enabled/Disabled. Default setting is **Disabled**.

# 2-2-5 SATA Configuration

Advanced	Bios Setup Utility	
SATA Configuration		(1) IDE Mode. (2) AHCI Mode. (3) RAID Mode.
SATA Port0	Not Present	
SATA Port1 SATA Port2	Not Present Not Present	
SATA Port3	Not Present	
SATA Port4	Not Present	
SATA Port5	Not Present	
SATA Mode	(AHCI Mode)	
Aggressive Link Power Management	[Enabled]	
Port 0 Hot Plug	[Disabled]	
Port 1 Hot Plug	[Disabled]	↔+: Select Screen
Port 2 Hot Plug	[Disabled]	↑↓: Select Item
Port 3 Hot Plug	[Disabled]	Enter: Select
Port 4 Hot Plug Port 5 Hot Plug	[Disabled] [Disabled]	+/–: Change Opt. F1: General Help
FORCE HOCFING	[DISabled]	F3: Previous Values
External SATA Port 0	[Disabled]	F9: Optimized Defaults
External SATA Port 1	[Disabled]	F10: Save & Exit
External SATA Port 2	[Disabled]	ESC: Exit
External SATA Port 3	[Disabled]	
External SATA Port 4	[Disabled]	
External SATA Port 5	[Disabled]	

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Advanced	Bios Setup Utility	
SATA Port5	Not Present	▲ AHCI Supports Staggered
SATA Mode Aggressive Link Power Management	[AHCI Mode] [Enabled]	
Port O Hot Plug	[Disabled]	
Port 1 Hot Plug	[Disabled]	181
Port 2 Hot Plug	[Disabled]	
Port 3 Hot Plug	[Disabled]	
Port 4 Hot Plug	[Disabled]	
Port 5 Hot Plug	[Disabled]	
External SATA Port 0	[Disabled]	
External SATA Port 1	[Disabled]	→+: Select Screen
External SATA Port 2	[Disabled]	†↓: Select Item
External SATA Port 3	[Disabled]	Enter: Select
External SATA Port 4	[Disabled]	+/-: Change Opt.
External SATA Port 5	[Disabled]	F1: General Help
		F3: Previous Values
Staggered Spin–up	[Disabled]	F9: Optimized Defaults
Staggered Spin–up	[Disabled]	F10: Save & Exit
Staggered Spin–up	[Disabled]	ESC: Exit
Staggered Spin–up	[Disabled]	
Staggered Spin–up	[Disabled]	
Staggered Spin–up	[Disabled]	v
Version 2.15.1	.236. Copyright (C) 2012 GIGABYT	E, Inc.

# ☞ SATA Configuration

# · SATA Port 0/1/2/3/4/5 (Note)

Displays the installed HDD devices information. System will automatically detect HDD type.

# ☞ SATA Mode Selection

Select the on chip SATA type. RAID Mode: When set to RAID, the SATA controllerenables both its RAID and AHCI functions. You will be allows access the RAID setup utility at boot time. AHCI Mode: When set to AHCI, the SATA controller enables its AHCI functionality. Then the RAID function is disabled and cannot be access the RAID setup utility at boot time. Options available: RAID/AHCI/Disabled. Default setting is **AHCI Mode**.

### Aggressive Link Power Management

Enable PCH to aggressively enter link power state. Options available: Enabled/Disabled. Default setting is **Enabled**.

### → Hot Plug (for Serial SATA Port 0/1/2/3/4/5)

Enable/Disable Hot Plug support for Serial ATA Port 0/1/2/3/4/5. Options available: Enabled/Disabled. Default setting is **Disabled**.

# ∽ External SATA (for Serial SATA Port 0/1/2/3/4/5)

Enable/Disable External SATA support for Serial ATA Port 0/1/2/3/4/5. Options available: Enabled/Disabled. Default setting is **Disabled**.

### ∽ SATA Device Type (for Serial SATA Port 0/1)

Define the SATA Device for Serial ATA Port 0/1. Options available: Hard Disk Drive/Solid State Drive. Default setting is **Disabled**.

# ∽ Straggered Spin Up (for Serial SATA Port 0/1/2/3/4/5)

On an edge detect from 0 to 1, the PCH starts a COMreset initialization to the device. Options available: Enabled/Disabled. Default setting is **Disabled**.

(Note) This item is will not appear when the SATA mode is set ot RAID mode.

# 2-2-6 Info Report Configuration

Info Report Configuration		Post Report Support
Post Report		Enabled/Disabled
Post Report		
Error Message Report		
Info Error Message	[Disabled]	
		→+: Select Screen
		t↓: Select Item
		Enter: Select +/-: Change Opt.
		F1: General Help
		F3: Previous Values
		F9: Optimized Defaults F10: Save & Exit
		ESC: Exit

- ∽ Info Report Configuration
- ∽ Post Report
- ∽ Post Report

Enable/Disable Post Report support. Options available: Enabled/Disabled. Default setting is **Enabled**.

☞ Error Message Report

### ∽ Info Error Message

Enable/Disable Info Error Message support. Options available: Enabled/Disabled. Default setting is **Disabled**.

# 2-2-7 Super IO Configuration

Super IO Configuration Super IO Chip IT8728 > Serial Port 0 Configuration > Serial Port 1 Configuration	Set Parameters of Serial
Serial Port 0 Configuration	Port O (COMA)
	++: Select Screen 14: Select Item Enter: Select +-: Change Opt. F1: General Help F3: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.15.1236. Copyright (C) 2012 GIGABYT	, Inc.

Advanced	Bios Setup Utility	
Serial Port O Configuration		Enable or Disable Serial
Serial Port Device Settings	[Enabled] IO=3F8h; IRQ=4;	Port (COM)
Change Settings	[Auto]	
		++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.15.12	36. Copyright (C) 2012 GIGABYTE	, Inc.

Advanced	Bios Setup Utility	
Serial Port 1 Config	yuration	Enable or Disable Serial Port (COM)
Serial Port Device Settings	[Enabled] IO=2F8h; IRQ=3;	
Change Settings	[Auto]	
		++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F5: Optimized Defaults F10: Save & Exit ESC: Exit
	Version 2.15.1236. Copyright (C) 201	2 GIGABYTE, Inc.

# ∽ Super IO Configuration

# ∽ Super IO Chip

Display the model name of Super IO chipset.

# ∽ Serial Port 0/1 Configuration

# ∽ Serial Port

When enabled allows you to configure the serial port settings. When set to Disabled, displays no configuration for the serial port.

Options available: Enabled/Disabled. Default setting is **Enabled**.

# → Device Settings

Displays the Serial Port base I/O address and IRQ.

# ∽ Change Settings

Change Serial Port 0/1 device settings. When set to Auto allows the server's BIOS or OS to select a configuration.

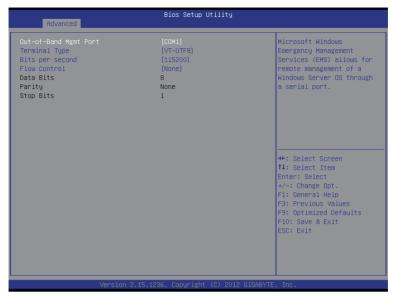
Options available: Auto/IO=3F8; IRQ=4/IO=3F8h; IRQ=3,4,5,6,7,10,11,12/

IO=2F8h; IRQ=3,4,5,6,7,10,11,12 /IO=3E8h; IRQ=3,4,5,6,7,10,11,12/IO=2E8h; IRQ=3,4,5,6,7,10,11,12.

# 2-2-8 Serial Port Console Redirection

Advanced	Bios Setup Utility	
Advanced COM1 Console Redirection Settings COM2 Console Redirection Settings Serial Port for Out-of-Band Management Windows Emergency Management Service Console Redirection > Console Redirection > Console Redirection > Console Redirection Settings SOL Switch		Console Redirection Enable or Disable.
Version 2.15.12 Advanced	36. Copyright (C) 2012 GIGABYTE Bios Setup Utility	, Inc. Emulation: ANSI: Extended

Console Redirection Settings ASCII char set. VT100: ASCII char set. VT100+: Extends VT100 to support Terminal Type Bits per second [ANSI] [115200] Data Bits [8] encoding to map Unicode chars onto 1 or more bytes. Parity VT-UTF8 Combo Key Support Recorder Mode [Enabled] [Disabled] Legacy OS Redirection Resolution [80x24] ++: Select Screen
f↓: Select Item Redirection After BIOS POST [Always Enable] +/-: Change Opt. F1: General Help F9: Optimized Defaults F10: Save & Exit ESC: Exit



# COM1/COM2/Serial Port for Out-of Band Management / Windows Emergency Management Service (EMS)

### Console Redirection (Note)

Select whether to enable console redirection for specified device. Console redirection enables users to manage the system from a remote location.

Options available: Enabled/Disabled. Default setting is **Disabled**.

### Console Redirection Settings

### ∽ Terminal Type

Select a terminal type to be used for console redirection. Options available: VT100/VT100+/ANSI /VT-UTF8.

### Bits per second

Select the baud rate for console redirection. Options available: 9600/19200/57600/115200.

### つ Data Bits

Select the data bits for console redirection.

### C Flow Control

Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. Options available: None/Hardware RTS/CTS. Options available: 7/8.

# Parity

A parity bit can be sent with the data bits to detect some transmission errors. Even: parity bit is 0 if the num of 1's in the data bits is even. Odd: parity bit is0if num of 1's the data bits is odd. Mark: parity bit is always 1. Space: Parity bit is always 0. Mark and Space Parity do not allow for error detection. Options available: None/Even/Odd/Mark/Space.

# Stop Bits

Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit. Options available: 1/2.

# ∽ VT-UTF8 Combo Key Support <sup>(Note)</sup>

Enable/Disable VT-UTF8 Combo Key Support. Options available: Enabled/Disabled. Default setting is **Enabled**.

# ∽ Recorder Mode <sup>(Note)</sup>

When this mode enabled, only text will be send. This is to capture Terminal data. Options available: Enabled/Disabled.

### ∽ Resolution 100x31 <sup>(Note)</sup>

Enables or disables extended terminal resolution. Options available: Enabled/Disabled.

### ∽ Legacy OS Redirection Resolution <sup>(Note)</sup>

On Legacy OS, the number of Rows and Columns supported redirection. Options available: 80x24/80X25.

# ∽ Putty KeyPad (Note)

Select function FunctionKey and KeyPad on Putty. Options available: VT100/LINUX/XTERMR6/SCO/ESCN/VT400.

# ∽ Redirection After BIOS POST (Note)

This option allows user to enable console redirection after O.S has loaded. Options available: Always Enable/Boot Loader. Default setting is **Always Enable**.

# ∽ Out-of-Bnad Mgmt Port

Microsoft Windows Emerency Management Service (EMS) allows for remote management of a Windows Server OS through a serial port.

Options available: COM1/COM2.

# SOL Switch

When enabled, COM1 Switch to AST2300 SOL UART. When disabled, COM1 Switch to IT8728 SOL UART. Options available: Enabled/Disabled. Default setting is **Disabled**.

# 2-2-9 Network Stack

Advanced	Bios Setup Utility	
Network stack		Enable∕Disable UEFI network stack
		++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Vers	ion 2.15.1236. Copyright (C) 2012	GIGABYTE, Inc.

# ∽ Network stack

Enable/Disable UEFI network stack. Options available: Enabled/DIsabled. Default setting is **Disabled**.

# 2-2-10 Intel (R) I350 Gigabit Network Connection



# ∽ PORT CONFIGURATION MENU

# ∽ NIC Configuration

# Link Speed

Change link speed duplex for current port. Options available: AutoNeg/10Mbps Half/10Mbps Half/10Mbps Half/100Mbps Full. Default setting is **AutoNeg**.

# ・ Wake On LAN

Enable/Disable Wake On LAN feature. Options available: Enabled/Disabled. Default setting is **Enabled**.

# ∽ Blink LEDs (range 0-15 seconds)

Blink LEDs for the specified duration (up to 15 seconds). Press the numberic keys to input the desired value.

# ∽ Link Status

Display the link status.

# Factory MAC Address/Alternate MAC Address

Display the Factory and Alternate MAC address information.

# 2-3 Chipset Menu

The Chipset menu display submenu options for configuring the function of North Bridge and South Bridge. Select a submenu item, then press Enter to access the related submenu screen.

	Bios Setup		
Main Advanced Chipset	Security Server Mgmt	Boot Save & Exit	
▶ North Bridge ▶ South Bridge ▶ ME Subsystem			North Bridge Parameters
			++: Select Screen 14: Select Item Enter: Select +/-: Change Opt.
			F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Ver	sion 2.15.1236. Copyrigh	t (C) 2012 GIGABYTE	. Inc.

# 2-3-1 North Bridge

Channel Interleaving Rank Interleaving Patrol Scrub Demand Scrub

Data Scrambling

Device Tagging Rank Margin

Altitude

Thermal Throttling OLTT Peak BW %

Chipset	Bios Setup Utility	
		▲ IOH Configuration Page
QPI Configuration		
Compatibility RID	[Enabled]	
Memory Configuration		
Total Memory Current Memory Mode Current Memory Speed Mirroring Sparing Memory Mode Numa MPST Support DDR Speed Channel Interleaving Rank Interleaving Patrol Scrub Demand Scrub Demand Scrub Deta Scrambling Device Tagging Rank Margin	2048 MB (DDR3) Independent 1333 MHz Not Possible Not Possible [Independent] [Enabled] [Disabled] [Auto] [Auto] [Enabled] [Disabled] [Disabled] [Disabled]	<pre>**: Select Screen 14: Select Trem Enter: Select */-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
Version	2.15.1236. Copyright (C) 2012	GIGABYTE, Inc.
Chipset	Bios Setup Utility	
Compatibility RID	[Enabled]	▲ Display DIMM presence and
comparinility win	[Elighten]	Size information.
Memory Configuration		
Total Memory Current Memory Mode Current Memory Speed Mirroring Memory Mode Numa MPST Support	2048 MB (DDR3) Independent 1333 MHz Not Possible Not Possible [Independent] [Enabled] [Disabled]	

(300 M) (6. Copyright (C) 2012 GIGABYTE, Inc.

+: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults E10: Come & Evit

F10: Save & Exit ESC: Exit

[Enabled]

[Enabled]

[Disabled]

# ☞ IOH Configuration

Press [Enter] for configuration of advanced items.

### 

Press [Enter] for configuration of advanced items.

### Compatibility RID

Enable/Disable Compatibility RID function. Options available: Enabled/Disabled. Default setting is **Enabled**.

### ∽ Memory Configuration

∽ Total Memory

Displays the total capacity of the installed memory.

### ∽ Current Memory Mode

Displays the current memory mode. Memory mode can be determined in Memory Mode item.

### ∽ Current Memory Speed

Displays the current memory speed.

#### ∽ Mirroring/Sparing

Displays the current support memory mode.

### ∽ Memory Mode

Determine the memory mode.

When set to Indendent mode, all DIMMs are available to the operation system.

When set to Mirroring mode, the motherboard maintains two identical (redundant) copies of all data in memory.

When set to Lockstep mode, the motherboard uses two areas of memory to run the same set of operations in parallel.

When set to Sparing mode, a preset threshold of coorectable errors is used to trigger fail-over. The spare memory is put online and used as active memory in place of the failed memory. Options available: Indpendent /Mirroring/ Lockstep/Sparing.

#### 🗢 Numa

Enable/Disable Non Uniform Memory Access (NUMA) function. Options available: Enabled/Disabled. Default setting is **Enabled**.

### ∽ MPST Support

Enable/Disable MPST Support function. Options available: Enabled/Disabled. Default setting is **Disabled**.

### ∽ DDR Speed

Configure the DDR Speed.

Options available: Auto/Force DDR3 800/Force DDR3 1066/Force DDR3 1333/Force DDR3 1600/Force DDR3 1866. Default setting is **Auto**.

### ∽ Channel interleaving

Configure DDR Channel Interleaving. Options available: Auto/1 Way/2 Way/3 Way/4 Way. Default setting is **Auto**.

### 

Configure DDR Rank Interleaving. This improves memory performance by masking the refresh cycles of each memory bank. The Rank Interleaving works between different physical banks. Options available: Auto/1 Way/2 Way/3 Way/4 Way. Default setting is **Auto**.

# Patrol Scrub

Enable/Disable Patrol Scrub function. Options available: Enabled/Disabled. Default setting is **Enabled**.

# Demand Scrub

Enable/Disable Demand Scrub function. Options available: Enabled/Disabled. Default setting is **Disabled**.

### → Device Tagging

Enable/Disable Device Tagging function. Options available: Enabled/Disabled. Default setting is **Disabled**.

### Control Rank Margin

Enable/Disable Rank Margin function. Options available: Enabled/Disabled. Default setting is **Disabled**.

### ∽ Thermal Thortting

Configure theThermal Thortting. Options available: Disabled/OLTT/CLTT. Default setting is **CLTT**.

### ∽ OLTT Peak BW %

Press the numberic keys to increase or decrease the desired values.

### ∽ Altitude

Configure the Altitude value. Options available: Auto/300 M/900 M/1500 M/3000 M. Default setting is **3000 M**.

# 2-3-1-1 IOH Configuration

Chipset	Bios Setup Utility	
Chipset ► Intel(R) VT for Directed I/O Config Intel(R) I/OAT DCA Support VGA Priority TargetVEA Gen3 Equalization WA's IOH Resource Selection Type No Snoop Optimization MMIOH Size MMCFG BASE		Intel(R) VT for Directed I/O Configuration ++: Select Screen 14: Select Item
	236. Copyright (C) 2012 GIGA	Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Chi	Bios Setup Utility pset	
Intel(R) VT-d	[Enabled]	Enable/Disable Intel(R) Virtualization Technology for Directed I/O.
		<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
Version 2.15.1236. Copyright (C) 2012 GIGABYTE, Inc.		

- ☞ IOH Configuration
- ∽ Intel(R) VT for Directed I/O Configuration Press [Enter] for configuration of advanced items. ∽ Intel(R) I/OAT (Intel I/O Acceleration Technology) Enable/Disable Intel I/OAT function. Options available: Enabled/Disabled. Default setting is **Disabled**. ∽ DCA Support (Direct Cache Access) Enable/Disable Intel DCA Support function. Options available: Enabled/Disabled. Default setting is Disabled. ∽ VGA Priority Define the display device priority. Options available: Onboard/Offboard. Default setting is Offboard. ☞ Gen3 Equalization WA's Enable/DIsable the support for Gen3 Equalization Workaround. Options available: Enabled/Disabled. Default setting is Disabled. ○ IOH Resource Seletion Type Configure IOH Resource Seletion Type. Options available: Auto/Manual. Default setting is Auto. > No Snoop Optimization Options VC0/VCP/VC1. Default setting is VC1. MMCFG Size (Size of the Memory Mapped Configuration Space) Options available: 1G/2G/4G/8G/16G/32G/64G. Default setting is 64G. ∽ MMCFGBASE (Base address of the Memory Mapped Configuration Space) Options available: 0x8000000/0xA000000/0xC0000000/0x40000000. Default setting is 0x80000000.

∽ Intel(R) VT-d

Enable/Disable Intel VT-d Technology function. Options available: Enabled/Disabled. Default setting is **Enabled**.

# 2-3-1-2 QPI Configuration

Chipset	Bios Setup Utility		
Current QPI Link Speed Current QPI Link Freq Isoc QPI Link Speed Mode QPI Link Frequency Select QPI LinkOp QPI LinkOp QPI Link1	Fast 6.4 GT/s [Disabled] [Fast] [Auto] [Disabled] [Disabled] [Enabled]	Enbale ∕Disable Isoc	
		<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>	
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### ∽ Current QPI Link Speed/ Current QPI Link Freq

Displays the current QPI Link Speed and Frequency information.

#### ∽ Isoc

Enable/Disable Isoc.

Options available: Enabled/Disabled. Default setting is Disabled.

### → QPI Link Speed Mode

Configure QPI Link Speed mode. Options available: Fast/Slow. Default setting is **Fast**.

### → QPI Link Frequency Select

Configure QPI Link Frequency. Options available: Auto/6.4 GT/s7.2 GT/s/8.6 GT/s. Default setting is **Auto**.

#### · QPI Link0s

Options available: Enabled/Disabled. Default setting is Disabled.

#### ∽ QPI Link0p

Options available: Enabled/Disabled. Default setting is Disabled.

#### C QPI Link1

Options available: Enabled/Disabled. Default setting is Enabled.

## 2-3-1-3 DIMM Information

Bios Setup Utility		
Chipset		·
CPU Socket 0 DIMM Information Node 0 Ch 0 Dimm 0 Node 0 Ch 1 Dimm 1 Node 0 Ch 1 Dimm 0 Node 0 Ch 2 Dimm 0 Node 0 Ch 2 Dimm 0 Node 0 Ch 3 Dimm 1	Not Present Not Present Not Present Not Present Not Present Not Present Present 2048 HB Independent Not Present	
CPU Socket 1 DIMM Information Node 1 Ch 0 Dimm 0 Node 1 Ch 0 Dimm 1 Node 1 Ch 1 Dimm 1 Node 1 Ch 1 Dimm 1 Node 1 Ch 2 Dimm 0 Node 1 Ch 2 Dimm 0 Node 1 Ch 3 Dimm 1	Not Present Not Present Not Present Not Present Not Present Not Present Not Present Not Present	++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.15.	1236. Copyright (C) 2012 GIGABYTE	, Inc.

- ∽ CPU Socket 0/1 DIMM Information
- ∽ CPU Socket 0:
- DDR3\_P0\_A0/DDR3\_P0\_A1/DDR3\_P0\_B0/DDR3\_P0\_B1 Status
- DDR3\_P0\_C0/DDR3\_P0\_C1/DDR3\_P0\_D0/DDR3\_P0\_D1 Status

The size of memory installed on each of the DDR3 slots.

- ∽ CPU Socket 1:
- DDR3\_P0\_E0/DDR3\_P0\_E1/DDR3\_P0\_F0/DDR3\_P0\_F1 Status
- DDR3\_P0\_G0/DDR3\_P0\_G1/DDR3\_P0\_H0/DDR3\_P0\_H1 Status

The size of memory installed on each of the DDR3 slots.

## 2-3-2 South Bridge Configuration

Bios Setup Utility Chipset		
PCH Information Name Stepping	Patsburg 06 (C1 Stepping)	Support for PCH Compatibility Revision ID (CRID) Functionality.
SB Chipset Configuration PCH Compatibility RID		
Restore AC Power Loss SCU devices	(Last State) (Disabled)	
High Precision Event Timer Configura		
High Precision Timer	[Enabled]	<pre>++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
Version 2.15.1236. Copyright (C) 2012 GIGABYTE, Inc.		

☞ PCH Information

#### Mame/Stepping Information

Displays the name and stepping information of the south bridge.

- ∽ SB Chipset Configuration
- ∽ PCH Compatibility RID

Enable/Disable PCH Compatibility RID support. Options available: Enabled/Disabled. Default setting is **Disabled**.

#### ∽ Restore on AC Power Loss <sup>(Note)</sup>

Defines the power state to resume to after a system shutdown that is due to an interruption in AC power. When set to Last State, the system will return to the active power state prior to shutdown. When set to Stay Off, the system remains off after power shutdown.

Options available: Last State/Stay Off/Power On. The default setting depends on the BMC setting.

#### ∽ SCU Devices

Enable/Disable Patsburg SCU device. Options available: Enabled/Disabled. Default setting is **Disabled**.

#### ∽ High Precision Event Timer Configuration

#### ∽ High Precision Event Timer

Enable/Disable High Precision Event Timer. Options available: Enabled/Disabled. Default setting is **Enabled**.

<sup>(</sup>Note) When the power policy is controlled by BMC, please wait for 15-20 seconds for BMC to save the last power state.

## 2-3-3 Intel ME Subsystem

Chips	et	Bios Setup Utility	
Intel ME Subsystem Con ME Subsystem ME BIOS Interface Vers	-	(Enabled) 1.2	ME Subsystem Help
ME Version		2.1.7.129	
ME FW Status Value	:	0xf0345	
ME FW State	:	SPS ME FW Active	
ME FW Operation State	:	MO without UMA	
ME FW Error Code	:	No Error	↔: Select Screen ↑↓: Select Item
ME Ext FW Status Value	:	0x39000301	F+. Select Enter: Select +/−: Change Opt.
BIOS Booting Mode	:	Performance Optmized mode	F1: General Help F3: Previous Values
Cores Disabled	:	0	F3: Frevious values F9: Optimized Defaults F10: Save & Exit
ME FW SKU Information	:	SiEn NM	ESC: Exit
End–of–POST Status	:	EOP disabled in POST	
V	ersion 2.15.12	236. Copyright (C) 2012 GIGABYTE	, Inc.

### ∽ ME Subsystem Configuration

Enable/Disable ME subsystem configuration. Options available: Enabled/Disabled. Default setting is **Enabled**.

## 2-4 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.

Main Advanced Chip	Bios Setup Utilit set <mark>Security</mark> Server Mgmt Boot	
Password Description		Set Administrator Password
then this only limits only asked for when e If ONLY the User's pa is a power on passwor boot or enter Setup. have Administrator ri The password length m in the following rang Winimum length	ssword is set, then this d and must be entered to In Setup the User will ghts. ust be e: 3	
Maximum length	20	↔: Select Screen t↓: Select Item
Administrator Passwor User Password		Enter: Select +/-: Change Opt. F1: General Help
▶ Secure Boot menu		F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
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There are two types of passwords that you can set:

Administrator Password

Entering this password will allow the user to access and change all settings in the Setup Utility.

User Password

Entering this password will restrict a user's access to the Setup menus. To enable or disable this field, a Administrator Password must first be set. A user can only access and modify the System Time, System Date, and Set User Password fields.

#### ∽ Administrator Password

Press Enter to configure the Administrator password.

#### ☞ User Password

Press Enter to configure the user password.

## 2-4-1 Secure Boot menu (Optional)

The Secure Boot Menu is applicable when your device is installed the Windows® 8 operatin system.

Platform Mode Secure Boot Secure Boot Control Secure Boot Mode > Image Execution Policy > Key Management	Setup Disabled [Enabled] [Custom]	Secure Boot mode selector. 'Custom' Mode allows for more flexibility changing Image Execution policy and Secure Boot Key management.
		+: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

#### ∽ Secure Boot menu

#### ∽ Platform Mode

Display the System Platform Mode State.

#### ∽ Secure Boot

Display the status of Secure Boot.

#### ∽ Secure Boot Control

Enable/Disable Secure Boot function.

Options available: Enabled/Disabled. Default setting is Enabled.

#### ☞ Secure Boot Mode

Secure Boot requires all the applications that are running during the booting process to be pre-signed with valid digital certificates. This way, the system knows all the files being loaded before Windows 8 loads and gets to the login screen have not been tampered with.

When set to Standard, it will automatically load the Secure Boot keys form the BIOS databases.

When set to Custom, you can customize the Secure Boot settings and manually load its keys from the BIOS database.

Options available: Standard/Custom. Default setting is Standard.

## 2-4-1-1 Image Execution Policy

Sector	Bios Setup Utility Scurity	
Internal FV Option ROM Removable Media Fixed Media	[Always Execute] [Deny Execute] [Deny Execute] [Deny Execute]	Image Execution Policy per device path on Security Violation.
		<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
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#### ☞ Image Execution policy

#### ∽ Internal FV

Image Execution Policy per device path on Security Violation. Options available: Always Execute. Default setting is **Always Execute**.

#### ∽ Option ROM

Image Execution Policy per device path on Security Violation. Options available: Always Execute/Always Deny/Allow Execute/Defer Execute/ Deny Execute/ Query User. Default setting is **Deny Execute**.

#### ☞ Removable Media

Image Execution Policy per device path on Security Violation. Options available: Always Execute/Always Deny/Allow Execute/Defer Execute/ Deny Execute/ Query User. Default setting is **Deny Execute**.

#### ☞ Fixed Media

Image Execution Policy per device path on Security Violation.

Options available: Always Execute/Always Deny/Allow Execute/Defer Execute/ Deny Execute/ Query User. Default setting is **Deny Execute**.

## 2-4-1-2 Key Management

Security	Bios Setup Utility	
Factory Default Key Provisioning ▶ Install All Factory Default Keys ▶ Save — All Secure Boot Variables	[Disabled]	Install Factory default Secure Boot Keys when System is in Setup Mode
Platform Key (PK) ▶ Delete PK ▶ Set new PK	NOT INSTALLED	
Key Exchange Key Database(KEK) ▶ Delete KEK ▶ Set new KEK ▶ Append Var to KEK	NOT INSTALLED	
Authorized Signature Database(DB) ▶ Delete DB ▶ Set new DB ▶ Append Var to DB	NOT INSTALLED	++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values
Forbidden Signature Database(DBX) ▶ Delete DBX ▶ Set new DBX ▶ Append Var to DBX	NOT INSTALLED	F9: Optimized Defaults F10: Save & Exit ESC: Exit

#### ☞ Key Management

This item appears only when the Secure Boot Mode is set to Custom.

#### ∽ Default Key Provisioning

Force the system to Setup Mode. This will clear all Secure Boot Variables such as Platform Key (PK), Key-exchange Key (KEK), Authorized Signature Database (db), and Forbidden Signatures Database (dbx).

Options available: Enabled/Disabled. Default setting is **Disabled**.

#### ∽ Install default Secure Boot Keys

Press [Enter] to install default Secure Boot Keys.

#### ∽ Platform Key (PK)

Display the status of Platform Key.

#### ∽ Set PK from File

Press [Enter] to configure a new PK.

#### ∽ Get PK from File

Press [Enter] to configure the existed PK.

#### ∽ Delete the PK

Press [Enter] to delete the existed PK. Once the PK is deleted, all the system's Secure Boot keys will not be activated.

∽ Key Exchange Key Database (KEK)

Display the status of Platform Key.

∽ Set new KEK from File

Press [Enter] to configure a new KEK.

#### ∽ Get new KEK from File

Press [Enter] to configure the existed KEK.

#### Delete KEK

Press [Enter] to delete the KEK from your system.

#### $\odot$ Append an entry to KEK

Press [Enter] to load additional KEK from a storage devices for an additional db and dbx management.

#### ∽ Authorized Signature Database (DB)

Display the status of Authorized Signature Database.

#### Set new DB

Press [Enter] to configure a new db.

Get new DB

Press [Enter] to configure the existed db.

∽ Delete DB

Press [Enter] to delete the db from your system.

#### ∽ Append an entry to DB

Press [Enter] to load additional db from a storage devices.

#### ∽ Forbidden Signature Database (DBX)

Display the status of Forbidden Signature Database.

#### ∽ Set DBX from File

Press [Enter] to configure a new dbx.

#### $\bigcirc$ Get DBX to File

Press [Enter] to configure the existed dbx.

→ Delete the DBX

Press [Enter] to delete the dbx from your system.

#### Append and entry to DBX

Press [Enter] to load additional db from a storage devices.

## 2-5 Server Management Menu



#### ☞ BMC LAN Configuration

BMC LAN Configuration. Press Enter to access the related submenu.

#### ∽ Gbt BMC Function

BMC related function configuration. Press Enter to access the related submenu.

#### $\curvearrowleft$ View FRU information

The FRU information submenu is a simple display page for basic system ID information, as well as system product information. Items on this window are non-configurable.

#### ∽ System Event Log

Displays Event Log advanced settings. Press Enter to access the related submenu.

## 2-5-1 BMC LAN Configuration

channel parameters statically or dynamically(DHCP). Do nothing option will not modify any BMC network parameters during BIOS phase
++: Select Screen T4: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

#### 🗢 Lan Channel 1

#### ∽ Configuration Source

Select to configure LAN channel parameters statically or dynamically (DHCP). Do nothing option willnot modify any BMC network parameters during BIOS phase. Options available: Static/Dynamic/Do Nothing.

#### ☞ IP Address

Display IP Address information.

#### ా Subnet Mask

Display Subnet Mask information.

Please note that the IP address must be in three digitals, for example, 192.168.000.001.

#### ∽ Default Gateway Address

Display Default Gateway Address information.

## 2-5-2 Gbt BMC Function

	Bios Setup Utility Server Mgmt	
Select NCSI and Dedicated LAN		Select to configure LAN channel parameters statically or dynamically(UHCP). Do nothing option will not modify any BMC network parameters during BIOS phase
		★: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
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#### ∽ Select NCSI and Dedicated LAN

Switch NCSI and dedicated LAN and send KCS command. Options available: Mode2(NSCI)/ Mode1 (Dedicated). Default setting is **Mode1 (Dedicated)**.

## 2-5-3 View FRU Information

The System Management submenu is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.

Bios Setup Utility Server Mgmt		
FRU Information System Manufacturer System Product Name System Serial Number Board Manufacturer Board Product Name Board Version Board Serial Number Chassis Manufacturer Chassis Product Name Chassis Serial Number		++: Select Screen 11: Select Item Enter: Select +/-: Change Opt.
	15.1236, Copyright (C) 2012 GIGAB	F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

## 2-5-4 System Event Log

Enabling/Disabling Options		Change this to enable or
SEL Components		disable all features of
		System Event Logging
Erasing Settings Erase SEL	[No]	during boot.
When SEL is Full	[Do Nothing]	
MICH DEE 10 TOTT	[00 1001116]	
Custom EFI Logging Options		
Log EFI Status Codes	[Both]	
NOTE: All values changed here do until computer is restarte		
		++: Select Screen
		†↓: Select Item
		Enter: Select +/-: Change Opt.
		F1: General Help
		F3: Previous Values
		F9: Optimized Defaults
		F10: Save & Exit ESC: Exit
		COT ENT

#### ∽ Enabling/Disabling Options

#### ∽ SEL Components

Change this to enable or disable all features of System Event Logging during boot. Options available: Enabled/Disabled. Default setting is **Enabled**.

#### ☞ Erasing Settings

#### ☞ Erasing SEL

Choose options for erasing SEL.

Options available: No/Yes, On next reset/Yes, On every reset. Default setting is No.

#### $\, \bigtriangledown \,$ When SEL is Full

Choose options for reactions to a full SEL.

Options available: Do Nothing/Erase Immediately. Default setting is Do Nothing.

#### Custom EFI Logging Options

#### ∽ Log EFI Status Codes

Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled/Both/Error code/Progress code. Default setting is **Both**.

## 2-6 Boot Menu

The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

Main Advanced Chipset Security	Bios Setup Utility Server Mgmt Boot Save & Exit	
Boot Configuration Setup Promot Timeout Bootup NumLock State Quiet Boot	1 [On] [Enabled]	Number of seconds to wait for setup activation key. 65535(0xFFF) means indefinite waiting.
Boot Option Priorities Boot Option #1 Boot Option #2 Boot Option #3 Boot Option #4	[UEFI: 512M8 Tiny 1.10] [ 512MB Tiny 1.10] [IBA GE Slot 0800 v1376] [UEFI: Built-in EFI Shell ]	
Network Device BBS Priorities Hard Onive BBS Priorities ▶ CSM16 Parameters ▶ CSM parameters		++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Optimized Defaults F10: Save & Exit ESD: Exit
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#### ☞ Boot Configuration

#### ∽ Setup Prompt Timeout

Number of seconds to wait for setup activation key. 65535(0xFFF) means indefinite waiting." Press the numberic keys to input the desired value.

#### Bootup NumLock State

Enable or Disable Bootup NumLock function. Options available: On/Off. Default setting is **On**.

#### ా Quiet Boot

Enables or disables showing the logo during POST.

Options available: Enabled/Disabled. Default setting is Enabled.

#### ☞ Boot Priority Order

#### ☞ Boot Option #1/#2/#3/#4

Press Enter to configure the boot priority.

By default, the server searches for boot devices in the following secquence:

- 1. UEFI device.
- 2. Hard drive.
- 3. Network device.
- 4. Removable device

#### ∽ Network Device BBS Priorities

Press Enter to configure the boot priority.

### ☞ Hard Drive BBS Priorities

Press Enter to configure the boot priority.

#### ☞ CSM16 Parameters

Press Enter to configure the CSM16 parameters.

#### ☞ CSM Parameters

Press Enter to configure the CSM parameters.

## 2-6-1 CSM16 Parameters

Bios Setup Utility Boot					
CSM16 Parameters		UPON REQUEST - GA20 can be disabled using BIOS			
CSM16 Module Version GateA20 Active Option ROM Messages INT19 Endless Retry INT19 Trap Response	07.70 [Upon Request] [Force BIOS] [Disabled] [Immediate]	services. ALMAYS – do not allow disabling GA2O; this option is useful when any RT code is executed above IMB.			
		<pre>+*: Select Screen 11: Select Item Enter: Select 4/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>			
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#### ☞ CSM16 Parameters

#### → CSM16 Module Version

Display CSM Module version information.

#### ∽ Gate20 Active

Upon Request: GA20 can be disabled using BIOS services. Always: Do not allow disabling GA20; this option is useful when any RT code is executed above 1MB. Options available: Upon Request/Always. Default setting is **Upon Request**.

#### ∽ Option ROM Messages

Option ROM Messages. Options available: Force BIOS/Keep Current. Default setting is Force BIOS.

#### ☞ INT19 Endless Retry

Enabled: Allowed headless retry boot Options available: Enabled/Disabled. Default setting is **Disabled**.

#### ☞ INT19 Trap Response

BIOS reaction on INT19 trapping by Option ROM

Immediate: execute the trap right away.

Postpone: execute the trap during legacy boot.

Options available: Immediate/Postpone. Default setting is Immediate.

## 2-6-2 CSM Parameters

	Bios Setup Utility Boot				
Launch CSM Boot option filter Launch PXE OpROM policy Launch Storage OpROM policy Launch Video OpROM policy Other PCI device ROM priority	[Enabled] [UEFI and Legacy] [Legacy only] [Legacy only] [Legacy only] [Legacy OpROM]	This option controls if CSM will be launched			
		++: Select Soreen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F3: Optimized Defaults F10: Save & Exit ESC: Exit			
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#### ☞ CSM parameters

Press Enter to configure the advanced items.

#### ∽ Launch CSM (Compatibility Support Module)

Enable/Disable Compatibility Support Module (CSM) launch.

Options available: Enabled/Disabled. Default setting is Enabled.



If the Launch CSM is set to Disabled, the following five items will not be able to support Legacy mode.

#### ∽ Boot option filter

Determines which devices system will boot to.

Options available: UEFI and Legacy/Legacy only/UEFI only. Default setting is UEFI and Legacy.

#### ∽ Launch PXE OpROM policy

Determines which devices system will boot to.

Options available: Do not launch/UEFI only/Legacy only/Legacy first/UEFI first. Default setting is **Do not launch**.

#### ∽ Launch Storage OpROM policy

Determines which devices system will boot to. Options available: Do not launch/UEFI only/Legacy only/Legacy first/UEFI first. Default setting is Legacy only.

#### ∽ Launch Video OpROM policy

Determines which devices system will boot to.

Options available: Do not launch/UEFI only/Legacy only/Legacy first/UEFI first. Default setting is Legacy only.

#### ∽ Other PCI device ROM priority

For PCI devices other than Network, Mass storage or Video device, defines which OpROM to launch. Options available: UEFI OpROM/Legacy OpROM. Default setting is **UEFI OpROM.** 

## 2-7 Exit Menu

The Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press **Enter**.

Main Advanced Chipset	Security	Bios Setup Server Mgmt			
Save Changes and Exit Discard Changes and Exit Save Options Save Changes Restore Defaults					Exit system setup after saving the changes.
Boot Override IEA GE Slot 0800 v1376 UEFI: Built-in EFI Shell UEFI: 512MB Tiny 1.10 512MB Tiny 1.10					<pre>++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
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#### ∽ Save Changes and Exit

Saves changes made and close the BIOS setup. Options available: Yes/No.

#### Discard Changes and Exit

Discards changes made and close the BIOS setup. Options available: Yes/No.

#### ∽ Save Options

#### ∽ Save Changes

Saves changes made in the BIOS setup. Options available: Yes/No.

#### ☞ Restore Defaults

Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes/No.

#### ∽ Boot Override

Press Enter to configure the device as the boot-up drive.

### → UEFI: Built-in in EFI Shell

Press <Enter> on this item to Launch EFI Shell from filesystem device.

# Chapter 3 Appendix

## 3-1 Regulatory Statements

### Regulatory Notices

This document must not be copied without our written permission, and the contents there of must not be imparted to a third party nor be used for any unauthorized purpose. Contravention will be prosecuted. We believe that the information contained herein was accurate in all respects at the time of printing. GIGABYTE cannot, however, assume any responsibility for errors or omissions in this text. Also note that the information in this document is subject to change without notice and should not be construed as a commitment by GIGABYTE.

#### Our Commitment to Preserving the Environment

In addition to high-efficiency performance, all GIGABYTE motherboards fulfill European Union regulations for RoHS (Restriction of Certain Hazardous Substances in Electrical and Electronic Equipment) and WEEE (Waste Electrical and Electronic Equipment) environmental directives, as well as most major worldwide safety requirements. To prevent releases of harmful substances into the environment and to maximize the use of our natural resources, GIGABYTE provides the following information on how you can responsibly recycle or reuse most of the materials in your "end of life" product.

#### Restriction of Hazardous Substances (RoHS) Directive Statement

GIGABYTE products have not intended to add and safe from hazardous substances (Cd, Pb, Hg, Cr+6, PBDE and PBB). The parts and components have been carefully selected to meet RoHS requirement. Moreover, we at GIGABYTE are continuing our efforts to develop products that do not use internationally banned toxic chemicals.

#### Waste Electrical & Electronic Equipment (WEEE) Directive Statement

GIGABYTE will fulfill the national laws as interpreted from the 2002/96/EC WEEE (Waste Electrical and Electronic Equipment) directive. The WEEE Directive specifies the treatment, collection, recycling and disposal of electric and electronic devices and their components. Under the Directive, used equipment must be marked, collected separately, and disposed of properly.

### WEEE Symbol Statement



The symbol shown below is on the product or on its packaging, which indicates that this product must not be disposed of with other waste. Instead, the device should be taken to the waste collection centers for activation of the treatment, collection, recycling and disposal procedure. The separate collection and recycling of your waste equipment at the time of disposal will help to conserve natural resources and ensure that it is recycled in a manner that protects human health

and the environment. For more information about where you can drop off your waste equipment for recycling, please contact your local government office, your household waste disposal service or where you purchased the product for details of environmentally safe recycling.

- When your electrical or electronic equipment is no longer useful to you, "take it back" to your local or regional waste collection administration for recycling.
- If you need further assistance in recycling, reusing in your "end of life" product, you may contact us at the Customer Care number listed in your product's user's manual and we will be glad to help you with your effort.

Finally, we suggest that you practice other environmentally friendly actions by understanding and using the energy-saving features of this product (where applicable), recycling the inner and outer packaging (including shipping containers) this product was delivered in, and by disposing of or recycling used batteries properly. With your help, we can reduce the amount of natural resources needed to produce electrical and electronic equipment, minimize the use of landfills for the disposal of "end of life" products, and generally improve our quality of life by ensuring that potentially hazardous substances are not released into the environment and are disposed of properly.