# **GA-6PXSV3**

LGA 2011 socket motherboard for Intel $^{\ensuremath{\$}}$  E5-1600/E5-2600 series processors

User's Manual

Rev. 1002

#### **Copyright**

© 2013 GIGA-BYTE TECHNOLOGY CO., LTD. All rights reserved. The trademarks mentioned in this manual are legally registered to their respective owners.

#### **Disclaimer**

Information in this manual is protected by copyright laws and is the property of GIGABYTE. Changes to the specifications and features in this manual may be made by GIGABYTE without prior notice. No part of this manual may be reproduced, copied, translated, transmitted, or published in any form or by any means without GIGABYTE's prior written permission.

#### **Documentation Classifications**

In order to assist in the use of this product, GIGABYTE provides the following types of documentations:

For detailed product information, carefully read the User's Manual.

For product-related information, check on our website at: http://www.gigabyte.com

## Table of Contents

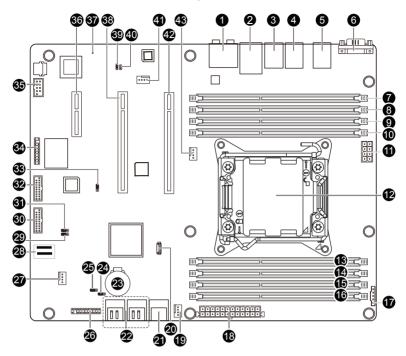
Box Conte	ents .			5	
GA-6PXS	V3 N	/lother	board Layout	6	
Chapter 1	Hardware Installation				
	1-1	Inst	allation Precautions	9	
	1-2	Pro	duct Specifications	10	
	1-3	Inst	alling the CPU and CPU Cooler	12	
		1-3-1	Installing the CPU		
	1-4	Inst	alling the Memory		
		1-4-1	Four Channel Memory Configuration		
		1-4-2	Installing a Memory		
		1-4-3	DIMM Population Table		
	1-5	Bac	k Panel Connectors	16	
	1-6	Inte	rnal Connectors	18	
Chapter 2	BIC		up		
	2-1	The	Main Menu	34	
	2-2		anced Menu		
		2-2-1	PCI Subsystem Settings		
		2-2-1-1	PCI Express Settings		
		2-2-2	Runtime Error Logging		
	:	2-2-3	CPU Configuration		
	:	2-2-3-1	CPU Power Management Configuration	.45	
	:	2-2-4	USB Configuration	.47	
	:	2-2-5	SATA Configuration	.48	
	:	2-2-6	Info Report Configuration	.50	
	:	2-2-7	Super IO Configuration		
		2-2-8	Serial Port Console Redirection	.53	
	1	2-2-9	Network Stack		
	:	2-2-10	Intel (R) 82574L Gigabit Network Connection	.57	
	2-3	Chi	pset Menu	59	
	:	2-3-1	North Bridge	.60	
	1	2-3-1-1	IOH Configuration	.63	
			QPI Configuration		
			DIMM Information		
		2-3-2	South Bridge Configuration		
	:	2-3-3	Intel ME Subsystem	.68	

	2-4	Security Menu	69
	2-5	Server Management Menu	70
	2-5	-1 BMC LAN Configuration	71
	2-5	-2 Gbt BMC Function	.72
	2-5	-3 View FRU Information	.73
	2-5	-4 System Event Log	.74
	2-6	Boot Menu	75
	2-6	-1 CSM16 Parameters	77
	2-6	-2 CSM Parameters	78
	2-7	Exit Menu	80
Chapter 3	Apper	ndix	81
	3-1	Regulatory Statements	81

Box Contents
GA-6PXSV3 motherboard
☑ Driver CD
☑ Two SATA cables
☑ I/O Shield
<ul> <li>The box contents above are for reference only and the actual items shall depend on the product package you obtain.</li> <li>The box contents are subject to change without notice.</li> </ul>

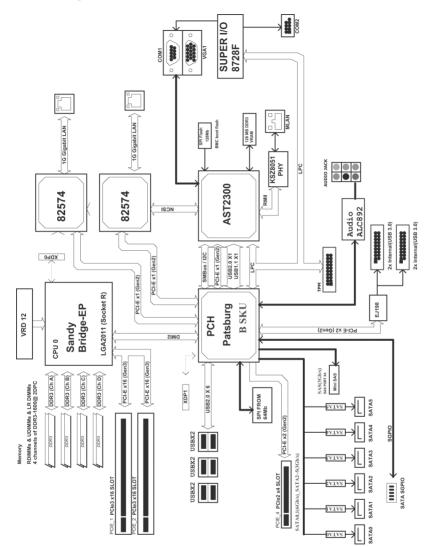
• The motherboard image is for reference only.

## GA-6PXSV3 Motherboard Layout



Item	Code	Description
1	AUDIO1	Audio jacks
2	MLAN1	BMC Management LAN port
3	USB LAN2	LAN connector and USB connectors
4	USB LAN1	LAN connector and USB connectors
5	R USB1	USB connectors
6	COM1 VGA1	Serial port and VGA port
7	DDR3 P0 A0	DIMM slot (channel A-0)
8	DDR3_P0_A1	DIMM slot (channel A-1)
9	DDR3_P0_B0	DIMM slot (channel B-0)
10	DDR3_P0_B1	DIMM slot (channel B-1 )
11	P2_CPU	8 pin power connector
12	CPU0	Intel LGA 2011 socket
13	DDR3_P0_D1	DIMM slot (channel D-1)
14	DDR3_P0_D0	DIMM slot (channel D-0)
15	DDR3_P0_C1	DIMM slot (channel C-1)
16	DDR3_P0_C0	DIMM slot (channel C-0)
17	PWR_DET1	PMBus connector
18	P1	24 pin power connector
19	SYS_FAN2	System fan cable connectors
20	SATA_SGPIO	SATA SGPIO connector
21	SATA01	SATA 6Gb/s connectors
22	SATA2345	SATA 3Gb/s connectors
23	BAT1	Battery socket
24	PASSWORD1	Clear password jumper
25	CLR_CMOS1	Clear CMOS jumper
26	F_PANEL1	Front panel connector
27	SYS_FAN3	System fan cable connectors
28	SAS0_3	Mini SAS connector
29	BMC_FRB1	Force to Stop FRB3 Timer jumper
30	F_USB3_2	Front USB 3.0 connector
31	BIOS_RCVR	BIOS recovery jumper
32	F_USB3_1	Front USB 3.0 connector
33	BIOS_WP	BIOS write protect jumper
34	TPM	TPM module
35	COM2	Serial cable connector
36	PCIE4	PCI-E slot 4 (x4 slot/Gen2/running x2)
37	BMC_LED1	BMC Firmware Readiness LED
38	PCIE2	PCI-E slot 2 (x16 slot/Gen3)
39	SSB_ME1	ME recovery jumper
40	SPDIF_IN	S/PDIF header
41	SYS_FAN1	System fan cable connector
42	PCIE1	PCI-E slot 1 (x16 slot/Gen3)
43	CPU_FAN1	CPU fan cable connector

## **Block Diagram**



## Chapter 1 Hardware Installation

### **1-1** Installation Precautions

The motherboard contains numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the user's manual and follow these procedures:

- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic components such as a motherboard, CPU or memory. If you do not have an ESD wrist strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- Do not place the computer system on an uneven surface.
- Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.

## 1-2 Product Specifications

CPU	<ul> <li>Support for Intel® Xeon® E5 series processors in the LGA2011 package</li> </ul>
	L3 cache varies with CPU
Chipset	<ul> <li>Intel® BD82C604 (X79) PCH</li> </ul>
Memory	• 8 x 1.5V DDR3 DIMM sockets supporting up to 64GB (UDIMM) and
	256GB (RDIMM) of system memory
	* Due to Windows 32-bit operating system limitation, when more than 4 GB of physical
	memory is installed, the actual memory size displayed will be less than 4 GB.
	Four channel memory architecture
	Support for 1600/1033/1066 memory modules
	Support for ECC memory modules
	<ul> <li>2 x Intel® 82574 supports 10/100/1000 Mbps</li> </ul>
Expansion Slots	<ul> <li>1 x PCI Express x16 slot, running at x16 (Gen3/PCIE1)</li> </ul>
	<ul> <li>1 x PCI Express x16 slot, running at x16 (Gen3/PCIE2)</li> </ul>
	<ul> <li>1 x PCI Express x4 slot, running at x2 (Gen2/PCIE4)</li> </ul>
Onboard Audio	REALTEK® ACL892 HD
Storage Interface	2 x SATA 6Gb/s connectors (SATA0/1)
	<ul> <li>4 x SATA 3Gb/s connectors (SATA2/3/4/5)</li> </ul>
	<ul> <li>1 x SAS 3Gb/s connector (SAS0_3)</li> </ul>
	<ul> <li>Support for Intel RSTe SATA RAID 0, RAID 1, RAID 10, RAID 5</li> </ul>
USB	Up to 4 USB 3.0 ports (4 via the USB brackets connected to the internal USB
(onboard	headers)
header)	
Internal	1 x 24-pin ATX main power connector
Connectors	1 x 8-pin ATX 12V power connector
	4 x SATA 3Gb/s connectors
	2 x SATA 6Gb/s connectors
	1 x SAS 3Gb/s connector
	1 x CPU fan header
	3 x System fan header
	1 x front panel header
	2 x USB3.0 headers
	1 x TPM header
	1 x Serial port header
	1 x PMBus header

Back Panel	• 6 x USB 2.0/1.1 ports
Connectors	2 x RJ-45 LAN ports
	<ul> <li>1 x 10/100 Management LAN port</li> </ul>
	<ul> <li>6 x Audio ports (4 x Line-out/1 x Line-in/1 x MIC)</li> </ul>
	1 x COM port
	1 x VGA port
I/O Controller	iTE IT8728 chip
Hardware	System voltage detection
Monitor	CPU/System temperature detection
	CPU/System/Power fan speed detection
	CPU overheating warning
	CPU/System/Power fan fail warning
	CPU/System fan speed control
	* Whether the CPU/system fan speed control function is supported will depend on
	the CPU/system cooler you install.
BIOS	<ul> <li>1 x 64 Mbit flash</li> </ul>
	AMI BIOS
Form Factor	ATX Form Factor; 12 inch x 9.6 inch

\* GIGABYTE reserves the right to make any changes to the product specifications and product-related information without prior notice.

## 1-3 Installing the CPU and CPU Cooler

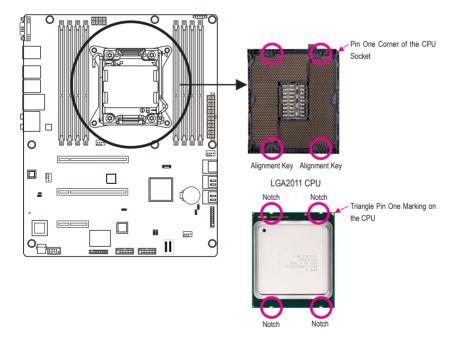


Read the following guidelines before you begin to install the CPU:

- Make sure that the motherboard supports the CPU.
   (Go to GIGABYTE's website for the latest CPU support list.)
- Always turn off the computer and unplug the power cord from the power outlet before installing the CPU to prevent hardware damage.
- Locate the pin one of the CPU. The CPU cannot be inserted if oriented incorrectly. (Or you may locate the notches on both sides of the CPU and alignment keys on the CPU socket.)
- Apply an even and thin layer of thermal grease on the surface of the CPU.
- Do not turn on the computer if the CPU cooler is not installed, otherwise overheating and damage of the CPU may occur.
- Set the CPU host frequency in accordance with the CPU specifications. It is not recommended
  that the system bus frequency be set beyond hardware specifications since it does not meet the
  standard requirements for the peripherals. If you wish to set the frequency beyond the standard
  specifications, please do so according to your hardware specifications including the CPU, graphics card, memory, hard drive, etc.

#### 1-3-1 Installing the CPU

A. Locate the alignment keys on the motherboard CPU socket and the notches on the CPU.



- B. Follow the steps below to correctly install the CPU into the motherboard CPU socket.
  - Before installing the CPU, make sure to turn off the computer and unplug the power cord from the power outlet to prevent damage to the CPU.
  - To protect the socket contacts, do not remove the protective plastic cover unless the CPU is inserted into the CPU socket. Save the cover properly and replace it if the CPU is removed.



Push the lever closest to the "unlock" marking "
"
"
(below referred as lever A) down and away from
the socket to release it.



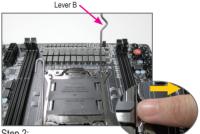
#### Step 3:

Gently press lever A to allow the load plate to rise. Open the load plate. (Note: DO NOT touch the socket contacts after the load plate is opened.)



#### Step 5:

Once the CPU is properly inserted, carefully replace the load plate. Then secure lever B under its retention tab. The protective plastic cover may pop off from the load plate during the process of engaging the lever. Remove the cover. Save the cover properly and always replace it when the CPU is not installed.



Step 2:

Push the lever closest to the "lock" marking " $\triangle$ " (below referred as lever B) down and away from the socket. Then lift the lever.





Hold the CPU with your thumb and index fingers. Align the CPU pin one marking (triangle) with the pin one corner of the CPU socket (or align the CPU notches with the socket alignment keys) and carefully insert the CPU into the socket vertically.





Finally, secure lever A under its retention tab to complete the installation of the CPU.

## 1-4 Installing the Memory



Read the following guidelines before you begin to install the memory:

 Make sure that the motherboard supports the memory. It is recommended that memory of the same capacity, brand, speed, and chips be used.

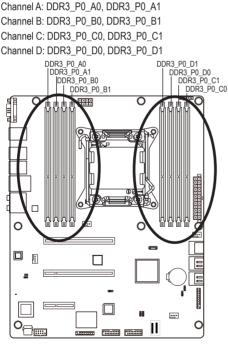
(Go to GIGABYTE's website for the latest supported memory speeds and memory modules.)

- Always turn off the computer and unplug the power cord from the power outlet before installing the memory to prevent hardware damage.
- Memory modules have a foolproof design. A memory module can be installed in only one direction. If you are unable to insert the memory, switch the direction.

#### 1-4-1 Four Channel Memory Configuration

This motherboard provides four DDR3 memory sockets and supports Four Channel Technology. After the memory is installed, the BIOS will automatically detect the specifications and capacity of the memory. Enabling Four Channel memory mode will double the original memory bandwidth.

The eight DDR3 memory sockets are divided into four channels and each channel has two memory sockets as following:



Due to CPU limitations, read the following guidelines before installing the memory in Four Channel mode.

- 1. Four Channel mode cannot be enabled if only one DDR3 memory module is installed.
- 2. When enabling Four Channel mode with two or four memory modules, it is recommended that memory of the same capacity, brand, speed, and chips be used for optimum performance.

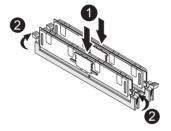
#### 1-4-2 Installing a Memory



Before installing a memory module, make sure to turn off the computer and unplug the power cord from the power outlet to prevent damage to the memory module. Be sure to install DDR3 DIMMs on this motherboard.

#### Installation Step:

- Step 1. Insert the DIMM memory module vertically into the DIMM slot, and push it down.
- Step 2. Close the plastic clip at both edges of the DIMM slots to lock the DIMM module.
- Note: For dual-channel and four-channel operation, DIMMs must be installed in matched pairs.
- Step 3. Reverse the installation steps when you wish to remove the DIMM module.

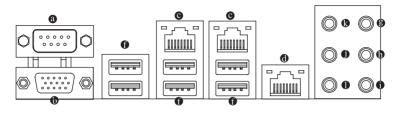


#### 1-4-3 DIMM Population Table

	1N or 2N	DIMM1	DIMM0
	1N	Empty	Single-Rank
	1N	Empty	Dual-Rank
	1N	Empty	Quad-Rank
R-DIMM	1N	Single-Rank	Single-Rank
TC Dimin	1N	Single-Rank	Dual-Rank
	1N	Dual-Rank	Dual-Rank
	1N	Single-Rank	Quad-Rank
	1N D	Dual-Rank	Quad-Rank
	1N	Quad-Rank	Quad-Rank

	1N or 2N	DIMM1	DIMM0
	1N	Empty	Single-Rank
	1N	Empty Dual-Rar	
U-DIMM	2N	Single-Rank	Single-Rank
	2N	Single-Rank	Dual-Rank
	2N	Dual-Rank	Dual-Rank

## 1-5 Back Panel Connectors



#### Serial Port

Connects to serial-based mouse or data processing devices.

#### O Video Port

The video in port allows connect to video in, which can also apply to video loop thru function.

#### RJ-45 LAN Port

The Gigabit Ethernet LAN port provides Internet connection at up to 1 Gbps data rate. The following describes the states of the LAN port LEDs.

#### KVM Server Management 10/100 LAN Port

The LAN port provides Internet connection with data transfer speeds of 10/100Mbps.

#### USB 3.0 Port

The USB port supports the USB 3.0 specification. Use this port for USB devices such as a USB keyboard/mouse, USB printer, USB flash drive and etc.

#### USB 2.0/1.1 Port

The USB port supports the USB 2.0/1.1 specification. Use this port for USB devices such as a USB keyboard/mouse, USB printer, USB flash drive and etc.

#### Line In (Blue)

The default Line In jack. Devices like CD-ROM, walkman etc. can be connected to Line In jack.

#### Line Out (Front Speaker Out/Green)

The default Line Out (Front Speaker Out) jack. Stereo speakers, earphone or front surroundspeakers can be connected to Line Out (Front Speaker Out) jack.

#### MIC In (Pink)

The default MIC In jack. Microphone must be connected to MIC In jack.

#### • Surround Speaker Out (Rear Speaker Out/Black)

The default Surround Speaker Out (Rear Speaker Out) jack. Rear surround speakers can beconnected to Surround Speaker Out (Rear Speaker Out) jack.

#### Center/Subwoofer Speaker Out (Orange)

The default Center/Subwoofer Speaker Out jack. Center/Subwoofer speakers can be connected to Center/Subwoofer Speaker Out jack.

#### • Side Speaker Out (Grey)

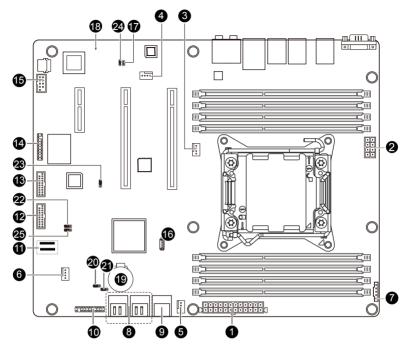
The default Side Speaker Out jack. Surround side speakers can be connected to Side SpeakerOut jack.

Link					
Speed LED Acti	Activity LED	MLAN Speed LED:		Link/Activity LED:	
	7	State	Description	State	Description
F7 '\F	٦ <del>٢</del>	Green On	100 Mbps data rate	On	Link between system and network or no
		Green Blink	10 Mbps or 100 Mbps		access
10/100 LAN Po	_l		data rate	Blinking	Data transmission or receiving is occurring
10/100 EAN 10		Off	10 Mbps data rate	Off	No data transmission or receiving is occurring
Link		82574 Speed LEI	D:	Link/Activ	ity LED:
Speed LED Activi	ty LED	State	Description	State	Description
		Yellow On	1 Gbps data rate	On	Link between system and network or no
		Yellow Blink	Identify 1 Gbps data		access
			rate	Blinking	Data transmission or receiving is occurring
10/100/1000 LAN	Port	Green On	100 Mbps data rate	Off	No data transmission or receiving is occurring
		Green Blink	Identify 100 Mbps data		
			rate		
		Off	10 Mbps data rate		



- When removing the cable connected to a back panel connector, first remove the cable from your device and then remove it from the motherboard.
- When removing the cable, pull it straight out from the connector. Do not rock it side to side to prevent an electrical short inside the cable connector.

## 1-6 Internal Connectors



1)	P1	14)	ТРМ
2)	P2_CPU	15)	COM2
3)	CPU_FAN1 (CPU Fan)	16)	SATA_SGPIO
4)	SYS_FAN1 (System Fan)	17)	SPDIF_IN
5)	SYS_FAN2 (System Fan)	18)	BMC_LED1
6)	SYS_FAN3 (System Fan)	19)	BAT1
7)	PWR_DET1	20)	CLR_CMOS1
8)	SATA23/SATA45	21)	PASSWORD1
9)	SATA01	22)	BIOS_RCVR
10)	F_PANEL1	23)	BIOS_WP
11)	SAS0_3	24)	SSB_ME1
12)	F_USB3_2	25)	BMC_FRB1
13)	F_USB3_1		



Read the following guidelines before connecting external devices:

- · First make sure your devices are compliant with the connectors you wish to connect.
- Before installing the devices, be sure to turn off the devices and your computer. Unplug the
  power cord from the power outlet to prevent damage to the devices.
- After installing the device and before turning on the computer, make sure the device cable has been securely attached to the connector on the motherboard.

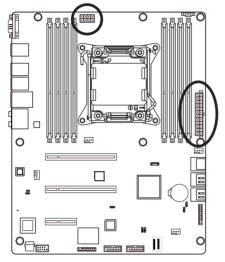
#### 1/2) P1/P2 CPU (2x12 Main Power Connector and 2x4 12V Power Connector)

With the use of the power connector, the power supply can supply enough stable power to all the components on the motherboard. Before connecting the power connector, first make sure the power supply is turned off and all devices are properly installed. The power connector possesses a foolproof design. Connect the power supply cable to the power connector in the correct orientation. The 12V power connector mainly supplies power to the CPU. If the 12V power connector is not connected, the computer will not start.



P1 24

To meet expansion requirements, it is recommended that a power supply that can withstand high power consumption be used (500W or greater). If a power supply is used that does not provide the required power, the result can lead to an unstable or unbootable system.





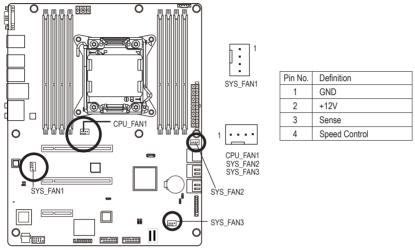
P2 CPU

P2_CPU:					
Pin No.	Definition				
1	GND				
2	GND				
3	GND				
4	GND				
5	+12V				
6	+12V				
7	+12V				
8	+12V				

12		P1:			
		Pin No.	Definition	Pin No.	Definition
금님		1	3.3V	13	3.3V
		2	3.3V	14	-12V
片비		3	GND	15	GND
H		4	+5V	16	PS_ON
느브	7	5	GND	17	GND
	_	6	+5V	18	GND
뜨미		7	GND	19	GND
		8	Power Good	20	-5V
		9	5VSB (stand by +5V)	21	+5V
		10	+12V	22	+5V
1 2 5		11	+12V	23	+5V
13		12	3.3V	24	GND

#### 3/4/5/6) CPU\_FAN1/SYS\_FAN1/SYS\_FAN2/SYS\_FAN3 (CPU Fan/System Fan Headers)

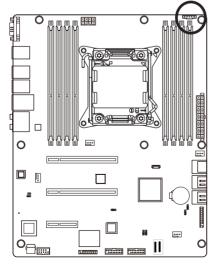
The motherboard has a 4-pin CPU fan header (CPU\_FAN1), three 4-pin (SYS\_FAN1/SYS\_FAN2/SYS\_ FAN3) system fan headers. Most fan headers possess a foolproof insertion design. When connecting a fan cable, be sure to connect it in the correct orientation (the black connector wire is the ground wire). The motherboard supports CPU fan speed control, which requires the use of a CPU fan with fan speed control design. For optimum heat dissipation, it is recommended that a system fan be installed inside the chassis.



Be sure to connect fan cables to the fan headers to prevent your CPU and system from overheating. Overheating may result in damage to the CPU or the system may hang.

These fan headers are not configuration jumper blocks. Do not place a jumper cap on the headers.

#### 7) PWR\_DET1 (Power management connector)



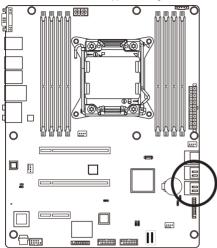
า		

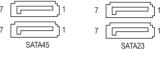
1

Pin No.	Definition
1	SMB CLK
2	SMB DATA
3	SMB Alert
4	GND
5	3.3V Sense

#### 8) SATA2/3/4/5 (SATA 3Gb/s Connectors)

The SATA connectors conform to SATA 3Gb/s standard and are compatible with SATA 1.5Gb/s standard. Each SATA connector supports a single SATA device.





Pin No.	Definition
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

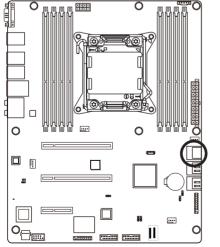


A RAID 0 or RAID 1 configuration requires at least two hard drives. If more than two hard drives are configured, the total number of hard drives must be an even number.
A RAID 10 configuration requires four hard drives.

(Note) When a RAID configuration is built across the SATA 6Gb/s channels, the system performance of the RAID configuration may vary depends on the devices are connected.

#### 9) SATA0/1 (SATA 6Gb/s Connectors)

The SATA connectors conform to SATA 6Gb/s standard and are compatible with SATA 3Gb/s and 1.5Gb/s standard. Each SATA connector supports a single SATA device.

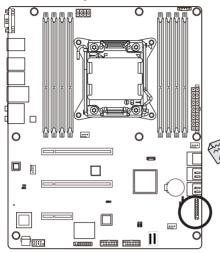




Pin No.	Definition
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND
	· · · · · · · · · · · · · · · · · · ·

#### 10) F\_PANEL1 (Front Panel Header)

Connect the power switch, reset switch, chassis intrusion switch/sensor and system status indicator on the chassis to this header according to the pin assignments below. Note the positive and negative pins before connecting the cables.



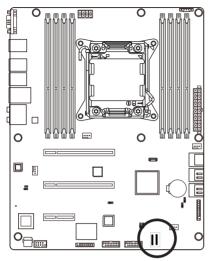


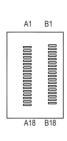
The front panel design may differ by chassis. A front panel module mainly consists of power switch, reset switch, power LED, hard drive activity LED, speaker and etc. When connecting your chassis front panel module to this header, make sure the wire assignments and the pin assignments are matched correctly.

Pin No.	Signal Name	Definition
1	PWLED+	Power LED Signal anode (+)
2	5VSB	5V Stanndby Power
3	NC	No Pin
4	ID_LED+	ID LED Signal anode (+)
5	PWLED-	Power LED Signal cathode(-)
6	ID_LED-	ID LED Signal cathode(-)
7	HD+	Hard Disk LED Signal anode (+)
8	F_SYSRDY	System Front board LED Signal
9	HD-	Hard Disk LED Signal cathode(-)
10	SYS_STATUS-	System Status LED Signal cathode(-)
11	PWB+	Power Button Signal anode (+)
12	L1_ACT	LAN1 active LED Signal
13	PWB+_GND	Ground
14	L1_LINK-	LAN1 Link LED Signal cathode(-)
15	RST_BTN+	Reset button Signal anode (+)
16	SENSOR_SDA	SMBus Data Signal
17	RST_BTN_GND	Ground
18	SENSOR_SCL	SMBus Clock Signal
19	ID_SW+	ID Switch Signal anode (+)
20	CASE_OPEN-	Chassis intrusion Signal cathode(-)
21	ID_SW (GND)	Ground
22	L2_ACT	LAN2 active LED Signal
23	NMI_SW-	NMI switch Signal cathode(-)
24	L2_LINK-	LAN2 Link LED Signal cathode(-)

#### 11) SAS0\_3 (Mini SAS cable connector)

The mini SAS connectors conform to SAS 3Gb/s standard.





Pin No.	Definition	Pin No.	Definition
A1	GND	B1	GND
A2	RX0+	B2	TX0+
A3	RX0-	B3	TX0-
A4	GND	B4	GND
A5	RX1+	B5	TX1+
A6	RX1-	B6	TX1-
A7	GND	B7	GND
A8	SIB7	B8	SIB0
A9	SIB3	B9	SIB1
A10	SIB4	B10	SIB2
A11	SIB5	B11	SIB6
A12	GND	B12	GND
A13	RX2+	B13	TX2+
A14	RX2-	B14	TX2-
A15	GND	B15	GND
A16	RX3+	B16	TX3+
A17	RX3-	B17	TX3-
A18	GND	B18	GND

#### 12/13) F\_USB3\_2/F\_USB3\_1 (USB 3.0 Headers)

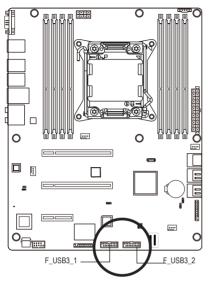
The headers conform to USB 3.0 specification. Each USB header can provide two USB ports via an optional USB bracket. For purchasing the optional USB bracket, please contact the local dealer.

1

•••• 20

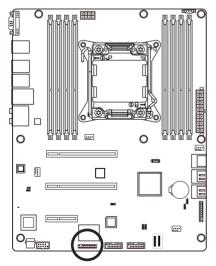
10

11



Pin No.	Definition
1	Power
2	IntA_P1_SSRX-
3	IntA_P1_SSRX+
4	GND
5	IntA_P1_SSTX-
6	IntA_P1_SSTX+
7	GND
8	IntA_P1_D-
9	IntA_P1_D+
10	NC
11	IntA_P2_D+
12	IntA_P2_D-
13	GND
14	IntA_P2_SSTX+
15	IntA_P2_SSTX-
16	GND
17	IntA_P2_SSRX+
18	IntA_P2_SSRX-
19	Power
20	No Pin

#### 14) TPM (TPM Module)



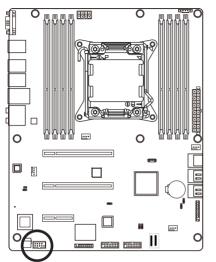


Pin No.	Definition	Pin No.	Definition
1	CLK_33M_TPM	11	LPC_LAD0
2	GND	12	GND
3	LPC_LFRAME_N	13	NC
4	NC	14	NC
5	LPC_RST_TPM	15	P_3V3_AUX
6	NC	16	LPC_SERIRQ
7	LPC_LAD3	17	GND
8	LPC_LAD2	18	GND
9	P3V3	19	P3V3
10	LPC_LAD1	20	NC

#### 15) COM2 (Serial Port Header)

The COM header can provide one serial port via an optional COM port cable. For purchasing the optional COM port cable, please contact the local dealer.

2

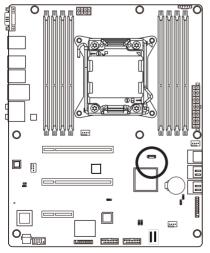


	Pin No.	Definition
10	1	NDCD-
	2	NDSR-
••	3	NSIN
· · ·	4	NRST-
9	5	NSOUT
	6	NCTS-
	7	NDTR-
	8	NRI
	9	GND
	10	No Pin

#### 16) SATA\_SGPIO (SGPIO Header)

SGPIO is stands for Serial General Purpose Input/Output which is a 4-signal (or 4-wire) bus used between a Host Bus Adapter (HBA) and a backplane. Out of the 4 signals, 3 are driven by the HBA and 1 is driven by the backplane. Typically, the HBA is a storage controller located inside a server, desktop, rack or workstation computer that interfaces with Hard disk drives (HDDs) to store and retrieve data.

1

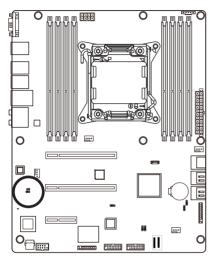


	Pin No.	Definition
"■	1	SGPIO_SATA_LOAD
	2	SGPIO_SATA_DATAOUT
	3	GND
┎┻╝	4	SGPIO_SATA_CLOCK
5	5	SGPIO_SATA_DATAIN

#### 17) SPDIF\_IN (S/PDIF In Header)

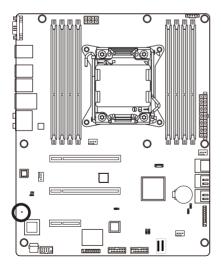
This header supports digital S/PDIF Out and connects a S/PDIF digital audio cable (provided by expansion cards) for digital audio input from your motherboard to certain expansion cards like graphics cards and sound cards.

1 💽



Pin No.	Definition
1	GND
2	SPDIF IN

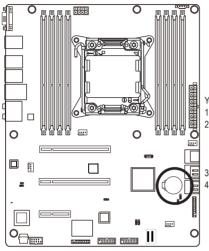
#### 18) BMC\_LED1 (BMC Firmware Readiness LED)



State	Description
On	BMC firmware is initial
Blinking	BMC firmware is ready
Off	System is powered off

#### 19) BAT1 (Battery)

The battery provides power to keep the values (such as BIOS configurations, date, and time information) in the CMOS when the computer is turned off. Replace the battery when the battery voltage drops to a low level, or the CMOS values may not be accurate or may be lost.





You may clear the CMOS values by removing the battery:

- 1. Turn off your computer and unplug the power cord.
- Gently remove the battery from the battery holder and wait for one minute. (Or use a metal object like a screwdriver to touch the positive and negative terminals of the battery holder, making them short for 5 seconds.)

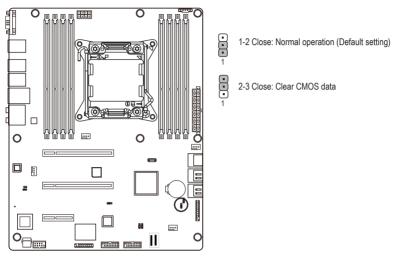
3. Replace the battery.

4. Plug in the power cord and restart your computer.

- · Always turn off your computer and unplug the power cord before replacing the battery.
- · Replace the battery with an equivalent one. Danger of explosion if the battery is replaced with an incorrect model.
- Contact the place of purchase or local dealer if you are not able to replace the battery by yourself or uncertain about the battery model.
- When installing the battery, note the orientation of the positive side (+) and the negative side (-) of the battery (the positive side should face up).
- Used batteries must be handled in accordance with local environmental regulations.

#### 20) CLR\_CMOS1 (Clearing CMOS Jumper)

Use this jumper to clear the CMOS values (e.g. date information and BIOS configurations) and reset the CMOS values to factory defaults. To clear the CMOS values, place a jumper cap on the two pins to temporarily short the two pins or use a metal object like a screwdriver to touch the two pins for a few seconds.



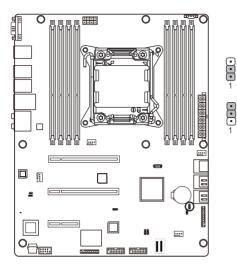


Always turn off your computer and unplug the power cord from the power outlet before clearing the CMOS values.

- After clearing the CMOS values and before turning on your computer, be sure to remove the jumper cap from the jumper. Failure to do so may cause damage to the motherboard.
- After system restart, go to BIOS Setup Exit menu and load factory defaults (select Load Default Values) or manually

configure the BIOS settings (refer to Chapter 2, "BIOS Setup," for BIOS configurations).

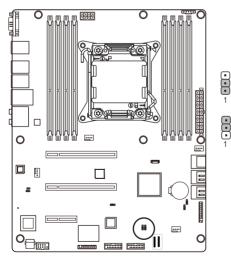
#### 21) PASSWORD1 (Clearing Supervisor Password Jumper)



1-2 Close: Normal operation. (Default setting)

2-3 Close: Clear supervisor password.

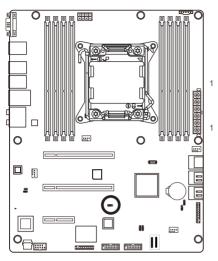
#### 22) BIOS\_RCVR (BIOS Recovery Jumper)



1-2 Close: Normal operation. (Default setting)

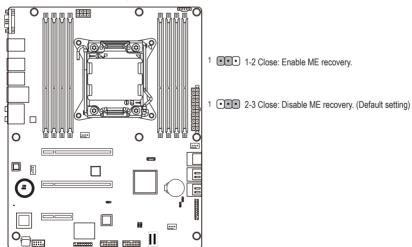
2-3 Close: BIOS recovery mode.

#### 23) BIOS\_WP (BIOS Write Protect Jumper)

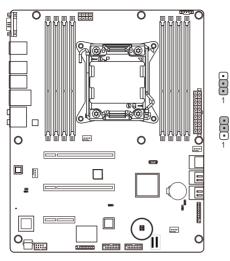


- 1 1-2 Close: Normal operation. (Default setting)
- 1 •••• 2-3 Close: Enable BIOS write protect function.

#### 24) SSB\_ME1 (ME Recovery Jumper)



#### 25) BMC\_FRB1 (Force to Stop FRB3 Timer Jumper)



1-2 Close: Normal operation. (Default setting)

2-3 Close: Force to Stop FRB3 Timer.

## Chapter 2 BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters and loading operating system, etc. BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the <F2> key during the POST when the power is turned on.



- BIOS flashing is potentially risky, if you do not encounter problems of using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.
- It is recommended that you not alter the default settings (unless you need to) to prevent system
  instability or other unexpected results. Inadequately altering the settings may result in system's
  failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values.
  (Refer to the "Restore Defaults" section in this chapter or introductions of the battery/clearing
  CMOS jumper in Chapter 1 for how to clear the CMOS values.)

<b>BIOS Setup</b>	Program	Function	Keys
-------------------	---------	----------	------

	-
<←><→>	Move the selection bar to select the screen
<↑><↓>	Move the selection bar to select an item
<enter></enter>	Execute command or enter the submenu
<+>	Increase the numeric value or make changes
<->	Decrease the numeric value or make changes
<esc></esc>	Main Menu: Exit the BIOS Setup program
	Submenus: Exit current submenu
<f1></f1>	Show descriptions of general help
<f3></f3>	Restore the previous BIOS settings for the current submenus
<f9></f9>	Load the Optimized BIOS default settings for the current submenus
<f10></f10>	Save all the changes and exit the BIOS Setup program

#### Main

This setup page includes all the items in standard compatible BIOS.

#### Advanced

This setup page includes all the items of BIOS special enhanced features.

(ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

#### Chipset

This setup page includes all the submenu options for configuring the function of North Bridge and South Bridge.

(ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

#### Security

Change, set, or disable supervisor and user password. Configuration supervisor password allows you to restrict access to the system and BIOS Setup.

A supervisor password allows you to make changes in BIOS Setup.

A user password only allows you to view the BIOS settings but not to make changes.

#### Server Management

Server additional features enabled/disabled setup menus.

#### Boot

This setup page provides items for configuration of boot sequence.

#### Exit

Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing <F10> can also carry out this task.)

Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)

## 2-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

#### Main Menu Help

The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

#### Submenu Help

While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.



When the system is not stable as usual, select the **Load Default Values** item to set your system to its defaults.

The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.

Main Advanced Chipset Security	Bios Setup Utility Server Mgmt Boot Save & Exit	
BIOS Information Project Version Build Date and Time	T06 04/03/2013 20:11:09	Set the Date. Use Tab to switch between Date elements.
BMC Information BMC Firmware Version SDR Revision FRU Version	01.40 00.22 01.00	
Processor Information CPU 0 Max CPU Speed CPU Signature Processor Cores Microcode Patch	Intel(R) Xeon(R) CPU E5-2643 3300 MHz 206d6 4 619	++: Select Screen 14: Select Item
Memory Information Total Memory Current Memory Speed	65536 MB (DDR3) 1066 MHz	Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults
System Date System Time	[Thu 05/16/2013] [16:13:30]	F10: Save & Exit ESC: Exit
Version 2.15.1	236. Copyright (C) 2012 GIGABYTE	, Inc.

- ☞ BIOS Information
- ∽ Project Version

Display version number of the project.

○→ BIOS Build Date and Time

Displays the date and time when the BIOS setup utility was created.

- ☞ BMC Information
- ☞ BMC Firmware Version

Display version number of the Firmware setup utility.

- SDR Reversion
   Display the SDR reversion information.
- ∽ FRU Version

Display the FRU reversion information.

- ∽ Processor Information
- CPU Type/ Max CPU Speed/ CPU Signature / Processor Cores / Microcode Patch Displays the technical specifications for the installed processor.
- ∽ Memory Information
- ∽ Total Memory / Current Memory Speed

Displays the technical specifications for the installed memory.

∽ System Date

Set the date following the weekday-month-day- year format.

∽ System Time

Set the system time following the hour-minute- second format.

## 2-2 Advanced Menu

The Advanced menu display submenu options for configuring the function of various hardware components. Select a submenu item, then press Enter to access the related submenu screen.

Bios Setup Utility Main Advanced Chipset Security Server Mgmt Boot Save & Exit	:
<ul> <li>PCI Subsystem Settings</li> <li>Runtime Error Logging</li> <li>PCU Configuration</li> <li>USB Configuration</li> <li>SATA Configuration</li> <li>Super 10 Configuration</li> <li>Serial Port Console Redirection</li> <li>Network Stack</li> <li>Intel(R) 82574L Gigabit Network Connection - 90:28:34:34:13:21</li> </ul>	PCI, PCI-X and PCI Express Settings.
Version 2,15,1236. Copyright (C) 2012 GIGABY	<pre>++: Select Screen f4: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>

# 2-2-1 PCI Subsystem Settings

Advanced	Bios Setup Utility	
PCI Express Slot #1 I/O ROM PCI Express Slot #2 I/O ROM PCI Express Slot #3 I/O ROM	(Enabled) (Enabled) (Enabled)	Enable/Disable PCI-Express slot #1 I/O ROM.
Onboard LAN1 Controller Onboard LAN2 Controller	[Enabled] [Enabled]	
Onboard LAN I/O ROM Option	[PXE]	
Onboard LAN1 I/O ROM Onboard LAN2 I/O ROM	[Enabled] [Enabled]	
PCI 64bit Resources Handling Above 4G Decoding	[Disabled]	↔: Select Screen †↓: Select Item Enter: Select
PCI Common Settings PCI Latency Timer VGA Palette Snoop PERR# Generation SERR# Generation	[32 PCI Bus Clocks] [Disabled] [Disabled] [Disabled]	+/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
▶ PCI Express Settings		ESU: EXIL
Version 2.15.12	236. Copyright (C) 2012 GIGABYTE	, Inc.

## ∽ PCI Express Slot #1/2/3 I/O ROM

When enabled, This setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Enabled/Disabled. Default setting is **Enabled**.

#### ∽ Onboard LAN #1/2 Controller

Enable/Disable Onboard LAN controllers. Options available: Enabled/Disabled. Default setting is **Enabled**.

#### ∽ Onboard LAN I/O ROM Option

Configure onboard LAN devices and initialize device expansion ROM. Options available: PXE/iSCSI. Default setting is **PXE**.

#### ∽ Onboard LAN1/2 I/O ROM

Enable/Disable onboard LAN devices and initialize device expansion ROM. Options available: Enabled/Disabled. Default setting is **Disabled**.

### PCI 64bit Resources Handling

## $\curvearrowleft$ Above 4G Decoding

Enable/Disable Above 4G Decoding.

Options available: Enabled/Disabled. Default setting is **Disabled**.

#### PCI Common Settings

#### ∽ PCI Latency Timer

Configure PCI Latency Timer. This BIOS feature controls how long a PCI device can hold the PCI bus before another takes over. The longer the latency, the longer the PCI device caon retain control of the bus before handing it over to another PCI device.

Options available: 32 PCI Bus Clocks/64 PCI Bus Clocks/96 PCI Bus Clocks/128 PCI Bus Clocks/160

PCI Bus Clocks/192 PCI Bus Clocks/224 PCI Bus Clocks/248 PCI Bus Clocks. Default setting is **32 PCI Bus Clocks**.

## ☞ VGA Platte Snoop

Enable/Disable VGA Palette Tegisters Snooping.

Options available: Enabled/Disabled. Default setting is **Disabled**.

## ∽ PERR Generation

When this item is set to enabled, PCI bus parity error (PERR) is generated and is routed to NMI. Options available: Enabled/Disabled. Default setting is **Disabled**.

#### ∽ SERR Generation

When this item is set to enabled, PCI bus system error (SERR) is generated and is routed to NMI. Options available: Enabled/Disabled. Default setting is **Disabled**.

# 2-2-1-1 PCI Express Settings

Advanced	Bios Setup Utility	
PCI Express Device Register Settings Relaxed Ordering Extended Tag No Snoop Maximum Payload Maximum Read Request Extended Synch Link Training Retry Link Training Timeout (uS) Unpopulated Links	(Disabled) (Disabled) (Enabled) (Auto) (Auto) (Disabled) (5) 100 (Keep Link ON)	Enables or Disables PCI Express Device Relaxed Ordering.
		<pre>+: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>

## ∽ PCI Express Device Register Settings

#### ☞ Relaxed Ordering

Enable/DIsable PCI Express Device Relaxed Ordering feature. Options available: Enabled/Disabled. Default setting is **Disabled**.

## Extended Tag

When this feature is enabled, the system will allow device to use 8-bit Tag field as a requester. Options available: Enabled/Disabled. Default setting is **Disabled**.

#### ా No Snoop

Enable/Disable PCI Express Device No Snoop option. Options available: Enabled/Disabled. Default setting is **Enabled**.

#### ∽ Maximum Playload

Set maximum playlooad for PCI Express Device or allow system BIOS to select the value. Options available: Auto/128 Bytes/256 Bytes/512 Bytes/1024 Bytes/2048 Bytes/4096 Bytes. Default setting is **Auto**.

#### Maximum Read Request

Set maximum Read Reuest size for PCI Express Device or allow system BIOS to select the value. Options available: Auto/128 Bytes/256 Bytes/512 Bytes/1024 Bytes/2048 Bytes/4096 Bytes. Default setting is **Auto**.

#### C Extended Synch

When this feature is enabled, the system will allow generation of Extended Synchronization patterns. Options available: Enabled/Disabled. Default setting is **Enabled**.

## ∽ Link Training Retry

Define the number of Retry Attempts software wil take to retrain the link if previous training attempt was unsuccessful. Press <+> / <-> keys to increase or decrease the desired values.

## ∽ Link Training Timeout (us)

Define the number of Microseconds software will wait before polling 'Link Training' bit in Link Status register. Press <+> / <-> keys to increase or decrease the desired values. Value rang is from 10 to 10000 us.

#### ☞ Unpopulated Links

When this item is set to 'Disable Link, the system will operate power save feature for those unpopulated PCI Express links.

Options available: Keep Link ON/ Disable. Default setting is Keep Link ON.

# 2-2-2 Runtime Error Logging

Advanced	Bios Setup Utility	
Runtime Error Logging Support		Enable/Disable Runtime Error Logging Support.
		++: Select Screen †4: Select Item Enter: Select +/-: Change Opt, F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.15.12	36. Copyright (C) 2012 GIGABYTE	, Inc.

## ∽ Runtime Error Logging

Enable/Disable Runtime error logging support. Options available: Enabled/Disabled. Default setting is **Disabled**.

# 2-2-3 CPU Configuration

Advanced	Bios Setup Utility	
CPU Configuration		Socket specific CPU
▶ Socket 0 CPU Information		TH Ormat Ton
CPU Speed 64-bit	3300 MHz Supported	
Hyper-threading Active Processor Cores Limit CPUID Maximum Execute Disable Bit Hardware Prefetcher Adjacent Cache Line Prefetch DCU Streamer Prefetcher DCU IF Prefetcher Intel Virtualization Technology CPU Power Management Configuration	[Enabled] [A11] [Disabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled]	<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
Version 2.15.12	36. Copyright (C) 2012 GIGABYT	E, Inc.

Advanced	Bios Setup Utility	
Socket 0 CPU Information		
Intel(R) Xeon(R) CPU E5-2643 0 @ 3.3 CPU Signature Microcode Patch Max CPU Speed Processor Cores Intel HT Technology Intel VT-x Technology L1 Data Cache L1 Code Cache L2 Cache L3 Cache	OGHZ 206d6 619 3300 MHZ 1200 MHZ 4 Supported Supported 32 kB x 4 32 kB x 4 32 kB x 4 256 kB x 4 10240 kB	++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.15.12	36. Copyright (C) 2012 GIGABYTE	, Inc.

- ∽ CPU Information
- ∽ Socket 0 CPU Information
- CPU Type/ Signature / Microcode Patch / Max CPU Speed / Min CPU Speed / Processor Cores / Intel HT Technology / Intel VT-x Technology Displays the technical specifications for the installed processor.

# Intel HT Technology / Intel VT-x Technology Displaye the support information for the installed process:

Displays the support information for the installed processor.

## → Cache Information

## ∽ L1 Data Cache / L1 Code Cache / L2 Cache / L3 Cache

Displays the technical specifications for the installed processor.

## ☞ CPU Speed / 64-bit

## ∽ Hyper Threading

The Intel Hyper Threading Technology allows a single processor to execute two or more separate threads concurrently. When hyper-threading is enabled, multi-threaded software applications can execute their threads, thereby improving performance.

Options available: Enabled/Disabled. Default setting is Enabled.

Displays the technical specifications for the installed processor.

## Active Processor Cores (Note)

Allows you to determine whether to enable all CPU cores.

Options available: All/1/2/3. Default setting is All.

## ∽ Limit CPUID Maximum

When enabled, the processor will limit the maximum COUID input values to 03h when queried, even if the processor supports a higher CPUID input value.

When disabled, the processor will return the actual maximum CPUID input value of the processor when queried.

Options available: Enabled/Disabled. Default setting is **Disabled**.

## ☞ Execute Disable Bit

When enabled, the processor prevents the execution of code in data-only memory pages. This provides some protection against buffer overflow attacks.

When disabled, the processor will not restrict code execution in any memory area. This makes the processor more vulnerable to buffer overflow attacks.

Options available: Enabled/Disabled. Default setting is **Enabled**.

## ☞ Hardware Prefetcher

Select whether to enable the speculative prefetch unit of the processor. Options available: Enabled/Disabled. Default setting is **Enabled**.

## Adjacent Cache Line Prefetch

When enabled, cache lines are fetched in pairs. When disabled, only the required cache line is fetched. Options available: Enabled/Disabled. Default setting is **Enabled**.

(Note) This item is present only if you install a CPU that supports this feature. For more information about Intel CPUs' unique features, please visit Intel's website.

## ☞ DCU Streamer Prefetch

Enable prefetch of next L1 Data line based upon multiple loads in same cache line. Options available: Enabled/Disabled. Default setting is **Enabled**.

## ∽ DCU IP Prefetch

Enable prefetch of next L1 Data line based upon sequential load history. Options available: Enabled/Disabled. Default setting is **Enabled**.

## ∽ Intel Virtualization Technology

Select whether to enable the Intel Virtualization Technology function. VT allows a single platform to run multiple operating systems in independent partitions.

Options available: Enabled/Disabled. Default setting is **Enabled**.

# 2-2-3-1 CPU Power Management Configuration

Advanced	Bios Setup Utility	
CPU Power Management Configuration Power Technology EIST Turbo Mode P-STATE Coordination CPU C3 Report CPU C6 report Package C State limit Energy Performance	[Custom] [Enabled] [Enabled] [HM_ALL] [Enabled] [Enabled] [No Limit] [Balanced Performance]	Enable the power management features.
		++: Select Screen tl: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.15.12	36. Copyright (C) 2012 GIGABYTE	, Inc.

#### ∽ CPU Power Management Configuration

#### Power Technology

Configure the power management features.

Options available: Disable/Energy Efficient/Custom. Default setting is **Custom**.

## ☞ EIST (Enhanced Intel SpeedStep Technology)

Conventional Intel SpeedStep Technology switches both voltage and frequency in tandem between high and low levels in response to processor load.

Options available: Enabled/Disabled. Default setting is **Enabled**.

## 🗢 Turbo Mode

When this item is enabled, tje processor will automatically ramp up the clock speed of 1-2 of its processing cores to improve its performance.

When this item is disabled, the processor will not overclock any of its core.

Options available: Enabled/Disabled. Default setting is Enabled.

## ∽ P-STATE Coordination

In HW\_ALL mode, the processor hardware is responsible for coordinating the P-state among logical processors dependencies. The OS is responsible for keeping the P-state request up to date on all logical processors.

In SW\_ALL mode, the OS Power Manager is responsible for coordinating the P-state among logical processors with dependencies and must initiate the transition on all of those Logical Processors. In SW\_ANY mode, the OS Power Manager is responsible for coordinating the P-state among logical processors with dependencies and may initiate the transition on any of those Logical Processors. Options available: HW\_ALL/SW\_ALL/SW\_ANY. Default setting is **HW\_ALL**.

## → CPU C3/C6 Report (Note)

Allows you to determine whether to let the CPU enter C3/C6 mode in system halt state. When enabled, the CPU core frequency and voltage will be reduced during system halt state to decrease power consumption. The C3/C6 state is a more enhanced power-saving state than C1. Options available: Enabled/Disabled. Default setting is **Enabled**.

#### → Package C State Limit

Configure state for the C-State package limit. Options available: C0/C1/C6/C7/No Limit. Default setting is **No Limit**.

#### ☞ Energy Performance

Energy Performance Bias is Intel CPU function. The larger value in MSR\_ENERGY\_PERFORMANCE\_BIAS register, CPU will save more power but lose more performance. **Note:** This register will be changed by OS too if OS support it like Windows 2008 or newer Linux. Options available: Performance : Write value 0 into MSR\_ENERGY\_PERFORMANCE\_BIAS Balanced Performance: Write value 7 into MSR\_ENERGY\_PERFORMANCE\_BIAS Balanced Energy: Write value 11 into MSR\_ENERGY\_PERFORMANCE\_BIAS

Energy Efficient: Write value 15 into MSR\_ENERGY\_PERFORMANCE\_BIAS

Default setting is Balance Performance.

<sup>(</sup>Note) This item is present only if you install a CPU that supports this feature. For more information about Intel CPUs' unique features, please visit Intel's website.

# 2-2-4 USB Configuration

Advanced	Bios Setup Utility	
USB Configuration		Enables Legacy USB
USB Devices:		support. AUTO option disables legacy support if
1 Drive, 2 Keyboards, 3	3 Mice, 1 Point	no USB devices are
		connected. DISABLE option
Legacy USB Support	[Enabled] [Enabled]	will keep USB devices
USB3.0 Support XHCI Hand-off	[Enabled]	available only for EFI applications.
EHCI Hand-off	[Disabled]	apprications.
Life Listia of I	[DIGGDICG]	
		→+: Select Screen
		14: Select Item
		Enter: Select
		+/-: Change Opt.
		F1: General Help
		F3: Previous Values
		F9: Optimized Defaults
		F10: Save & Exit
		ESC: Exit
	2.15.1236. Copyright (C) 2012	

#### ☞ USB Configuration

## ☞ Legacy USB Support

Enables or disables support for legacy USB devices. Options available: Auto/Enabled/Disabled. Default setting is **Enabled**.

## → USB3.0 Support

Enables or disables onboard USB 3.0 devices. Options available: Enabled/Disabled. Default setting is **Enabled**.

## ∽ XHCI Hand-off

Enable/Disable EHCI (USB 3.0) Hand-off function. Options available: Enabled/Disabled. Default setting is **Enabled**.

## ☞ EHCI Hand-off

Enable/Disable EHCI (USB 2.0) Hand-off function. Options available: Enabled/Disabled. Default setting is **Disabled**.

## 2-2-5 SATA Configuration

Advanced	Bios Setup Utility	
SATA Configuration		(1) IDE Mode. (2) AHCI Mode. (3) RAID Mode.
SATA Port0	Not Present	House. (b) Milb House.
SATA Port1	Not Present	
SATA Port2	Not Present	
SATA Port3	Not Present	
SATA Port4	Not Present	
SATA Port5	Not Present	
SATA Mode	[AHCI Mode]	
Aggressive Link Power Management	[Enabled]	
Port 0 Hot Plug	[Disabled]	
Port 1 Hot Plug	[Disabled]	→+: Select Screen
Port 2 Hot Plug	[Disabled]	†↓: Select Item
Port 3 Hot Plug	[Disabled]	Enter: Select
Port 4 Hot Plug	[Disabled]	+/-: Change Opt.
Port 5 Hot Plug	[Disabled]	F1: General Help
		F3: Previous Values
External SATA Port 0	[Disabled]	F9: Optimized Defaults
External SATA Port 1	[Disabled]	F10: Save & Exit
External SATA Port 2	[Disabled]	ESC: Exit
External SATA Port 3	[Disabled]	
External SATA Port 4	[Disabled]	
External SATA Port 5	[Disabled]	•
Version 2 15 1	1236. Copyright (C) 2012 GIGAB	YTE Inc

Bios Setup Utility SATA Port5 Not Present 4 [AHCI Mode] SATA Mode Aggressive Link Power Management Port 2 Hot Plug Port 3 Hot Plug Port 4 Hot Plug [Disabled] ↔: Select Screen Enter: Select Item +/-: Change Opt. F1: General Help [Disabled] External SATA Port 3 External SATA Port 4 [Disabled] F3: Previous Values F9: Optimized Defaults F10: Save & Exit Staggered Spin-up Staggered Spin-up Staggered Spin-up Staggered Spin-up [Disabled] Staggered Spin-up Staggered Spin-up [Disabled] [Disabled]

## ☞ SATA Configuration

## ∽ SATA Port 0/1/2/3/4/5

Displays the installed HDD devices information. System will automatically detect HDD type.

## ∽ SATA Mode Selection

Select the on chip SATA type. RAID Mode: When set to RAID, the SATA controllerenables both its RAID and AHCI functions. You will be allows access the RAID setup utility at boot time. AHCI Mode: When set to AHCI,the SATA controller enables its AHCI functionality. Then the RAID function is disabled and cannot be access the RAID setup utility at boot time. Options available: RAID/AHCI/Disabled. Default setting is **AHCI Mode**.

## ∽ Aggressive Link Power Management

Enable PCH to aggressively enter link power state. Options available: Enabled/Disabled. Default setting is **Enabled**.

#### ∽ Hot Plug (for Serial SATA Port 0/1/2/3/4/5)

Enable/Disable Hot Plug support for Serial ATA Port 0/1/2/3/4/5. Options available: Enabled/Disabled. Default setting is **Disabled**.

## ∽ External SATA (for Serial SATA Port 0/1/2/3/4/5)

Enable/Disable External SATA support for Serial ATA Port 0/1/2/3/4/5. Options available: Enabled/Disabled. Default setting is **Disabled**. Options available: Hard Disk Drive/Solid State Drive. Default setting is **Disabled**.

## ∽ Straggered Spin Up (for Serial SATA Port 0/1/2/3/4/5)

On an edge detect from 0 to 1, the PCH starts a COMreset initialization to the device. Options available: Enabled/Disabled. Default setting is **Disabled**.

# 2-2-6 Info Report Configuration



- ∽ Info Report Configuration
- ∽ Post Report
- ∽ Post Report

Enable/Disable Post Report support. Options available: Enabled/Disabled. Default setting is **Enabled**.

☞ Error Message Report

#### ☞ Info Error Message

Enable/Disable Info Error Message support. Options available: Enabled/Disabled. Default setting is **Disabled**.

# 2-2-7 Super IO Configuration



Advanced	Bios Setup Utility	
Serial Port O Configurat.	ion	Enable or Disable Serial Port (COM)
Serial Port Device Settings	[Enabled] IO=3F8h; IRQ=4;	ruit (con)
Change Settings	[Auto]	
		++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Vers	sion 2.15.1236. Copyright (C) 2012 (	GIGABYTE, Inc.

Bios Setup Utility	
	Enable or Disable Serial Port (COM)
[Enabled] IO=2F8h; IRQ=3;	For C (con)
[Auto]	
	++: Select Screen 11: Select Item Enter: Select +/-: Change Opt.
	F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit
	ESC: Exit
	(Enabled) IO=2F8h; IRQ=3;

## ∽ Super IO Configuration

#### ∽ Super IO Chip

Display the model name of Super IO chipset.

## ∽ Serial Port 0/1 Configuration

## ∽ Serial Port

When enabled allows you to configure the serial port settings. When set to Disabled, displays no configuration for the serial port.

Options available: Enabled/Disabled. Default setting is **Enabled**.

## → Device Settings

Displays the Serial Port base I/O address and IRQ.

## ∽ Change Settings

Change Serial Port 0/1 device settings. When set to Auto allows the server's BIOS or OS to select a configuration.

Options available: Auto/IO=3F8; IRQ=4/IO=3F8h; IRQ=3,4,5,6,7,10,11,12/

IO=2F8h; IRQ=3,4,5,6,7,10,11,12 /IO=3E8h; IRQ=3,4,5,6,7,10,11,12/IO=2E8h; IRQ=3,4,5,6,7,10,11,12.

# 2-2-8 Serial Port Console Redirection

Advanced	Bios Setup Utility	
COM1 Console Redirection ▶ Console Redirection Settings	[Enabled]	Console Redirection Enable or Disable.
COM2 Console Redirection ▶ Console Redirection Settings	[Enabled]	
Serial Port for Out-of-Band Managemer Windows Emergency Management Services Console Redirection ► Console Redirection Settings		
SOL Switch	[Disabled]	++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.15.123	36. Copyright (C) 2012 GIGABYTE	, Inc.

COM1 Console Redirection Settings Terminal Type [ANS1] Bits per second [115200] Data Bits [8] Parity [None] Stop Bits [1] Flow Control [None] VT-UTF8 Combo Key Support [Enabled] Recorder Mode [Disabled] Resolution 100x31 [Disabled] Legacy OS Redirection Resolution [60x24] Putty KeyPad [VT100] Redirection After BIOS POST [Always Enable]	Emulation: ANSI: Extended ASCII char set. V1100: ASCII char set. V1100+: Extends V1100 to support color, function keys, etc. VT-UTF8: Uses UTF8 encoding to map Unicode chars onto 1 or more bytes. +*: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F3: Optimized Defaults F10: Save & Exit ESC: Exit

Advanced	Bios Setup Utility	
Out-of-Band Mgmt Port Terminal Type Bits per second Flow Control Data Bits Parity Stop Bits	[COM1] [VT-UTF8] [115200] [None] 8 None 1	Microsoft Windows Emergency Management Services (EMS) allows for remote management of a Windows Server OS through a serial port.
		++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2	2.15.1236. Copyright (C) 2012	GIGABYTE, Inc.

## COM1/COM2/Serial Port for Out-of Band Management / Windows Emergency Management Service (EMS)

## Console Redirection (Note)

Select whether to enable console redirection for specified device. Console redirection enables users to manage the system from a remote location.

Options available: Enabled/Disabled. Default setting is **Disabled**.

#### Console Redirection Settings

∽ Terminal Type

Select a terminal type to be used for console redirection. Options available: VT100/VT100+/ANSI /VT-UTF8.

#### Bits per second

Select the baud rate for console redirection. Options available: 9600/19200/57600/115200.

#### つ Data Bits

Select the data bits for console redirection.

#### ☞ Flow Control

Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. Options available: None/Hardware RTS/CTS. Options available: 7/8.

## Parity

A parity bit can be sent with the data bits to detect some transmission errors. Even: parity bit is 0 if the num of 1's in the data bits is even. Odd: parity bit is0if num of 1's the data bits is odd. Mark: parity bit is always 1. Space: Parity bit is always 0. Mark and Space Parity do not allow for error detection. Options available: None/Even/Odd/Mark/Space.

## Stop Bits

Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit. Options available: 1/2.

## ∽ VT-UTF8 Combo Key Support <sup>(Note)</sup>

Enable/Disable VT-UTF8 Combo Key Support. Options available: Enabled/Disabled. Default setting is **Enabled**.

## ∽ Recorder Mode <sup>(Note)</sup>

When this mode enabled, only text will be send. This is to capture Terminal data. Options available: Enabled/Disabled.

#### ∽ Resolution 100x31 <sup>(Note)</sup>

Enables or disables extended terminal resolution. Options available: Enabled/Disabled.

#### ∽ Legacy OS Redirection Resolution <sup>(Note)</sup>

On Legacy OS, the number of Rows and Columns supported redirection. Options available: 80x24/80X25.

#### ∽ Putty KeyPad (Note)

Select function FunctionKey and KeyPad on Putty. Options available: VT100/LINUX/XTERMR6/SCO/ESCN/VT400.

## ∽ Redirection After BIOS POST (Note)

This option allows user to enable console redirection after O.S has loaded. Options available: Always Enable/Boot Loader. Default setting is **Always Enable**.

## ∽ Out-of-Bnad Mgmt Port

Microsoft Windows Emerency Management Service (EMS) allows for remote management of a Windows Server OS through a serial port.

Options available: COM1/COM2.

## SOL Switch

When enabled, COM1 Switch to AST2300 SOL UART. When disabled, COM1 Switch to IT8728 SOL UART. Options available: Enabled/Disabled. Default setting is **Disabled**.

## 2-2-9 Network Stack

Advanced	Bios Setup Utility	
Network stack		Enable∕Disable UEFI networK stack
		++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Vers	sion 2.15.1236. Copyright (C) 2012	GIGABYTE, Inc.

## ∽ Network stack

Enable/Disable UEFI network stack. Options available: Enabled/DIsabled. Default setting is **Disabled**.

# 2-2-10 Intel (R) 82574L Gigabit Network Connection

Oduancad	Bios Setup Utility	
Advanced PORT CONFIGURATION MENU > NIC Configuration Blink LEDS (mange 0-15 seconds) PORT CONFIGURATION INFORMATION UEFI Driver: Adapter PBA: Chio Type: PCI Device ID PCI Device ID PCI Bus:Device:Function: Link Status Factory MAC Address:	0 Intel(R) PR0/1000 5.4.19 PCI 120523-003 Intel 82574 1003 09:00:00 [Disconnected] 90:2B:34:34:13:20	Click to configure the network device port. +: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.15.12	36. Copyright (C) 2012 GIGABYTE	, Inc.

Advanced	Bios Setup Utility	
Link Speed Wake on LAN	[AutoNeg] [Enabled]	Change link speed and duplex for current port.
		++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
	Version 2.15.1236. Copyright (C) 2012 GIGAB	YTE, Inc.

## ∽ PORT CONFIGURATION MENU

## ∽ NIC Configuration

Press [Enter] for configuration of advanced items.

## ∽ Blink LEDs (range 0-15 seconds)

Blink LEDs for the specified duration (up to 15 seconds). Input the desired value, the range is from 0 to 15 seconds.

## ∽ PORT CONFIGURATION INFORMATION

## → UEFI Driver/Adapter PBA/Chip Type/ PCI Device ID/ PCI Bus Device Function

Displays the technical specifications of the current port.

## ∽ Link Status

Display the link status.

## ☞ Factory MAC Address/Alternate MAC Address

Display the Factory and Alternate MAC address information.

#### ∽ NIC Configuration

## ∽ Link Speed

Change link speed duplex for current port. Options available: AutoNeg/10Mbps Half/10Mbps Half/10Mbps Half/100Mbps Full. Default setting is **AutoNeg**.

## ⑦ Wake On LAN

Enable/Disable Wake On LAN feature. Options available: Enabled/DIsabled. Default setting is **Enabled**.

# 2-3 Chipset Menu

The Chipset menu display submenu options for configuring the function of North Bridge and South Bridge. Select a submenu item, then press Enter to access the related submenu screen.

Main Advanced <mark>Chi</mark>		Setup Utility Mgmt Boot Save & Exit	
Main Advanced Chi > North Bridge > South Bridge > ME Subsystem	pset Security Server	N - - + f E F F F F F F F F F F F F F F F F F F	<ul> <li>←: Select Screen</li> <li>↓: Select Item Inter: Select /~: Change Opt.</li> <li>1: General Help 3: Previous Values</li> <li>9: Optimized Defaults</li> <li>10: Save &amp; Exit</li> <li>SC: Exit</li> </ul>
	Version 2.15.1236. Cop	oyright (C) 2012 GIGABYTE,	Inc.

## 2-3-1 North Bridge

Chipset	Bios Setup Utility	
<ul> <li>► IOH Configuration</li> <li>► QPI Configuration</li> <li>Compatibility RID</li> </ul>	(Enabled)	▲ IOH Configuration Page
Memory Configuration Total Memory	65536 MB (DDR3)	
Current Memory Mode Current Memory Speed Mirroring Sparing Memory Mode DDR Speed Channel Interleaving Patrol Scrub Demand Scrub Demand Scrub Deta Scrambling Device Tagging Rank Margin Thermal Throttling OLITT Peak BW %	Independent 1066 MHz Supported Supported [Auto] [Auto] [Auto] [Enabled] [Disabled] [Disabled] [Disabled] [Disabled] [Disabled] [CLTT] 50	<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
Version	2.15.1236.Copyright (C) 2012	GIGABYTE, Inc.
Chipset	Bios Setup Utility	
<ul> <li>QPI Configuration</li> <li>Compatibility RID</li> </ul>	[Enabled]	Display DIMM presence and Size information.
Memory Configuration Total Memory	65536 MB (DDR3)	

Current Memory Mode Current Memory Mode Current Memory Speed Mirroring Sparing Memory Mode DDR Speed Channel Interleaving Patrol Scrub Demand Scrub Demand Scrub Deta Scrambling Device Tagging Rank Margin Thermal Throttling OLTT Peak BM % Altitude DIMM Information Independent 1066 MH2 Supported Supported [Auto] [Auto] [Auto] [Enabled] [Disabled] [Disabled] [Disabled] [CLTT] 50 [300 M]

sion 2.15.1236. Copyright (C) 2012 GIGABYTE, Inc

+: Select Screen 14: Select Item Enter: Select 4/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit F00: Save & Exit

## ☞ IOH Configuration

Press [Enter] for configuration of advanced items.

#### ☞ QPI Configuration

Press [Enter] for configuration of advanced items.

#### ∽ Compatibility RID

Enable/Disable Compatibility RID function. Options available: Enabled/Disabled. Default setting is **Enabled**.

#### ∽ Memory Configuration

∽ Total Memory

Displays the total capacity of the installed memory.

#### ∽ Current Memory Mode

Displays the current memory mode. Memory mode can be determined in Memory Mode item.

#### ∽ Current Memory Speed

Displays the current memory speed.

#### ∽ Mirroring/Sparing

Displays the current support memory mode.

#### ∽ Memory Mode

Determine the memory mode.

When set to Indendent mode, all DIMMs are available to the operation system.

When set to Mirroring mode, the motherboard maintains two identical (redundant) copies of all data in memory.

When set to Lockstep mode, the motherboard uses two areas of memory to run the same set of operations in parallel.

When set to Sparing mode, a preset threshold of coorectable errors is used to trigger fail-over. The spare memory is put online and used as active memory in place of the failed memory. Options available: Indpendent /Mirroring/Lockstep/Sparing.

Options available: Indpendent /Mirroring/Locksto

## → DDR Speed

Configure the DDR Speed.

Options available: Auto/Force DDR3 800/Force DDR3 1066/Force DDR3 1333/Force DDR3 1600/Force DDR3 1866. Default setting is **Auto**.

#### ∽ Channel interleaving

Configure DDR Channel Interleaving.

Options available: Auto/1 Way/2 Way/3 Way/4 Way. Default setting is Auto.

#### ∽ Rank interleaving

Configure DDR Rank Interleaving. This improves memory performance by masking the refresh cycles of each memory bank. The Rank Interleaving works between different physical banks. Options available: Auto/1 Way/2 Way/3 Way/4 Way. Default setting is **Auto**.

#### ∽ Patrol Scrub

Enable/Disable Patrol Scrub function.

Options available: Enabled/Disabled. Default setting is **Enabled**.

#### Demand Scrub

Enable/Disable Demand Scrub function. Options available: Enabled/Disabled. Default setting is **Disabled**.

## ∽ Device Tagging

Enable/Disable Device Tagging function. Options available: Enabled/Disabled. Default setting is **Disabled**.

## Rank Margin

Enable/Disable Rank Margin function. Options available: Enabled/Disabled. Default setting is **Disabled**.

## ∽ Thermal Thortting

Configure theThermal Thortting. Options available: Disabled/OLTT/CLTT. Default setting is **CLTT**.

#### → OLTT Peak BW %

Press the numberic keys to increase or decrease the desired values.

#### Altitude

Configure the Altitude value. Options available: Auto/300 M/900 M/1500 M/3000 M. Default setting is **3000 M**.

#### ☞ DIMM Information

Press [Enter] to view the information of installed memory.

# 2-3-1-1 IOH Configuration

Intel(R) VT for Dir I/O Configuration d] 1 d1	ected
d) d)	
000] ++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defau F10: Save & Exit ESC: Exit	
	000000] ++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defau F10: Save & Exit

Chi	Bios Setup Utility pset	
Intel(R) VT-d		Enable/Disable Intel(R) Virtualization Technology for Directed I/O.
		++: Select Screen T4: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
	Version 2.15.1236. Copyright (C) 2012	GIGABYTE, Inc.

- ☞ IOH Configuration
- ∽ Intel(R) VT for Directed I/O Configuration Press [Enter] for configuration of advanced items. ∽ Intel(R) I/OAT (Intel I/O Acceleration Technology) Enable/Disable Intel I/OAT function. Options available: Enabled/Disabled. Default setting is **Disabled**. DCA Support (Direct Cache Access) Enable/Disable Intel DCA Support function. Options available: Enabled/Disabled. Default setting is **Disabled**. ∽ VGA Priority Define the display device priority. ∽ TargetVGA Display the Target VGA support information. Options available: Onboard/Offboard. Default setting is Offboard. → Gen3 Equalization WA's Enable/DIsable the support for Gen3 Equalization Workaround. Options available: Enabled/Disabled. Default setting is **Disabled**. ○ IOH Resource Seletion Type Configure IOH Resource Seletion Type. Options available: Auto/Manual. Default setting is Auto. ∽ No Snoop Optimization Options VC0/VCP/VC1. Default setting is VC1. ∽ MMIOH Size (Memory Mapped Size from IOH) Options available: 1G/2G/4G/8G/16G/32G/64G. Default setting is 64G. MMCFGBASE (Base address of the Memory Mapped Configuration Space) Options available: 0x8000000/0xA0000000/0xC0000000/0x40000000. Default setting is 0x80000000. ∽ Intel(R) VT-d

Enable/Disable Intel VT-d Technology function. Options available: Enabled/Disabled. Default setting is **Enabled**.

# 2-3-1-2 QPI Configuration

Chipset	Bios Setup Utility	
Current QPI Link Speed Current QPI Link Freq Isoc QPI Link Speed Mode QPI Link Frequency Select QPI LinkOp QPI LinkOp QPI Link1	Slow Unknown [Disabled] [Fast] [Auto] [Disabled] [Disabled] [Enabled]	Enbale ∕Disable Isoc
		<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
Version 2.	15.1236. Copyright (C) 2012	GIGABYTE, Inc.

#### ∽ Current QPI Link Speed/ Current QPI Link Freq

Displays the current QPI Link Speed and Frequency information.

#### ∽ lsoc

Enable/Disable Isoc.

Options available: Enabled/Disabled. Default setting is **Disabled**.

#### 

Configure QPI Link Speed mode. Options available: Fast/Slow. Default setting is **Fast**.

## → QPI Link Frequency Select

Configure QPI Link Frequency. Options available: Auto/6.4 GT/s7.2 GT/s/8.6 GT/s. Default setting is **Auto**.

#### ာ QPI Link0s

Options available: Enabled/Disabled. Default setting is **Disabled**.

#### ∽ QPI Link0p

Options available: Enabled/Disabled. Default setting is **Disabled**.

#### CP QPI Link1

Options available: Enabled/Disabled. Default setting is **Enabled**.

## 2-3-1-3 DIMM Information

Chipset	Bios Setup Utility	
CPU Socket 0 DIMM Information DDR3_P0_A0 DDR3_P0_B1 DDR3_P0_B1 DDR3_P0_B1 DDR3_P0_C0 DDR3_P0_C1 DDR3_P0_D0 DDR3_P0_D1	Present 8192 MB Independent Present 8192 MB Independent	<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
Version 2.15.	1236. Copyright (C) 2012 GIGABYTE	, Inc.

- → CPU Socket 0 DIMM Information
- → CPU Socket 0:
- DDR3\_P0\_A0/DDR3\_P0\_A1/DDR3\_P0\_B0/DDR3\_P0\_B1 Status
- DDR3\_P0\_C0/DDR3\_P0\_C1/DDR3\_P0\_D0/DDR3\_P0\_D1 Status

The size of memory installed on each of the DDR3 slots.

# 2-3-2 South Bridge Configuration

Bios Setup Utility Chipset			
PCH Information Name Stepping SB Chipset Configuration PCH Compatibility RID	Patsburg 06 (C1 Stepping) [Disabled]	Support for PCH Compatibility Revision ID (CRID) Functionality.	
Restore AC Power Loss SCU devices	[Last State] [Disabled]		
High Precision Event Timer Configur			
High Precision Timer	[Enabled]	<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>	
Version 2.15.1236. Copyright (C) 2012 GIGABYTE, Inc.			

#### ☞ PCH Information

#### ∽ Name/Stepping Information

Displays the name and stepping information of the south bridge.

## ∽ SB Chipset Configuration

#### ∽ PCH Compatibility RID

Enable/Disable PCH Compatibility RID support.

Options available: Enabled/Disabled. Default setting is Disabled.

## Restore on AC Power Loss (Note)

Defines the power state to resume to after a system shutdown that is due to an interruption in AC power. When set to Last State, the system will return to the active power state prior to shutdown.

When set to Stay Off, the system remains off after power shutdown.

When set to Power On, the system keeps powering on when AC cord is re-plugged.

Options available: Last State/Stay Off/Power On. The default setting depends on the BMC setting.

#### ☞ SCU Devices

Enable/Disable Patsburg SCU device. Options available: Enabled/Disabled. Default setting is **Disabled**.

#### ∽ High Precision Event Timer Configuration

#### ∽ High Precision Event Timer

Enable/Disable High Precision Event Timer. Options available: Enabled/Disabled. Default setting is **Enabled**.

#### (Note) When the power policy is controlled by BMC, please wait for 15-20 seconds for BMC to save the last power state.

# 2-3-3 Intel ME Subsystem

Chips	et	Bios Setup Utility	
Intel ME Subsystem Con ME Subsystem ME BIOS Interface Vers	-	[Enabled] 1.2	ME Subsystem Help
ME Version		2.1.7.129	
ME FW Status Value	:	0xf0345	
ME FW State	:	SPS ME FW Active	
ME FW Operation State	:	MO without UMA	
ME FW Error Code	:	No Error	→+: Select Screen ↑↓: Select Item
ME Ext FW Status Value	:	0x39000301	F+. Select Enter: Select +/−: Change Opt.
BIOS Booting Mode	:	Performance Optmized mode	F1: General Help F3: Previous Values
Cores Disabled	:	0	F3: Previous values F9: Optimized Defaults F10: Save & Exit
ME FW SKU Information	:	SiEn NM	ESC: Exit
End–of–POST Status	:	EOP disabled in POST	
Vi	ersion 2.15.12	36. Copyright (C) 2012 GIGABYTE	, Inc.

∽ ME Subsystem Configuration

#### ∽ ME Subsystem

Enable/Disable ME subsystem configuration. Options available: Enabled/Disabled. Default setting is **Enabled**.

# 2-4 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.

Main Advanced	Bios Setup N Chipset <mark>Security</mark> Server Mgmt N		
Password Descript	Password Description		
then this only li only asked for wh If ONLY the User' is a power on pas	th must be		
Maximum length	20		
			++: Select Screen
Administrator Pas			↑↓: Select Item Enter: Select
User Password			+/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
	Version 2.15.1236. Copyright	(C) 2012 GIGABYTE	, Inc.

There are two types of passwords that you can set:

- Administrator Password
  - Entering this password will allow the user to access and change all settings in the Setup Utility.
- User Password

Entering this password will restrict a user's access to the Setup menus. To enable or disable this field, a Administrator Password must first be set. A user can only access and modify the System Time, System Date, and Set User Password fields.

## ∽ Administrator Password

Press Enter to configure the Administrator password.

∽ User Password

Press Enter to configure the user password.

# 2-5 Server Management Menu



#### ☞ BMC LAN Configuration

BMC LAN Configuration. Press Enter to access the related submenu.

#### ∽ Gbt BMC Function

BMC related function configuration. Press Enter to access the related submenu.

#### ∽ View FRU information

The FRU information submenu is a simple display page for basic system ID information, as well as system product information. Items on this window are non-configurable.

#### ∽ System Event Log

Displays Event Log advanced settings. Press Enter to access the related submenu.

# 2-5-1 BMC LAN Configuration

	Bios Setup Utility Server Mgmt	
Lan Channel 1 Configuration Source IP Address Subnet Mask Default Gateway Address	[Dynamic] 000.000.000.000 000.000.000.000 000.000.000.000	Select to configure LAN channel parameters statically or dynamically(DHCP). Do nothing option will not modify any BMC network parameters during BIOS phase
		<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
Version 2	2.15.1236. Copyright (C) 2012 GI	GABYTE, Inc.

## 그 Lan Channel 1

## ∽ Configuration Source

Select to configure LAN channel parameters statically or dynamically (DHCP). Do nothing option will not modify any BMC network parameters during BIOS phase.

Options available: Static/Dynamic/Do Nothing.

## $\frown$ IP Address

Display IP Address information.

#### Subnet Mask

Display Subnet Mask information.

Please note that the IP address must be in three digitals, for example, 192.168.000.001.

#### ∽ Default Gateway Address

Display Default Gateway Address information.

# 2-5-2 Gbt BMC Function

	Bios Setup Utility Server Mgmt	
Select NCSI and Dedicated LAN		Select to configure LAN channel parameters statically or dynamically(UHCP). Do nothing option will not modify any BMC network parameters during BIOS phase
		★: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.15	.1236. Copyright (C) 2012 GI	GABYTE, Inc.

## ∽ Select NCSI and Dedicated LAN

Switch NCSI and dedicated LAN and send KCS command. Options available: Mode2(NSCI)/ Mode1 (Dedicated). Default setting is **Mode1 (Dedicated)**.

# 2-5-3 View FRU Information

The System Management submenu is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.

Bios Setup Utility Server Mgmt				
FRU Information				
System Manufacturer System Product Name System Version Board Manufacturer Board Version Board Serial Number Chassis Manufacturer Chassis Product Name Chassis Serial Number	61GABYTE GA-6PXSV3 0100 01234567890123456789AB 61GABYTE GA-6PXSV3 00000001 01234567890123456789AB 61GABYTE 00000001 01234567890123456789AB	+: Select Screen 14: Select Item Enter: Select +/-: Change Dpt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit		
Version 2.	.15.1236. Copyright (C) 2012 GIGA	BYTE, Inc.		

# 2-5-4 System Event Log

Enabling/Disabling Options		Change this to enable or
		disable all features of
Erasing Settings		System Event Logging during boot.
Enase SEL	[No]	
When SEL is Full	[Do Nothing]	
Custom EFI Logging Options		
Log EFI Status Codes	[Both]	
NOTE: All values changed here do until computer is restarted		
		↔+: Select Screen
		†↓: Select Item Enter: Select
		+/-: Change Opt.
		F1: General Help
		F3: Previous Values F9: Optimized Defaults
		F10: Save & Exit
		ESC: Exit

#### ☞ Enabling/Disabling Options

#### ∽ SEL Components

Change this to enable or disable all features of System Event Logging during boot. Options available: Enabled/Disabled. Default setting is **Enabled**.

#### ☞ Erasing Settings

#### ☞ Erasing SEL

Choose options for erasing SEL.

Options available: No/Yes, On next reset/Yes, On every reset. Default setting is No.

#### ∽ When SEL is Full

Choose options for reactions to a full SEL.

Options available: Do Nothing/Erase Immediately. Default setting is Do Nothing.

#### ∽ Custom EFI Logging Options

∽ Log EFI Status Codes

Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled/Both/Error code/Progress code. Default setting is **Both**.

# 2-6 Boot Menu

The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

Main Advanced Chipset Security	Bios Setup Utility Server Mgmt Boot Save & Exit	
Boot Configuration Setup Promot Timeout Bootup NumLock State Quiet Boot	1 [On] [Enabled]	Number of seconds to wait for setup activation key. 65535(0xFFF) means indefinite waiting.
Boot Option Priorities Boot Option #1 Boot Option #2 Boot Option #3 Boot Option #4	[UEFI: 512M8 Tiny 1.10] [ 512MB Tiny 1.10] [IBA GE Slot 0900 v1376] [UEFI: Built-in EFI Shell ]	
Network Device BBS Priorities Hard Drive BBS Priorities ▶ CSM16 Parameters ▶ CSM parameters		++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Pptmized Defaults F10: Save & Exit ESC: Exit
Version 2.15.1	236. Copyright (C) 2012 GIGABYTE	, Inc.

#### ∽ Boot Configuration

#### ∽ Setup Prompt Timeout

Number of seconds to wait for setup activation key. 65535(0xFFF) means indefinite waiting." Press the numberic keys to input the desired value.

#### ∽ Bootup NumLock State

Enable or Disable Bootup NumLock function. Options available: On/Off. Default setting is **On**.

#### ా Quiet Boot

Enables or disables showing the logo during POST. Options available: Enabled/Disabled. Default setting is **Enabled**.

### ☞ Boot Priority Order

#### ☞ Boot Option #1/#2/#3/#4

Press Enter to configure the boot priority. By default, the server searches for boot devices in the following secquence:

- 1. UEFI device.
- 2. Hard drive.
- 3. Network device.
- 4. Removable device

## ∽ Network Device BBS Priorities

Press Enter to configure the boot priority.

## ☞ Hard Drive BBS Priorities

Press Enter to configure the boot priority.

## ☞ CSM16 Parameters

Press Enter to configure the CSM16 parameters.

#### ☞ CSM Parameters

Press Enter to configure the CSM parameters.

# 2-6-1 CSM16 Parameters

Bios Setup Utility Boot				
CSM16 Parameters		UPON REQUEST - GA20 can be disabled using BIOS		
CSM16 Module Version BateA20 Active Option ROM Messages INT19 Endless Retry INT19 Trap Response	07.70 [Upon Request] [Force BIOS] [Disabled] [Immediate]	services. ALMAYS - do not allow disabling GA20; this option is useful when any RT code is executed above IMB.		
		<pre>++: Select Screen f4: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>		
Version 2.15.1236. Copyright (C) 2012 GIGABYTE, Inc.				

#### ☞ CSM16 Parameters

## ightarrow CSM16 Module Version

Display CSM Module version information.

#### ∽ Gate20 Active

Upon Request: GA20 can be disabled using BIOS services. Always: Do not allow disabling GA20; this option is useful when any RT code is executed above 1MB. Options available: Upon Request/Always. Default setting is **Upon Request**.

#### ∽ Option ROM Messages

Option ROM Messages.

Options available: Force BIOS/Keep Current. Default setting is Force BIOS.

#### ☞ INT19 Endless Retry

Enabled: Allowed headless retry boot Options available: Enabled/Disabled. Default setting is **Disabled**.

## ☞ INT19 Trap Response

BIOS reaction on INT19 trapping by Option ROM

Immediate: execute the trap right away.

Postpone: execute the trap during legacy boot.

Options available: Immediate/Postpone. Default setting is Immediate.

## 2-6-2 CSM Parameters

	Bios Setup Utility Boot	
Launch CSM Boot option filter Launch FXE OpROM policy Launch Storage OpROM policy Launch Video OpROM policy Other PCI device ROM priority	[Enabled] [UEFI and Legacy] [Legacy only] [Legacy only] [Legacy only] [Legacy OpROM]	This option controls if CSM will be launched
		++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.15.	1236. Copyright (C) 2012 GIGABYT	E, Inc.

#### ∽ CSM parameters

Press Enter to configure the advanced items.

#### ∽ Launch CSM (Compatibility Support Module)

Enable/Disable Compatibility Support Module (CSM) launch.

Options available: Enabled/Disabled. Default setting is Enabled.

The following five items appears and configurable when the Launch CSM is set to Enabled.

If the Launch CSM is set to Disabled, the following five items will not be able to support Legacy mode.

#### ∽ Boot option filter

Determines which devices system will boot to.

Options available: UEFI and Legacy/Legacy only/UEFI only. Default setting is UEFI and Legacy.

#### ∽ Launch PXE OpROM policy

Determines which devices system will boot to.

Options available: Do not launch/UEFI only/Legacy only/Legacy first/UEFI first. Default setting is **Do not launch**.

#### ∽ Launch Storage OpROM policy

Determines which devices system will boot to. Options available: Do not launch/UEFI only/Legacy only/Legacy first/UEFI first. Default setting is Legacy only.

#### Launch Video OpROM policy

Determines which devices system will boot to.

Options available: Do not launch/UEFI only/Legacy only/Legacy first/UEFI first. Default setting is Legacy only.

## ∽ Other PCI device ROM priority

For PCI devices other than Network, Mass storage or Video device, defines which OpROM to launch. Options available: UEFI OpROM/Legacy OpROM. Default setting is **UEFI OpROM.** 

# 2-7 Exit Menu

The Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press **Enter**.

Main Advanced Chipset	Security	Bios Setup Server Mgmt			
Save Changes and Exit Discard Changes and Exit					Exit system setup after saving the changes.
Save Options Save Changes					
Restore Defaults					
Boot Overnide IBA GE Slot 0900 v1376 UEFI: Built-in EFI Shell UEFI: 512MB Tiny 1.10 512MB Tiny 1.10					++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Versi	ion 2.15.1	236. Copyrigh	t (C) :	2012 GIGABYTE	, Inc.

#### ∽ Save Changes and Exit

Saves changes made and close the BIOS setup. Options available: Yes/No.

#### Discard Changes and Exit

Discards changes made and close the BIOS setup. Options available: Yes/No.

#### ∽ Save Options

#### ∽ Save Changes

Saves changes made in the BIOS setup. Options available: Yes/No.

#### ∽ Restore Defaults

Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes/No.

#### ∽ Boot Override

Press Enter to configure the device as the boot-up drive.

## ☞ UEFI: Built-in in EFI Shell

Press <Enter> on this item to Launch EFI Shell from filesystem device.

# Chapter 3 Appendix

# 3-1 Regulatory Statements

## Regulatory Notices

This document must not be copied without our written permission, and the contents there of must not be imparted to a third party nor be used for any unauthorized purpose. Contravention will be prosecuted. We believe that the information contained herein was accurate in all respects at the time of printing. GIGABYTE cannot, however, assume any responsibility for errors or omissions in this text. Also note that the information in this document is subject to change without notice and should not be construed as a commitment by GIGABYTE.

## Our Commitment to Preserving the Environment

In addition to high-efficiency performance, all GIGABYTE motherboards fulfill European Union regulations for RoHS (Restriction of Certain Hazardous Substances in Electrical and Electronic Equipment) and WEEE (Waste Electrical and Electronic Equipment) environmental directives, as well as most major worldwide safety requirements. To prevent releases of harmful substances into the environment and to maximize the use of our natural resources, GIGABYTE provides the following information on how you can responsibly recycle or reuse most of the materials in your "end of life" product.

## Restriction of Hazardous Substances (RoHS) Directive Statement

GIGABYTE products have not intended to add and safe from hazardous substances (Cd, Pb, Hg, Cr+6, PBDE and PBB). The parts and components have been carefully selected to meet RoHS requirement. Moreover, we at GIGABYTE are continuing our efforts to develop products that do not use internationally banned toxic chemicals.

## Waste Electrical & Electronic Equipment (WEEE) Directive Statement

GIGABYTE will fulfill the national laws as interpreted from the 2002/96/EC WEEE (Waste Electrical and Electronic Equipment) directive. The WEEE Directive specifies the treatment, collection, recycling and disposal of electric and electronic devices and their components. Under the Directive, used equipment must be marked, collected separately, and disposed of properly.

## WEEE Symbol Statement



The symbol shown below is on the product or on its packaging, which indicates that this product must not be disposed of with other waste. Instead, the device should be taken to the waste collection centers for activation of the treatment, collection, recycling and disposal procedure. The separate collection and recycling of your waste equipment at the time of disposal will help to conserve natural resources and ensure that it is recycled in a manner that protects human health

and the environment. For more information about where you can drop off your waste equipment for recycling, please contact your local government office, your household waste disposal service or where you purchased the product for details of environmentally safe recycling.

- When your electrical or electronic equipment is no longer useful to you, "take it back" to your local or regional waste collection administration for recycling.
- If you need further assistance in recycling, reusing in your "end of life" product, you may contact us at the Customer Care number listed in your product's user's manual and we will be glad to help you with your effort.

Finally, we suggest that you practice other environmentally friendly actions by understanding and using the energy-saving features of this product (where applicable), recycling the inner and outer packaging (including shipping containers) this product was delivered in, and by disposing of or recycling used batteries properly. With your help, we can reduce the amount of natural resources needed to produce electrical and electronic equipment, minimize the use of landfills for the disposal of "end of life" products, and generally improve our quality of life by ensuring that potentially hazardous substances are not released into the environment and are disposed of properly.