GA-7NCSV-RH Dual Xeon[®] Processor Motherboard

USER'S MANUAL

Xeon[®] Processor Motherboard Rev. 1001



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Product Manual Classification

In order to assist in the use of this product, Gigabyte has categorized the user manual in the following:

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Introduction

Item Checklist

- ☑ The GA7NCSV-RH motherboard
- ☑ IDE (ATA133) cable x 1
- CD for motherboard driver & utility
- ☑ USB cable x 1

- ☑ Serial ATA cable x 6
- ☑ I/O Shield Kit
- SATA Power cable x 3
- ☑ GA-7NCSV-RH Quick Reference Guide



Computer motherboards and expansion cards contain very delicate Integrated Circuit (IC) chips. To protect them against damage from static electricity, you should follow some precautions whenever you work on your computer.

- 1. Unplug your computer when working on the inside.
- 2. Use a grounded wrist strap before handling computer components. If you do not have one, touch both of your hands to a safely grounded object or to a metal object, such as the power supply case.
- Hold components by the edges and try not touch the IC chips, leads or connectors, or other components.
- 4. Place components on a grounded antistatic pad or on the bag that came with the components whenever the components are separated from the system.
- 5. Ensure that the ATX power supply is switched off before you plug in or remove the ATX power connector on the motherboard.

Installing the motherboard to the chassis...

If the motherboard has mounting holes, but they don't line up with the holes on the base and there are no slots to attach the spacers, do not become alarmed you can still attach the spacers to the mounting holes. Just cut the bottom portion of the spacers (the spacer may be a little hard to cut off, so be careful of your hands). In this way you can still attach the motherboard to the base without worrying about short circuits. Sometimes you may need to use the plastic springs to isolate the screw from the motherboard PCB surface, because the circuit wire may be near by the hole. Be careful, don't let the screw contact any printed circuit write or parts on the PCB that are near the fixing hole, otherwise it may damage the board or cause board malfunctioning.

Chapter 1 Introduction

1.1 Features Summary

Form Factor	 12" x 10.5" CEB form factor, 6 layers PCB.
CPU	Supports Dual Intel [®] Xeon [®] processor (Hapertown/Wolfdale)
	Intel Xeon® Quad-Core/Dual -Core processor in LGA 775 socket
	Supports 1066/1333MHz FSB
Chipset	Intel [®] 5100 Chipset
	 Intel[®] ICH9 RAID
Memory	6 x DDR2 DIMM sockets
	 Supports up to 48GB 533/667 memory
	Dual Channel memory bus
	ECC Registered DDR2 533/667
	Supports 1GB, 2GB memory
VGA Controller	Integrated in ServerEngines Pilot II (Matrox G200e)
On-Board LAN	 Intel[®] 82566DM single-port GbE controllers
	 Intel[®] 82573L single-port GbE controllers
	 Single-port 100BASE-TX (for Management)
	I/O Acceleration Technology
	Supports WOL
I/O Control	Integrated in ServerEngines Pilot II
Expansion Slots	 1 PCI slots 32-Bit/33MHz
	 3 PCI-Express x8 slot (at x8 bandwidth)
	 1 PCI-Express x8 slot (at x1 bandwidth)
	 1 PCI-Express x1 slot (at x1 bandwidth)
SATA RAID Controller	 Built in Intel[®] ICH9R with Software RAID 0,1,10, 5
	(Windows Only)
	Optional LSI SW RAID 0/1/10
Internal Connectors	1 24-pin ATX power connector
	1 8-pin ATX power connector
	1 front panel connector
	1 IDE connector
	2 USB 2.0 connectors for additional 4 ports by cables
	1 COM connector
	6 SATA 3Gb/s connectors
	2 CPU fan cable connectors
	 2 System fan cable connectors

Introduction

Rear Panel I/O	1 PS/2 keyboard port
	• 1 PS/2 mouse port
	• 2 USB 2.0 pots
	1 COM port
	• 1 VGA port
	• 2 RJ-45 LAN ports
Hardware Monitor	Enhanced features with CPU Vcore, 1.2V reference,
	VCC3 (3.3V), VBAT3V, +5VSB, CPU Temperature, and
	System Temperature Values viewing
	CPU/Power/System Fan Revolution Detect
	CPU shutdown when overheat
BIOS	Phoenix BIOS on 8Mb flash ROM
Additional Features	PS/2 Mouse wake up from S1 under Windows Operating System
	External Modem wake up
	 Supports S1,S4, S5 under Windows Operating System
	Wake on LAN (WOL)
	Supports Console Redirection
	Supports 4-pin Fan controller
Optional Device	GC- RLE086-RH (SAS 8-port SW RAID)
	 GC- RLE087-RH (SAS 8-port HW RAID)

1.2 GA-7NCSV-RH Motherboard Components

- 1. Primary CPU
- 2. Secondary CPU
- 3. Intel 5100
- 4. Intel 82801IR ICH9 RAID
- 5. ServerEngines SE -SM4210-P01
- 6. ServerEngine memory
- 7. Intel 82566DM GbE
- 8. Intel 82573LGbE
- 9. Broadcom BCM5221 BMC
- 10. IDE cable connector
- 11. Front VGA cable connector**
- 12. Front USB cable connector
- 13. Internal USB cable connector
- 14. SGPIO connector
- 15. IPMB connector
- 16. SATA1 Connector
- 17. SATA2 Connector
- 18. SATA3 Connector
- 19. SATA4 Connector
- 20. SATA5 Connector
- 21. SATA6 Connector
- 22. Power supply H/W status monitor connetor

- 23. 24-pin ATX power connector
- 24. 8-pin ATX power connector
- 25. 32bit/33MHz PCI slot #1
- 26. PCI Express x1 slot #6
- 27. PCI Express x8 slot #5
- 28. PCI Express x8 slot #4
- 29. PCI Express x8 slot #3
- 30. PCI Express x8 slot #1
- 31. Channel 1 DDR2 sockets
- 32. Channel 2 DDR2 sockets
- 33. Keyboard Mouse ports
- 34. USB 2.0 ports & Gigabit LAN port
- 35. Serial port
- 36. Monitor port
- 37. Gigabit LAN port
- 38. Gigabit LAN port
- 39. CPU1 fan cable connector
- 40. CPU2 fan cable connector
- 41. System fan cable connector
- 42. System fan cable connector
- 43. Front panel connector
- 44. CMOS Battery

Introduction



Chapter 2 Hardware Installation Process

2-1: Installing Processor and CPU Haet Sink



Before installing the processor and cooling fan, adhere to the following cautions:

- 1. The processor will overheat without the heatsink and/or fan, resulting in permanent irreparable damage.
- 2. Never force the processor into the socket.
- 3. Apply thermal grease on the processor before placing cooling fan.
- 4. Please make sure the CPU type is supported by the motherboard.
- 5. If you do not match the CPU socket Pin 1 and CPU cut edge well, it may damage the CPU. Please change the insert orientation.

2-1-1: Installing CPU

- Step 1 Raise the metal locking lever on the socket.
- Step 2 Remove the plastic covering on the CPU socket.
- Step 3 Lift the metal cover.
- Step 4 Insert the CPU with the correct orientation. The CPU only fits in one orientation.
- Step 5 Once the CPU is properly placed, please replace the metal cover and push the metal lever back into locked position.



2-1-2: Installing Cooling Fan

- Step 1 Attach the heat sink clip to the processor socket.
- Step 2 Place the cooling fan on the heat sink.
- Step 3 Secure the cooing fan with screws.
- Step 4 Connect processor fan can cable to the processor fan connector



2-2: Install Memory Modules



Before installing the memory modules, please comply with the following conditions: 1. Please make sure that the memory used is supported by the motherboard. It is recommended that memory of similar capacity, specifications and brand be used. Before installing or removing memory modules, please make sure that the computer power is switched off to prevent hardware damage.

3. Memory modules have a foolproof insertion design. A memory module can be installed in only one direction. If you are unable to insert the module, please switch the direction.

The motherboard supports DDRII memory modules, whereby BIOS will automatically detect memory capacity and specifications. Memory modules are designed so that they can be inserted only in one direction. The memory capacity used can differ with each slot.



Installation Steps:

- 1. Unlock a DIMM socket by pressing the retaining clips outwards. Aling a DIMM on the socket such that the notch on the DIMM exactly match the notch in the socket.
- Firmly insert the DIMMinto the socket until the retaining clips snap back in place.
 NOTE!! We recommened you to populate the same device size on each socket and the same DIMM size.
- 4. Reverse the installation steps if you want to remove the DIMM module.



Supported DDR2 DIMM Populations

DIMM1	DIMM2	DIMM3	DIMM4	DIMM5	DIMM6
Х	Х	Х	Х	Single Rank	Single Rank
Х	Х	Single Rank	Single Rank	Single Rank	Single Rank
Single Rank					
Х	Х	Х	Х	Dual Rank	Dual Rank
Х	Х	Dual Rank	Dual Rank	Dual Rank	Dual Rank
Dual Rank					
Х	Х	Dual Rank	Dual Rank	Single Rank	Single Rank
Х	Х	Single Rank	Single Rank	Dual Rank	Dual Rank
Dual Rank	Dual Rank	Single Rank	Single Rank	Single Rank	Single Rank
Dual Rank	Dual Rank	Dual Rank	Dual Rank	Single Rank	Single Rank

2-3: Connect ribbon cables, cabinet wires, and power supply

2-3-1 : I/O Back Panel Introduction



O PS/2 Keyboard and PS/2 Mouse Connector

To install a PS/2 port keyboard and mouse, plug the mouse to the upper port (green) and the keyboard to the lower port (purple).

USB Ports

Before you connect your device(s) into USB connector(s), please make sure your device(s) such as USB keyboard, mouse, scanner, zip, speaker...etc. have a standard USB interface. Also make sure your OS supports USB controller. If your OS does not support USB controller, please contact OS vendor for possible patch or driver updated. For more information please contact your OS or device(s) vendors.

Serial Port

This connector supports 1 standard COM port. Device like mouse and modem etc can be connected to Serial port.

OVGA Port

This connector supports 1 standard VGA port. Monitor can be connected to VGA port.

C LAN Ports

The LAN port provides Internet connection of Gigabit Ethernet with data transfer speeds of 10/100/1000Mbps.

LAN LED Description

LED2 (Green/Yellow)	LED1 (Green)
---------------------	--------------

Name	Color	Condition	Description
LED1	Green	ON	LAN Link / no Access
	Green	BLINK	LAN Access
	-	OFF	Idle
LED2	-	OFF	10Mbps connection
	-	OFF	Port identification with 10 Mbps connection
	Green	ON	100Mbps connection
	Green	BLINK	Port identification with 100Mbps connection
	Yellow	ON	1Gbps connection
	Yellow	BLINK	Port identification with 1Gbps connection

2-4: Connectors Introduction



- 1. ATX2
- 2. ATX1
- 3. IDE1 (IDE cable connector)
- 4. S_ATA 1 (SATA cable connector)
- 5. S_ATA 2 (SATA cable connector)
- 6. S_ATA 3 (SATA cable connector)
- 7. S_ATA 4 (SATA cable connector)
- 8. S_ATA 5 (SATA cable connector)
- 9. S_ATA 6 (SATA cable connector)
- 10. IPMB1
- 11. SGPI01

- 12. F_USB1 (USB cable connector)
- 13. F_USB2 (USB cable connector)
- 14. FAN_CPU1
- 15. FAN_CPU2
- 16. R_FAN1
- 17. R_FAN2
- 18. BAT1 (Battery)
- 19. GP_F1 (Front Panel connector)

1/2) ATX2/1 (24-pin/8-pin ATX power connector)

With the use of the power connector, the power supply can supply enough stable power to all the components on the motherboard. Before connecting the power connector, please make sure that all components and devices are properly installed. Align the power connector with its proper location on the motherboard and connect tightly.

The ATX_12V power connector mainly supplies power to the CPU. If the ATX_12V power connector is not connected, the system will not start.

Caution! Please use a power supply that is able to support the system voltage requirements. It is recommended that a power supply that can withstand high power consumption be used (300W or greater). If a power supply is used that does not provide the required power, the result can lead to an unstable system or a system that is unable to start. If you use a power supply that provides a 24-pin ATX power connector, please remove the small cover on the power connector on the motherboard before plugging in the power cord; otherwise, please do not remove it.



Pin No.	Definition
1	GND
2	GND
3	GND
4	GND
5	P12V_CPU1
6	P12V_CPU1
7	P12V_CPU0
8	P12V_CPU0

Pin No.	Definition	Pin No.	Definition
1	3.3V	13	3.3V
2	3.3V	14	-12V
3	GND	15	GND
4	+5V	16	PS_ON(soft On/Off)
5	GND	17	GND
6	+5V	18	GND
7	GND	19	GND
8	Power Good	20	-5V
9	5V SB(stand by +5V)	21	+5V
10	+12V	22	+5V
11	+12V(Only for 24-pin ATX)	23	+5V (Only for 24-pin ATX)
12	3.3V(Only for 24-pin ATX)	24	GND(Only for 24-pin ATX)

4 8

F

3) IDE1 (IDE Connector)

An IDE device connects to the computer via an IDE connector. One IDE connector can connect to one IDE cable, and the single IDE cable can then connect to two IDE devices (hard drive or optical drive). If you want to connect two IDE devices, please set the jumper on one IDE device as Master and the other as Slave (for information, please refer to the instructions located on the IDE device).

Before attaching the IDE cable, please take note of the foolproof groove in IDE connector.





4/5/6/7/8/9) S_ATA 1~6 (Serial ATA cable connectors)

SATA 3Gb/s can provide up to 300MB/s stransfer rate. Please refer to the BIOS setting for the SATA 3Gb/s and install the proper driver in order to work properly.



SATA1 SATA2

Pin No.	Definition
1	GND
2	ТХР
3	TXN
4	GND
5	RXN
6	RXP
7	GND

10) IPMB1 (IPMB connector)





11) SGPI01

SGPIO is stands for Serial General Purpose Input/Output which is a 4-signal (or 4-wire) bus used between a Host Bus Adapter (HBA) and a backplane. Out of the 4 signals, 3 are driven by the HBA and 1 is driven by the backplane. Typically, the HBA is a storage controller located inside a server, desktop, rack or workstation computer that interfaces with Hard disk drives (HDDs) to store and retrieve data.

8 7



Pin No.	Definition
1	DATAOUT
2	NC
3	DATAOUT
4	GND
5	GND
6	LOAD
7	NC
8	CLOCK

12/13) F_USB1/2 (USB cable connectors)

Be careful with the polarity of the front USB connector. Check the pin assignment carefully while you connect the front USB cable, incorrect connection between the cable and connector will make the device unable to work or even damage it. For optional front USB cable, please contact your local dealer.



	Pin No.	Definition
1 2	1	5V power
	2	5V power
	3	-FUSB4
	4	-FUSB5
	5	+FUSB4
	6	+FUSB5
9 10	7	GND
F USB1	8	GND
-	9	NC
12	10	NC
	Pin No.	Definition
	Pin No. 1	Definition 5V power
	Pin No. 1 2	Definition 5V power GND
	Pin No. 1 2 3	Definition 5V power GND -FUSB2
	Pin No. 1 2 3 4	Definition 5V power GND -FUSB2 NC
9 10	Pin No. 1 2 3 4 5	Definition 5V power GND -FUSB2 NC +FUSB2
9 10	Pin No. 1 2 3 4 5 6	Definition 5V power GND -FUSB2 NC +FUSB2 +FUSB3
9 10 F_USB2	Pin No. 1 2 3 4 5 6 7	Definition 5V power GND -FUSB2 NC +FUSB2 +FUSB3 NC
9 10 F_USB2	Pin No. 1 2 3 4 5 6 7 8	Definition 5V power GND -FUSB2 NC +FUSB2 +FUSB3 NC -FUSB3
9 10 F_USB2	Pin No. 1 2 3 4 5 6 7 8 9	Definition 5V power GND -FUSB2 +FUSB2 +FUSB3 NC -FUSB3 GND
9 10 F_USB2	Pin No. 1 2 3 4 5 6 7 8 9 10	Definition 5V power GND -FUSB2 NC +FUSB3 NC -FUSB3 GND 5V power

14/ 15/ 16/ 17) FAN_CPU1/2/R_FAN1/2 (CPU fan / System fan cable connectors)

The cooler fan power connector supplies a +12V power voltage via a 3-pin/4-pin(CPU_FAN) power connector and possesses a foolproof connection design.

Most coolers are designed with color-coded power connector wires. A red power connector wire indicates a positive connection and requires a +12V power voltage. The black connector wire is the ground wire (GND).

Remember to connect the CPU/system fan cable to the CPU_FAN/SYS_FAN connector to prevent CPU damage or system hanging caused by overheating.



18) Battery

If you want to erase CMOS...

1. Turn OFF the computer and unplug the power cord.

2.Remove the battery, wait for 30 second.

3.Re-install the battery.

4. Plug the power cord and turn ON the computer.





CAUTION

- Danger of explosion if battery is incorrectly replaced.
- Replace only with the same or equivalent type recommended by the manufacturer.
- Dispose of used batteries according to the manufacturer's instructions.

19) F_Panel (2X12 Pins Front Panel connector)

Please connect the power LED, PC speaker, reset switch and power switch of your chassis front panel to the F_PANEL connector according to the pin assignment above.



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	23	24

Pin No.	Signal Name	Description
1.	Power LED +	Power LED Signal anode (+)
2.	5V standby	P5V Stand By Power
3.	Pin reomoved	Pin removed
4.	ID LED+	ID LED Signal anode (+)
5.	Power LED -	Power LED Signal cathode(-)
6.	ID LED -	ID LED Signal cathode(-)
7.	HD status LED+	Hard Disk LED Signal anode (+)
8.	System ready LED+	System Fan Fail LED Signal anode (+)
9.	HD status LED-	Hard Disk LED Signal cathode(-)
10.	System ready LED-	System Fan Fail LED Signal cathode(-)
11.	Power on switch	Power button
12.	LAN1 active LED (-)	LAN1 active LED Signal cathode(-)
13.	GND	Ground
14.	LAN1 active LED (+)	LAN1 active LED Signal anode (+)
15.	Reset switch	Reset button Signal
16.	SMBUS data	SMBusData
17.	GND	Ground
18.	SMBUS clock	SMBusClock
19.	ID LED switch	ID Switch Signal
20.	CASEOPEN	Chassis intrusion Signal
21.	GND	Ground
22.	LAN2 active LED (-)	LAN2 active LED Signal cathode(-)
23.	NMI switch	NMI switch Signal
24.	LAN2 active LED (+)	LAN2 active LED Signal anode (+)



- 1. CLR_CMOS1
- 2. PWR_JP1
- 3. PWR_JP2
- 4. PILOT_DIS
- 5. BMC_SEL1

- 6. SPI_WP1
- 7. RECOVERY1
- 8. PASSWORD1
- 9. System Identify

1) CLR_CMOS1 (Clear CMOS jumper)

You may clear the CMOS data to restore its default values by this jumper.

Default value doesn't include the "Shunter" to prevent from improper use this jumper. To clear CMOS, temporarily short 2-3 pin.



- 1-2 Close: Normal operation. (Default setting)
- 1.... 2-3 Close: Clear CMOS

2) PWR_JP1 (Front panel USB1 power source selection jumper)



- 1 •••• 1-2 close: 5V_AUX
- 1 ••• 2-3 close: 5V

- GA-7NCSV-RH Motherboard
- 3) PWR_JP2 (Front panel USB2 power source selection jumper)



- 1 ••• 1-2 close: 5V_AUX
- 1 ••• 2-3 close: 5V





1 2 close: Enable ServerEngine Pilot II BMC function.
 2-3 close: Disable ServerEnginePilot II BMC function.

5) BMC_SEL1 (BMC firmware area selection jumper)



1 • 1-2 close: BMC firmware area select bank0. 1



6) SPI_WP1 (SPI BIOS write protection Disable/Enable jumper)



- 1 1-2 close: Disable SPI BIOS write protection function.
- ¹ 2-3 close: Enable SPI BIOS write protection function.

7) RECCOVERY1 (BIOS recovery jumper)



1-2 Close: Normal operatio. (Default setting)
 2-3 Close: Enable BIOS Recovery function.

8) PASSWORD1 (BIOS password clear jumper)



1 1-2 Close: Normal operation. (Default setting)

¹ 2-3 Close: Clear Supervisor Password in BIOS setup menu.

9) SATARAID1 (System Identify Enable/Disable jumper)



1 a 1-2 Close: Enable System Identify function. (Default setting)

¹ 2-3 Close: Disable System Identify function.

Chapter 3 BIOS Setup

BIOS (Basic Input and Output System) includes a CMOS SETUP utility which allows user to configure required settings or to activate certain system features.

The CMOS SETUP saves the configuration in the CMOS SRAM of the motherboard.

When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS SRAM.

ENTERINGSETUP

When the power is turned on, press the <F2> button during the BIOS POST (Power-On Self Test) will take you to the CMOS SETUP screen. You can enter the BIOS setup screen by pressing "Ctrl + F1".

CONTROLKEYS

< 1 >	Move to previous item
<↓>	Move to next item
< ← >	Move to the item in the left hand
< > >	Move to the item in the right hand
<esc></esc>	Main Menu - Quit and not save changes into CMOS Status Page Setup Menu and
	Option Page Setup Menu - Exit current page and return to Main Menu
<+/PgUp>	Increase the numeric value or make changes
<-/PgDn>	Decrease the numeric value or make changes
<f1></f1>	General help, only for Status Page Setup Menu and Option Page Setup Menu
<f2></f2>	Reserved
<f3></f3>	Reserved
<f4></f4>	Reserved
<f6></f6>	Reserved
<f7></f7>	Reserved
<f8></f8>	Reserved
<f9></f9>	Load the Optimized Defaults
<f10></f10>	Save all the CMOS changes, only for Main Menu

GETTINGHELP

Main Menu

The on-line description of the highlighted setup function is displayed at the bottom of the screen.

Status Page Setup Menu / Option Page Setup Menu

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <Esc>. Select the **Load Setup Defaults** item in the BIOS Exit Setup menu when somehow the system is not stable as usual. This action makes the system reset to the default settings for stability.

• Main

This setup page includes all the items in standard compatible BIOS.

Advanced

This setup page includes all the items of Phoenix BIOS special enhanced features. (ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

• Security

Change, set, or disable password. It allows you to limit access the system and setup.

Server

Server additional features enabled/disabled setup menus.

Boot

This setup page include all the items of first boot function features.

Exit

There are five options this selection: Exit Saving Changes, Exit Discarding Changes, Load Optimal Defaults, Load Failsafe Defaults, and Discard Changes.

Main

Once you enter Phoenix BIOS Setup Utility, the Main Menu (Figure 1) will appear on the screen. Use arrow keys to select among the items and press <Enter> to accept or enter the sub-menu.

PhoenixBIOS Setup Utility				
Main Advanced	Security TPM St	tate Server Boot Exit		
Suctors Theory	r 37 , 20, 521	Item Specific Help		
System Line: System Date:	[11/15/2007]	<tab>, <shift-tab>, or <enter> selects field.</enter></shift-tab></tab>		
> SATA Port 1 > SATA Port 2 > SATA Port 3	ENone] ENone] ENone]			
> SATA Port 4 > SATA Port 5	[None] [None]			
> SHIH PORT 6 > Advanced Processor Op	unone]			
F1 Help ^o Select For Fxit O Select	Item -/+ Change Menu Enter Select	Values P9 Setup Defaults		

Figure 1: Main

🖙 System Time

The time is calculated based on the 24-hour military time clock. Set the System Time (HH:MM:SS)

🗢 System Time

Set the System Date. Note that the "Day" automatically changed after you set the date. (Weekend: DD: MM: YY) (YY: 1099~2099)

∽ SATA Port 1/2/3/4/5/6

The category identifies the types of Serial SATA hard disk from drive 1 to 6 that has been installed in the computer. System will automatically detect HDD type.

Note that the specifications of your drive must match with the drive table. The hard disk will not work properly if you enter improper information for this category.

Hard drive information should be labled on the outside device casing. Enter the appropriate option based on this information.

► TYPE

1-39: Predefined types.

Users: Set parameters by User.

Auto: Set parameters automatically. (Default setting)

CD-ROM: Use for ATAPI CD-ROM drives or double click [Auto] to set all HDD parameters automatically.

ATAPI Removable: Removable disk drive is installed here.

Multi-Sector Transfer

This field displays the information of Multi-Sector Transfer Mode.

Disabled: The data transfer from and to the device occurs one sector at a time.

Auto: The data transfer from and to the device occurs multiple sectors at a time if the device supports it.

LBA Mode	This field shows if the device type in the specific IDE channel		
	support LBA Mode.		

- ▶ 32-Bit I/O Enable this function to max imize the IDE data transfer rate.
- Transfer Mode This field shows the information of Teansfer Mode.
- Ultra DMA Mode This filed displays the DMA mode of the device in the specific IDE channel.

BIOS Setup

Advanced Processor Options

PhoenixBIOS Setup Utility			
Main			
Advanced Processor Options	1	Item Specific Help	
CPU Speed Processor CPUID: Processor L2 Cache:	2300 MHz 000006FB 4096kB	When enabled, a VMM can utilize the additional hardware virtualization capabilities provided	
Intel(R) Virtualization Technology	(Enabled)	by this technology.	
Thermal Management 2 C1 Enhanced Mode Execute Disable Bit Set Max Ext CPUID = 3	(Enabled) (Enabled) (Enabled) (Disabled)		
Discrete MTRR Allocation Processor Power Management:	Disabled] [Enabled] v		
F1 Help ∿ SelectItem -/• C Esc Exit ◇ SelectMenu Enter S	Change Values Gelect > Sub-Men	F9 Setup Defaults nu F10 Save and Exit	

Figure 1-1: Advanced Processor Options

C Advanced Processor Option

This category includes the information of CPU Speed, Processor ID and Processor L2 Cache. And setup menu for Intel Virtualizational Technology, C1 Enhanced Mode, Execute Disable Bit, Set Max Ext CPUID =3, Discrete MTRR Allocation, Processor Power Management, PECI Interface ,and CPU Cache Control.

Please note that setup menu options will be variable depends on the type of CPU.

Intel (R) Virtualization Technology

Intel(R) Virtualization Technology will allow a platform to run multiple operating systems and applications in independent partitions. With virtualization, one computer system can function as multiple "virtual" systems. With processor and I/O enhancements to Intel's various platforms, Intel Virtualization Technology can improve the performance and robustness of today's software-only virtual machine solutions.

Enabled	Enable Intel Virtualization T	Technology. (Default setting)	
---------	-------------------------------	-------------------------------	--

➤ Disabled Disable this function.

∽ C1 Enhanced Mode

With enabling C1 Enhanced Mode, all loical processors in the physical processor have entered the C1 state, the processor will reduce the core clock frequency to system bus ratio and VID.

••	Enabled	Enabled C1	Enhanced Mode.

Disabled Disables C1 Enhanced Mode. (Default setting)

C Execute Disable Bit

► Enabled	Enable Execute Disable Bit.	(Default setting)
➡ Disabled	Disable this function.	

∽Set Max Ext CPUID = 3

Set MAX CPUID extended function value to 3.

- ► Enabled Enable Set Max Ext CPUID = 3 function.
- ➡ Disabled Disable this function. (Default setting)

C Discrete MTRR Allocation

- ➤ Enabled Enable Discrete MTRR Allocation
- ➡ Disabled Disable this function. (Default setting)

Processor Power Management

Select the Power Management desired:

- ➤ Enabled C states and GV1/GV3 are enabled.
- ▶ C States Only GV1/GV3 are disabled.
- GV1/GV3 Only C states are disabled.
- ➡ Disabled C states and GV1/GV3 are disabled. (Default setting)

BIOS Setup

∽ PECI Interface

The Platform Environmental Control Interface (PECI Interface) is designed specifically to convey system management information from the processor. It is a proprietary single wire bus between the processor and the chipset or other health monitoring device. Data from the Digital Thermal Sensors are processed and stored in a processor register (MSR) which is queried through the Platform Environment Control Interface (PECI).

➡ Enabled Enable PECI Interface. (Default settin	ig)
--	-----

➤ Disabled Disable this function.

CPU Cache Control

~ Hardware Prefetcher

- ▶ Enabled Enable Hardware Prefetcher function. (Default setting)
- ▶ Disabled Disable this function.

C Adjacent Cache Line Prefetch

Enabled	Processor will fetch both cache lines when it requires data that is not
	currently inits cache.
➡ Disabled	Disable this function. (Default setting)

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Advanced

About This Section: Advanced

With this section, allowing user to configure your system for advanced operation. User can set the Memory configuration, PnP configuration, I/O configuration, and Advanced chipset control.

		PhoenixE	IOS Setup Utilit	ty .		
Main	Advanced	Secur i ty	TPM State	Server	Boot	Exit
> Memory > PnP Com > I/O Dev > Advance Boot-ti Reset C NunLock Multipr Option	Configuratio figuration ice Configur d Chipset Co ne Diagnosti onfiguration : ocessor Spec ROM Placemen	ation ntrol c Screen: Data: ification: t	(Enabled) (No) (On) (1.4) (Disabled)	Iten Additio menus f Memory	Specific He onal setup to configure devices.	e1p
F1 Hel Esc Exi	p ^u Selec t ⇔ Selec	t Iten -/+ t Menu Ente	Change Values	i F9 -Menu F10	Setup Defau Saue and Ex	ults cit

Figure 2: Advanced

BIOS Setup

Memory Configuration

PhoenixBIOS Setup Utility				
Advanced				
Memory Con	figuration	Item Specific Help		
System Memory:633kBExtended Memory:1046528kBDIMM Group #1 Status:1024 MBDIMM Group #2 Status:Not InstalledDIMM Group #3 Status:Not InstalledDIMM Group #4 Status:Not InstalledDIMM Group #5 Status:Not InstalledDIMM Group #6 Status:Not Installed_		Clears the memory error status.		
Menory Retest:		EQ Protuce De Cou Har		
Esc Exit \diamond Select M	ten -/+ Change Values enu Enter Select > Sub-Mei	nu F10 Save and Exit		

Figure 2-1: Memory Configuration

∽ System Memory/Extended Memory/DIMM Group #1,2,3,4,5,6 Status

These category is display-only which is determined by POST (Power On Self Test) of the BIOS.

C Memory Reset

→ Yes	Select 'Yes', system will clear the memory error status. Save the
	changes and restart system. After rebooting system, the Memory
	Reset item will set to 'No' automatically.
►No	Disable this function. (Default setting)

PnP Configuration

PhoenixBIOS Setup Utility			
Advanced			
PnP Configuration	Item Specific Help		
<pre>> Enhedded MIC PCI Slot 1 Option ROM: [Enabled] PCI Slot 2 Option ROM: [Enabled] PCI Slot 3 Option ROM: [Enabled] PCI Slot 4 Option ROM: [Enabled] PCI Slot 5 Option ROM: [Enabled] PCI Slot 6 Option ROM: [Enabled] Onboard JMB IDE: [Enabled]</pre>	Additional setup menus to configure embedded LAN controller.		
F1 Help ^u Select Iten -/+ Change Values	F9 Setup Defaults		

Figure 2-2: PnP Configuration

C Embedded NIC

► LAN1 Option ROM Scan

➡ Enabled	Enable onboard LAN1 device and initialize device expansion ROM. (Default setting)
Disabled	Disable this function.
LAN2 Option ROM	Scan
➡ Enabled	Enable onboard LAN2 device and initialize device expansion ROM. (Default setting)
Disabled	Disable this function.

BIOS Setup

∽ PCI Slot 1/2/3/4/5/6 Option ROM

► Enabled	Enable this item to initialize device expansion ROM.
	(Defualt setting)
➡ Disabled	Disable this function.
Onboard JMB IDE	
➡ Enabled	Enable OnboardJMB IDE. (Defualt setting)
➡ Disabled	Disable this function.

I/O Device Configuration

PhoenixBIOS Setup Utility			
Advanced			
I/O Device Configuration		Item Specific Help	
Serial port B: Base I/O address/IRQ: Route Port 80h cycles to Serial ATA: Native Mode Operation: SATA Controller Mode Option: SATA RAID Enable SATA AHCI Enable	(Enabled) (3F8/IRQ 4) (PCI) (Enabled) (Auto) (Compatible) (Disabled) (Disabled)	Configure serial port B using options: [Disabled] No configuration [Enabled] User configuration [Auto] BIOS or OS chooses configuration (OS Controlled) Displayed when controlled by OS	
F1 Help ^∪ SelectItem -/+ Esc Exit ◇ SelectMenu Enter	Change Values Select > Sub-Mer	F9 Setup Defaults nu F10 Save and Exit	

Figure 2-3: I/O Device Configuration

🖙 Serial Port B

This allows users to configure serial prot A by using this option.

Enabled	Enable the conf	iguration (Defa	ult setting)
---------	-----------------	-----------------	--------------

▶ Disabled Disable the configuration.

Base I/O Address/IRQ

- ➡ 3F8/IRQ4 Set IO address to 3F8/IRQ4. (Default setting)
- ▶ 2F8/IRQ3 Set IO address to 2F8/IRQ3.
- ▶ 3E8/IRQ4 Set IO address to 3E8/IRQ4.
- ► 2E8/IRQ3 Set IO address to 2E8/IRQ3.

BIOS Setup

∽Route Port 80h cycles to

Set route port 80h cycles to either PCI or LPC bus.

▶PCI	Set Route Port 80h I/O	cycles to the PCI bus.	(Default setting)
		/	· · · · · · · · · · · · · · · · · · ·

▶LPC Set Route Port 80h I/O cycles to the LPC bus.

C Serial ATA

Enabled	Enables on-board serial ATA function. (Default setting)
Disabled	Disables on-board serial ATA function.

► Native Mode Operation

This option allows user to set the native mode for Serial ATA function.

- ► Auto Auto detected. (Default setting)
- Serial ATA Set Native mode to Serial ATA.

SATA Controller Mode Option

Compatible Mode	SATA and PATA drives are auto-detected and placed in
	Legacy mode. (Default setting)
▶ Enhanced Mode	SATA and PATA drives are auto-detected and placed in
	Native mode.

Note: Pre-Win2000 operating system do not work in Enhanced mode.

SATA RAID Enable

➡Enabled	Enabled SATA RAID function.
Disabled	Disable this function. (Default setting)
SATA AHCI Enable	
➡ Enabled	Set this item to enable SATAAHCI function for WinXP-SP1+IAA driver supports AHCI mode.
Disabled	Disabled this function. (Default setting)

Advanced Chipset Control

PhoenixBIOS Setup Utility				
Advanced				
Advanced Chipset Control	Item Specific Help			
Enable Multimedia Timer (<mark>Tes</mark>)	Enable/Disable Multimedia Timer support.			
 F1 Help [^]⊎ Select Item -/+ Change Values Esc Exit <> Select Menu Enter Select > Sub-Men 	F9 Setup Defaults nu F10 Save and Exit			

Figure 2-4: Advanced Chipset Control

🗢 Enable Multimedia Timer

→

Yes

Enable Multimedia Timer support. (Default setting)

►No Disable this function.

BIOS Setup

	Phoenix	BIOS Setup Utility			
Main Advanced	Security	TPM State	Server	Boot	Exit
PhoenixB Nain Advanced Security > Menory Configuration > PnP Configuration > I/D Device Configuration > Advanced Chipset Control Boot-time Diagnostic Screen: Reset Configuration Data: NunLock: Multiprocessor Specification: Dption ROM Placement		(Enabled) (No) (On) [1.4] (Disabled]	y Server Boot Exit Item Specific Help Additional setup menus to configure Memory devices.		Exit Help re
F1 Help îv Selec	t Iten -/+ t Menu Fut	Change Values	F9 510	Setup Def	aults Fxit

C Boot -time Diagnostic

When this item is enabled, system will shows Diagnostic status when system boot.

- ▶ Enabled Enable Boot-time Diagnostic. (Default setting)
- ► Disabled Disable this function.

CRESET Configuration Data

- ► No Do not make any changes. (Default setting)

~NumLock

This option allows user to select power-on state for NumLock.

- ►On Enable NumLock. (Default setting)
- ▶Off Disable this function.

Multiprocessor Specification

This option allows user to configure the multiprocessor(MP) specification revision level. Some operating system will require 1.1 for compatibility reasons.

▶ 1.4 Support MPS Version 1.4. (Default setting)

▶1.1 Support M PS Version 1.1.

Option ROM Placement

Select option ROM placement feature when system hangs.

Options Disabled, E000 Extension by PFA, Temporary Relocation by PFA, E000 Extension by Size, Temporary Relocation by Size.
 The default setting is disabled.

Security

About This Section: Security

In this section, user can set either supervisor or user passwords, or both for different level of password securities. In addition, user also can set the virus protection for boot sector.



Figure 3: Security

C Set Supervisor Password

You can install and change this options for the setup menus. Type the password up to 6 characters in lengh and press <Enter>. The password typed now will clear any previously entered password from the CMOS memory. You will be asked to confirm the entered password. Type the password again and press <Enter>. You may also press <Esc> to abort the selection and not enter a specified password or press <Enter> key to disable this option.

CSet User Password

You can only enter but do not have the right to change the options of the setup menus. When you select this function, the following message will appear at the center of the screen to assist you in creating a password.

Type the password up to 6 characters in lengh and press <Enter>. The password typed now will clear any previously entered password from the CMOS memory. You will be asked to confirm the entered password. Type the password again and press <Enter>. You may also press <Esc> to abort the selection and not enter a specified password.

CPassword on boot

Password entering will be required when system on boot.

Enabled Requries entering password when system on boot.

➡ Disabled Disable this function. (Default setting)

TPMSupport

TPM, stands for Trusted Platform Module. A Trusted Platform Module provides function for secure generation of cryptographic keys, the ability to limit the use of cryptographic keys, as well as a hardware pseudo-random number generator.

Enabled	Enable TPM Support. (De	efault setting)
---------	-------------------------	-----------------

► Disabled Disable this function.

BIOS Setup

TPM State

					Phoe	nixBIC	IS Setup	Utility			
	- Ma in	n	Adva	nced	Secur	ity	TPH St	tate 📃	Server	Boot	Exit
Γ	Cum		DM 9	tatar	Dicable		Deact in:	tot	Item	Specific	Help
	curi	ent i	rii 3	tate.	DISADIE	1 and	Deaction	iteu			
					No Cha	nge	þ		Change	s TPM Stat	te
	F1	Help	^v	Select	Item	-/•	Change	Values	F9	Setup Def	faults
	Esc	Exit	\diamond	Select	Menu	Enter	Select	> Sub-Me	nu F10	Save and	Exit

Figure 4: TPM State

Current TPM State

▶ Displays the current TPM State status.

🗢 Change TPM State

- ▶ No Change No configuration on TPM State. (Default setting)
- ► Enable & Activate Enable and activate TPM State.
- ► Deactivate & Disable Disable and deactivate TPM State.
- ➡ Clear Clear the TPM stored information on the TPM.

Server

		Phoen ixBI05	Setup Utility			
Main	Advanced	Security	TPM State	Server	Boot	Exit
> System M > Console Post Err After Po	anagement Redirection or Pause: wer Failure:	(Enabled) [Last State]		Item Additi view s featur	Specific onal setup erver mana es.	Help nenu to gment
F1 Help Esc Exit	 ∩ Select ◇ Select 	Item -/+ Menu Enter	Change Values Select > Sub-M	F9 enu F10	Setup Def Save and	aults Exit

Figure 5: Server

BIOS Setup

System Management

Phoe	nixBIOS Setup Utility –	
	S	erver
System Managem	ent	Item Specific Help
BIOS Version:	7NCSU-F2	
GBIA Module Version: BMC Firnware Revision: SDR Revision: BMC Command Line Interface	00.02 00.15 00.06 (<mark>Disabled</mark>)	
> Event Log Configuration		
IP Address Source IP Address IP Subnet Mask Default Gateway	EDHCP3 [000.000.000.000] [000.000.000.000] [000.000.000.000]	
F1 Help ^u SelectItem Esc Exit ↔ SelectMenu	-/+ Change Values Enter Select > Sub-Men	F9 Setup Defaults a F10 Save and Exit

Figure 5-1: System Management

∽ Server Management

This category allows user to view the server management features. Including information of BIOS Version, GBIA Module Version, BMC Firmware Revision, and SDR Revision.

C-BMC Command Line Interface

➡ Enabled Enable BMC Command Line Interface.

Disabled Disable BMC Command Line Interface. (Default setting)

~ Event Log Confuguration

This option contains additional setup menu to configure the Event Log Configuration.

Clear all Event Logs	Clear all Event Logs						
▶Enter	The system event log will be cleared if pressing Enter.						
7-IP Address Source							
► DHCP	Address obtained by BMC running DHCP address.						
► Static	Address obtained by BMC running static address.						

Console Redirection

Pho	enixBIOS Setup Utility	
		Server
Console Redire	ction	Item Specific Help
Con Port Address Baud Rate Console Type Flow Control Continue C.R. after POST:	Disabled [19.2k] IPC ANSI] ICTS/RTS] IOff]	If enabled, it will use a port on the motherboard.
F1 Help ^v Select Item Esc Exit ◇ Select Menu	-/+ Change Values Enter Select > Sub-Me	F9 Setup Defaults nu F10 Save and Exit

Figure 5-2: Console Redirection

COM Port Address

If this option is set to enabled, it will use a port on the motherboard to run console redirection function.

- Serial Port B Use Serial Port B as the COM port address.
- Disabled Disable this function. (Default setting)

C Baud Rate

This option allows user to set the specified baud rate.

▶ Options 300, 1200, 2400, 9600, 19.2K, 38.4K, 57.6K, 115.2K.

Console Type

This option allows user to select the specified terminal type. This is defined by IEEE.

BIOS Setup

▶ Options VT100, VT100 8bit, PC-ANSI 7bit, VT100+, VT-UTF8

CFlow Control

This option provide user to enable the flow control function.

► None	Not supported
--------	---------------

►XON/OFF	Software control.

➤ CTS/RTS Hardware control. (Default setting)

Continue C.R. after POST

This option allows user to enable console redirection after O.S has loaded.

▶On	$\label{eq:endergy} Enable \ console \ redirection \ after \ O.S \ has \ loaded.$

▶Off Disable this function. (Default setting)

Boot

Figure 6: Boot

C Boot Priority Order

This field determines which type of device the system attempt to boot from after **PhoenixBIOS Post** completed. Specifies the boot sequence from the available devices. If the first device is not a bootable device, the system will seek for next available device.

Key used to view or configure devices:

Up and Down arrows select a device.

- <+> and <-> moves the device up or down.
- <f> and <r> specifies the device fixed or removable.
- <x> exclude or include the device to boot.
- <Shift + 1> Enable or disable a device.
- <1-4> Loads default boot secquence.

BIOS Setup

Exit



Figure 7: Exit

About This Section: Exit

Once you have changed all of the set values in the BIOS setup, you should save your changes and exit BIOS setup program. Select "Exit" from the menu bar, to display the following sub-menu.

- Exit Saving Changes
- Exit Discarding Changes
- Load Settup Default
- Discard Changes
- Save Changes

Transformed Series Changes

This option allows user to exit system setup with saving the changes. Press <Enter> on this item to ask for the following confirmation message: Pressing 'Y' to store all the present setting values tha user made in this time into CMOS. Therefore, whenyou boot up your computer next time, the BIOS will

re-configure your system according data in CMOS.

Main	Advanced	Security	Server	Boot	Exit
					Item Specific Help
Exit Di Load Se Discard Save Ch	scarding Cha tup Defaults Changes anges	nges		E. S- C	xit System Setup an ave your changes to MOS.
		Setup (Confirmation	1	
	Save	configuration	n changes ar (No)	nd exit n	ow?
		Dollars	A		

BIOS Setup

C-Exit Discarding Changes

This option allows user to exit system setup without changing any

previous settings values in CMOS. The previous selection remain in effect.

This will exit the Setup Utility and restart your compuetr when selecting this option.

Main f	Idvanced	Security	Server	Boot	Exit
Exit Savin	ng Changes	1			Item Specific Help
Load Setup Discard Cl	p Defaults hanges			E	xit utility without aving Setup data to
Save Chan	jes	Setup Warning			
		Configuration has not been saved! Save before exiting? [<mark>Tes</mark>] [No]			
	-				
					- 257

∽Load Settup Default

This option allows user to load default values for all setup items.

When you press <Enter> on this item, you will get a confirmation dialog box with a message as below:

Main Adua	anced Security	Server	Boot	Exit
Full Caulou (Channen		1	Item Specific Help
Exit Discard Discard Chan Save Changes	ing Changes fontts ges		Lo. for	ad default values r all SETUP items.
	Setup	Confirmation	n	
	Load defaul (<mark>Tes</mark>)	t configurat [No]	ion now?	

C-Discard Changes

This option allows user to load previos values from CMOS for all setup item.

When you press <Enter> on this item, you will get a confirmation dialog box with a message as below:

		PhoenixBI	IS Setup Ut i	lity	WARREN MIL
Main	Advanced	Security	Server	Boot	Exit
					Item Specific Help
Exit Sau Exit Dis Load Set Saue Cha	Exit Saving Changes Exit Discarding Changes Load Setup Defaults Discard Changes Save Changes			Li fi i	oad previous values rom CMOS for all SETUP tems.
	E	Setup (Confirmation	00.0047	-
	Ľ	(Tes)	(No)		
				- 23	
		Space Select	Ent	er Acce	pt

∽ Save Changes

This option allows user to save setup dat ato CMOS.

When you press <Enter> on this item, you will get a confirmation dialog box with a message as below:

		PhoenixBI	OS Setup Ut	ility	
Main	Advanced	Secur i ty	Server	Boot	Exit
Fyit S	auing Changes	a			Item Specific Help
Exit Discarding Chang Exit Discarding C Load Setup Defaul Discard Changes		nges		Sa Ch	ive Setup Data to IOS
	ſ	Setup (Confirmatio	n :	
		Save configu	ration chan [No]	ges now?	
		Space Selec	t En	ter Accep	nt

Press [Yes] to save setup data to CMOS.