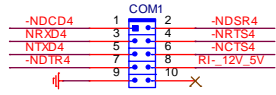


Location	Schematic	Pin Define																																																																																				
1.LLVDS		<table border="1"> <thead> <tr> <th>Pin</th> <th>Signal Name</th> <th>Pin</th> <th>Signal</th> </tr> </thead> <tbody> <tr><td>1</td><td>ODD_Lane3_P</td><td>21</td><td>N/C</td></tr> <tr><td>2</td><td>ODD_Lane3_N</td><td>22</td><td>EDID_3_3V</td></tr> <tr><td>3</td><td>ODD_Lane2_P</td><td>23</td><td>LCD_SDP</td></tr> <tr><td>4</td><td>ODD_Lane2_N</td><td>24</td><td>LCD_GND</td></tr> <tr><td>5</td><td>ODD_Lane1_P</td><td>25</td><td>LCD_SDP</td></tr> <tr><td>6</td><td>ODD_Lane1_N</td><td>26</td><td>ODD_CLK_P</td></tr> <tr><td>7</td><td>ODD_Lane0_P</td><td>27</td><td>ODD_CLK_N</td></tr> <tr><td>8</td><td>ODD_Lane0_N</td><td>28</td><td>BKLT_GND</td></tr> <tr><td>9</td><td>EVEN_Lane3_P</td><td>29</td><td>BKLT_GND</td></tr> <tr><td>10</td><td>EVEN_Lane3_N</td><td>30</td><td>BKLT_GND</td></tr> <tr><td>11</td><td>EVEN_Lane2_P</td><td>31</td><td>EDID_CLK</td></tr> <tr><td>12</td><td>EVEN_Lane2_N</td><td>32</td><td>BKLT_ENABLE</td></tr> <tr><td>13</td><td>EVEN_Lane1_P</td><td>33</td><td>BKLT_PWM_DSM</td></tr> <tr><td>14</td><td>EVEN_Lane1_N</td><td>34</td><td>EVEN_CLK_P</td></tr> <tr><td>15</td><td>EVEN_Lane0_P</td><td>35</td><td>EVEN_CLK_N</td></tr> <tr><td>16</td><td>EVEN_Lane0_N</td><td>36</td><td>BKLT_PWR</td></tr> <tr><td>17</td><td>EDID_GND</td><td>37</td><td>BKLT_PWR</td></tr> <tr><td>18</td><td>LCD_VCC</td><td>38</td><td>BKLT_PWR</td></tr> <tr><td>19</td><td>LCD_VCC</td><td>39</td><td>N/C</td></tr> <tr><td>20</td><td>LCD_VCC</td><td>40</td><td>EDID_DATA</td></tr> </tbody> </table>	Pin	Signal Name	Pin	Signal	1	ODD_Lane3_P	21	N/C	2	ODD_Lane3_N	22	EDID_3_3V	3	ODD_Lane2_P	23	LCD_SDP	4	ODD_Lane2_N	24	LCD_GND	5	ODD_Lane1_P	25	LCD_SDP	6	ODD_Lane1_N	26	ODD_CLK_P	7	ODD_Lane0_P	27	ODD_CLK_N	8	ODD_Lane0_N	28	BKLT_GND	9	EVEN_Lane3_P	29	BKLT_GND	10	EVEN_Lane3_N	30	BKLT_GND	11	EVEN_Lane2_P	31	EDID_CLK	12	EVEN_Lane2_N	32	BKLT_ENABLE	13	EVEN_Lane1_P	33	BKLT_PWM_DSM	14	EVEN_Lane1_N	34	EVEN_CLK_P	15	EVEN_Lane0_P	35	EVEN_CLK_N	16	EVEN_Lane0_N	36	BKLT_PWR	17	EDID_GND	37	BKLT_PWR	18	LCD_VCC	38	BKLT_PWR	19	LCD_VCC	39	N/C	20	LCD_VCC	40	EDID_DATA
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2.SYS_PANEL		<p>PIN1: HD+  PIN2:MPD+  PIN3:HD-  PIN4:MPD-  PIN5:GND  PIN6:PW+  PIN7:RST  PIN8:PW-  PIN9:WF_LED</p>																																																																																				
3.FUSB2_2.FUSB2_3.FUSB2_4		<p>PIN1: VCC  PIN2:USB-  PIN3:USB+  PIN4:GND</p>																																																																																				
4.DMIC		<p>PIN1: VCC  PIN2:DMI DATA  PIN3:GND  PIN4:DMI CLK</p>																																																																																				
5.DISPLAY_BRT		<p>Table 2-4. 8-pin FPD Brightness Header Pin-Out</p> <table border="1"> <thead> <tr> <th>Pin</th> <th>Signal Name</th> <th>Description</th> </tr> </thead> <tbody> <tr><td>1</td><td>BKLT_EN</td><td>Backlight enable</td></tr> <tr><td>2</td><td>BKLT_PWM</td><td>Backlight control</td></tr> <tr><td>3</td><td>BKLT_PWR</td><td>Backlight inverter power</td></tr> <tr><td>4</td><td>BKLT_PWR</td><td>Backlight inverter power</td></tr> <tr><td>5</td><td>BKLT_GND/Brightness_GND</td><td>Ground (shared)</td></tr> <tr><td>6</td><td>BKLT_GND/Brightness_GND</td><td>Ground (shared)</td></tr> <tr><td>7</td><td>Brightness_Up</td><td>Panel brightness increase</td></tr> <tr><td>8</td><td>Brightness_Down</td><td>Panel brightness decrease</td></tr> </tbody> </table>	Pin	Signal Name	Description	1	BKLT_EN	Backlight enable	2	BKLT_PWM	Backlight control	3	BKLT_PWR	Backlight inverter power	4	BKLT_PWR	Backlight inverter power	5	BKLT_GND/Brightness_GND	Ground (shared)	6	BKLT_GND/Brightness_GND	Ground (shared)	7	Brightness_Up	Panel brightness increase	8	Brightness_Down	Panel brightness decrease																																																									
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6.FUSB2_1		<p>PIN1:VCC  PIN2:VCC  PIN3:USB3-  PIN4:USB2-  PIN5:USB3+  PIN6:USB2+  PIN7:GND  PIN8:GND  PIN9:NC  PIN10:NC</p>																																																																																				
7.FP_AUDIO		<p>PIN1:F_MIC_L  PIN2:AGND  PIN3:F_MIC_R  PIN4:GPIO_DET  PIN5:F_LINE_R  PIN6:F_MIC_JD  PIN7:AGND  PIN8:NC  PIN9:F_LINE_L  PIN10:F_LINE_JD</p>																																																																																				
8.SPKR		<p>PIN1:SP_OUTR-  PIN2:SP_OUTR+  PIN3:SP_OUTL+  PIN4:SP_OUTL-</p>																																																																																				
9.BL_SW		<p>PIN1:GPIO6 (BL_DOWN)  PIN1:GPIO1 (BL_UP)</p>																																																																																				

10. COM1



PIN1:-NDCD  
PIN2:-NDSR  
PIN3:-NRXD  
PIN4:-NRYS  
PIN5:-NTXD  
PIN6:-NCTS  
PIN7:-NDTR  
PIN8:-RI  
PIN9:-GND  
PIN10:-NC